

SHEET INDEX	
CONTENTS	SHEET NO.
SHEET INDEX SUPPORTING INFORMATION CIRCUIT NOTES EQUIPMENT NOTES INFORMATION NOTES HIGHEST COMPONENT TABLE	1
FIG. 1 - 5 KHZ PROG CH UNIT RCVR	2

- EQUIPMENT NOTES:**
- DESIGNATIONS SHOWN IN BRACKETS [ ] SHALL BE SHOWN ON EQUIPMENT.
  - ALL CONTACTS SHOWN WITH  $\rightarrow$  ARE PART OF THE PRINTED WIRING BOARD FINGER CONTACTS. (DESIGNATED PART OF P1 FOR IDENTIFICATION ONLY)
  - WIRED FROM PINS 4 AND 5 OF IC1 TO CONNECTING RESISTORS SHALL BE LESS THAN 1/8 INCH IN LENGTH AND PAIRED. A GROUND PLANE CONNECTED TO TP GROUND SHALL BE APPLIED TO BOTH SIDES OF PWB IN AREA OF IC1 AND ALL DIRECTLY CONNECTED COMPONENTS.

- INFORMATION NOTES:**
- UNLESS OTHERWISE SPECIFIED:  
ALL RESISTANCE VALUES ARE IN OHMS.  
ALL CAPACITANCE VALUES ARE IN MICROFARADS.  
ALL VALUES PRECEDED BY THE SYMBOL +(PLUS)  
OR -(MINUS) ARE IN VOLTS.

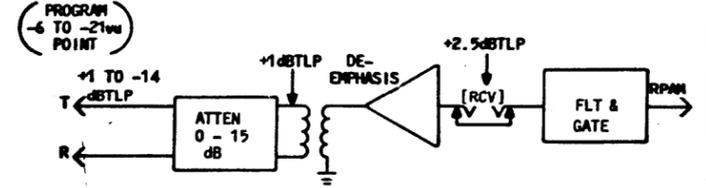
DATE	CD	DATE	ISSUE
1	1	4-8-76	1
20	20	3-15-77	2

- CIRCUIT NOTES:**
- CURRENT DRAIN FOR THIS CIRCUIT IS SHOWN ON SD-3C104-01, THE D3 APPLICATION SCHEMATIC.

FEATURE OR OPTION	PROVIDE		
	FIG.	APP OR HRC	QUANTITY

RECORD OF FIGURE, WIRING AND APPARATUS CHANGES						
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD

- UNLESS OTHERWISE SPECIFIED, ALL RESISTORS ARE KS-20810, L1A.
- FOR INTERCONNECTING INFORMATION, SEE APPLICATION SCHEMATIC FOR THE D3 BANK SD-3C104- ( ).
- TO PREVENT ELECTRICAL INTERFERENCE, THE FOLLOWING INSTALLATION RULES MUST BE ADHERED TO:
  - PGCU-5R MAY BE LOCATED IN CHANNEL POSITIONS 2 THROUGH 11 ONLY.
  - PGCU-5R MAY NOT BE LOCATED EXACTLY FOUR CHANNEL POSITIONS ON EITHER SIDE OF A PGCU-8R.
  - FOR EACH LOCATION SELECTED FOR INSTALLATION OF PGCU-5R, THE CHANNEL UNIT LOCATED TWELVE POSITIONS GREATER MUST BE REMOVED AND A BLANK UNIT INSTALLED IN THE VACATED CHANNEL POSITION.
- THE ATTENUATOR SETTING IS INDICATED BY THE SUM OF THE DIGITS EXPOSED UPON OPERATION OF THE SLIDERS. TO DETERMINE THE REQUIRED SETTING, ADD 6 TO THE VOLUME UNIT (VU) LEVEL DESIRED AT THE T AND P TERMINALS. FOR EXAMPLE: IF THE DESIRED LEVEL IS -6VU THE ATTENUATOR SETTING SHOULD BE 0 dB. (-6VU+6=0). THE OUTPUT RANGE IS -6VU TO -2VU. UNITS ARE TO BE SHIPPED WITH MAXIMUM ATTENUATOR SETTING.



CIRCUIT LEVELS SHOWN IN dB TLP ARE OBTAINED WITH STANDARD D3 CHANNEL ACCESS UNIT (CAU) TESTS. IF STANDARD PROGRAM TESTS ARE USED (APPLICATION OF 0dBm, 1KHZ TEST TONE AT A PROGRAM +8VU POINT) IN A PROPERLY ADJUSTED PROGRAM CIRCUIT THE LEVEL MEASURED AT THE RCV JACK, USING THE CAU WITH EXTERNAL DETECTOR WILL BE -15dBm. ALSO, FOR STANDARD PROGRAM TEST TONE INJECTION AT THE RCV JACK THE CAU WITH EXTERNAL OSCILLATOR SET AT -15dBm SHOULD BE USED.

HIGHEST COMPONENT USED ON THIS DRAWING				
R12	C7	Q1	CR1	IC4
NOT USED				

SUPPORTING INFORMATION	
CATEGORY	NO.
EQUIPMENT DRAWING	J98718CB
X-SPEC	X-78908

**NOTICE-** NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

COMMON SYSTEMS  
24 CHANNEL PCM BANK TYPE D3  
5 KHZ PROGRAM CHANNEL UNIT  
RECEIVER CIRCUIT

BELL TELEPHONE LABORATORIES  
INCORPORATED

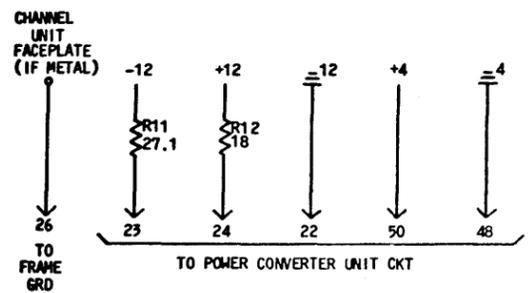
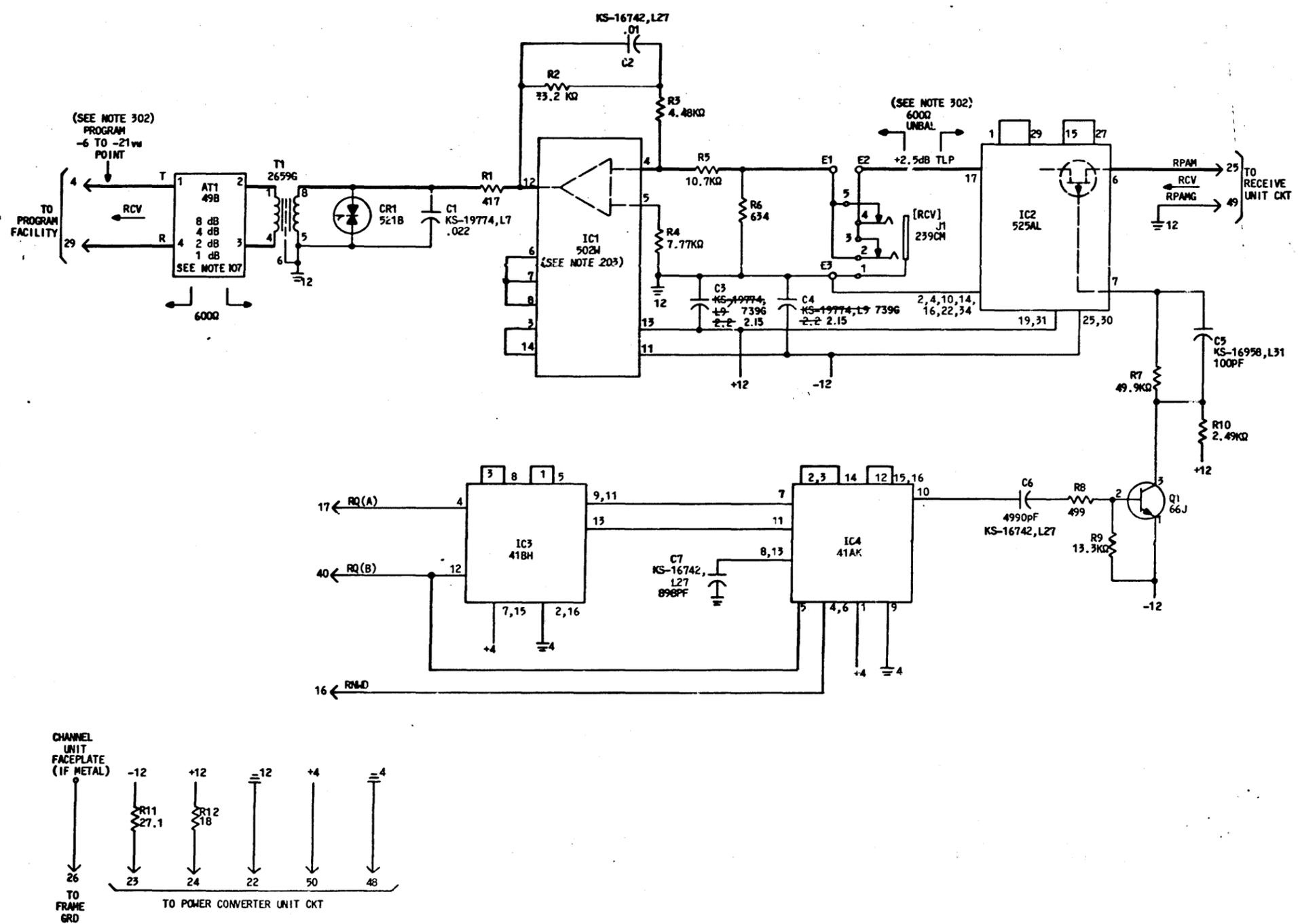
AT&T  
PROVISIONAL

SD-3C270-01-1

2 SHEETS

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FIG. 1  
5 KHZ PROGRAM CHANNEL UNIT RECEIVER



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SD-3C270-01-2

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24 CHANNEL PCM BANK TYPE D3 5 KHZ PROGRAM CHANNEL UNIT RECEIVER CIRCUIT		SD-3C270-01-2
BELL TELEPHONE LABORATORIES INCORPORATED		6S