

REV.	CD	DATE	BY
1	1	4-11-77	MAC

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CIRCUIT NOTES: (CONT)

105. TO PREVENT ELECTRICAL INTERFERENCE, THE FOLLOWING INSTALLATION RULES MUST BE ADHERED TO:
1. PCCU-BT MAY BE LOCATED IN CHANNEL POSITIONS 2 THROUGH 7 ONLY.
 2. PCCU-BT MAY NOT BE LOCATED EXACTLY FOUR CHANNEL POSITIONS ON EITHER SIDE OF A PCCU-5T.
 3. FOR EACH LOCATION SELECTED FOR INSTALLATION OF PCCU-BT, THE CHANNEL UNITS LOCATED EIGHT AND SIXTEEN POSITIONS GREATER MUST BE REMOVED AND BLANK UNITS, INSTALLED IN THE VACATED CHANNEL POSITIONS.
106. THE ATTENUATOR SETTING IS INDICATED BY THE SUM OF THE DIGITS EXPOSED UPON OPERATION OF THE SLIDERS. TO DETERMINE THE REQUIRED SETTING, ADD 6 TO THE VOLUME UNIT (VU) LEVEL EXPECTED AT THE T AND R TERMINALS. FOR EXAMPLE, IF THE EXPECTED LEVEL IS +8VU THE ATTENUATOR SETTING SHOULD BE 14 dB (+8VU + 6 = 14). THE INPUT RANGE IS +9VU TO -6VU. UNITS ARE SHIPPED WITH MAXIMUM ATTENUATOR SETTING.

EQUIPMENT NOTES:

201. DESIGNATIONS SHOWN IN BRACKETS [] SHALL BE SHOWN ON EQUIPMENT.
202. ALL CONTACTS SHOWN WITH $\frac{50}{\text{---}}$ ARE PART OF PRINTED WIRING BOARD FINGER CONTACTS, DESIGNATED P1 FOR IDENTIFICATION ONLY.
203. WIRING FROM 4 AND 5 OF IC1 TO CONNECTING RESISTORS SHALL BE LESS THAN 1 INCH IN LENGTH AND PAIRED. A GROUND PLANE CONNECTED TO 12 GROUND SHALL BE APPLIED TO BOTH SIDES OF P18 IN AREA OF IC1 AND ALL DIRECTLY CONNECTED COMPONENTS.
204. UNLESS OTHERWISE SPECIFIED: ALL RESISTORS ARE RS-208010, L1A.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED: ALL RESISTANCE VALUES ARE IN OHMS. ALL CAPACITANCE VALUES ARE IN MICROFARADS. ALL VALUES PRECEDED BY THE SYMBOLS + (PLUS) OR A - (MINUS) ARE IN VOLTS.
302. CIRCUIT LEVELS SHOWN IN dBTP ARE OBTAINED WITH STANDARD D3 CHANNEL ACCESS UNIT (CAU) TESTS. IF STANDARD PROGRAM TESTS ARE USED (APPLICATION OF 0dBm, 1KHZ TEST TONE AT A PROGRAM +8VU POINT) IN A PROPERLY ADJUSTED PROGRAM CIRCUIT THE LEVEL MEASURED AT THE XMT JACK, USING THE CAU WITH EXTERNAL DETECTOR WILL BE -15dBm. ALSO, FOR STANDARD PROGRAM TEST TONE INJL. "N" AT THE XMT JACK THE CAU WITH EXTERNAL OSCILLATOR SET AT -150dB SHOULD BE USED.

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER

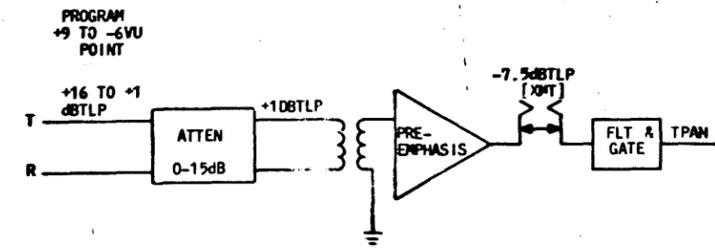
102.

FEATURE OR OPTION	PROVIDE	
	FIG	APP OR HSG QUANTITY

103.

RECORD OF FIGURES, WIRING & APPARATUS CHANGES						
CHANGED ON ISSUE	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NONE	USE IN CIRCUIT		
				STD	A&M	MD

104. FOR CONNECTING INFORMATION SEE APPLICATION SCHEMATIC FOR THE D3 BANK SD-3C104().



HIGHEST COMPONENT USED ON THIS DRAWING				
C13	CR2	Q8	R27	RV2
NOT USED				

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	J98718CC
X-SPEC	X-78908

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

7N22

COMMON SYSTEMS
24 CHANNEL PCM BANK TYPE D3
8KHZ PROGRAM
CHANNEL UNIT TRANSMITTER CIRCUIT

(PCCU-BT)

D3CP

BELL TELEPHONE LABORATORIES
INCORPORATED

AT&TCO
PROVISIONAL

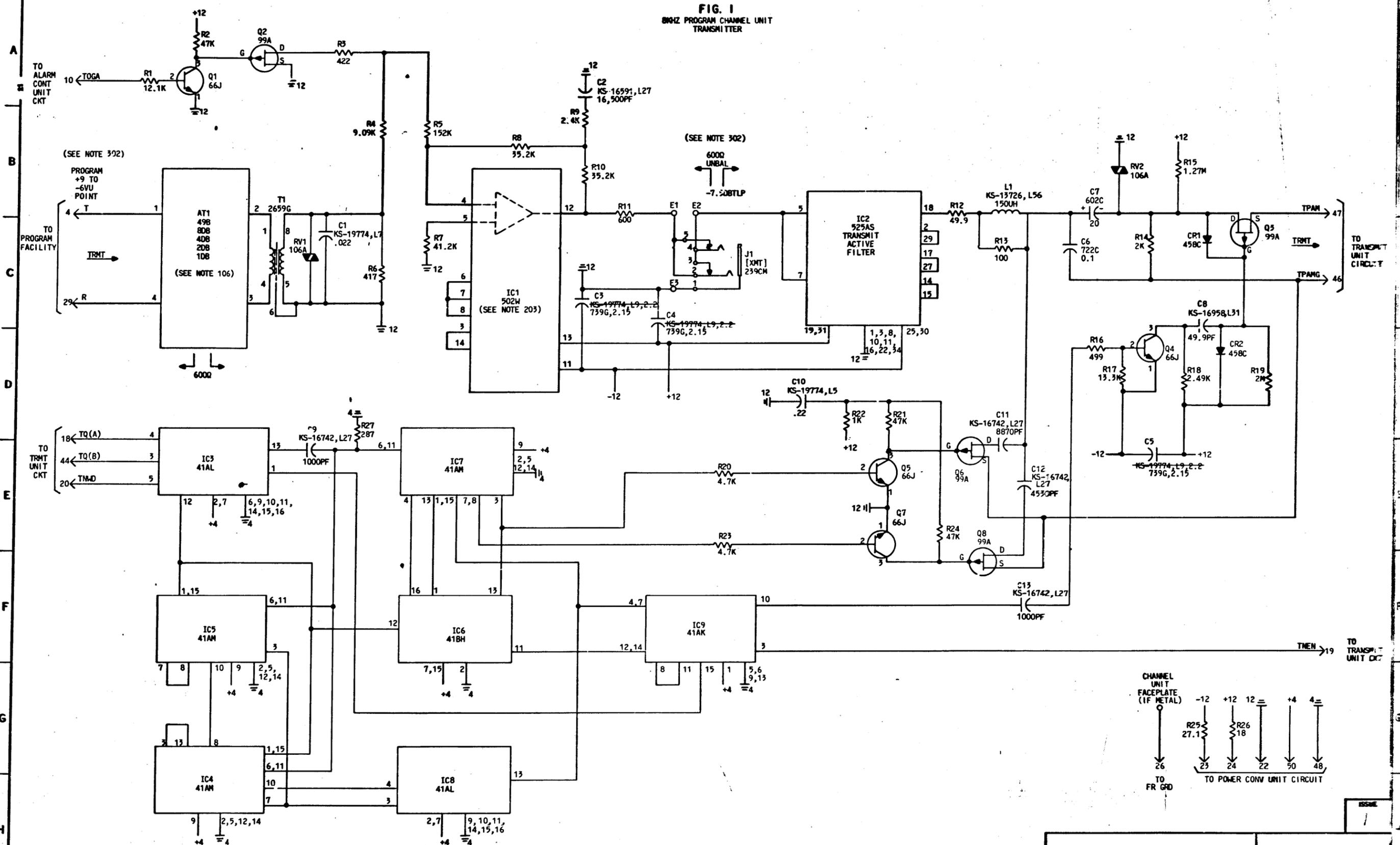
SD-3C271-01-1

2 SHEETS

6S

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FIG. 1
8KHZ PROGRAM CHANNEL UNIT
TRANSMITTER



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