

DRWG. NO.	CD. NO.	DATE ISSUED	BY
1	1	4-1-77	JLB/KP MR

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CIRCUIT NOTES:(CONT)

109. TO PREVENT ELECTRICAL INTERFERENCE, THE FOLLOWING INSTALLATION RULES MUST BE ADHERED TO:
1. PDCU-BR MAY BE LOCATED IN CHANNEL POSITIONS 2 THROUGH 7 ONLY.
 2. PDCU-BR MAY NOT BE LOCATED EXACTLY FOUR CHANNEL POSITIONS ON EITHER SIDE OF A PDCU-SR.
 3. FOR EACH LOCATION SELECTED FOR INSTALLATION OF PDCU-BR, THE CHANNEL UNITS LOCATED EIGHT AND SIXTEEN POSITIONS GREATER MUST BE REMOVED AND BLANK UNITS INSTALLED IN THE VACATED CHANNEL POSITIONS.
106. THE ATTENUATOR SETTING IS INDICATED BY THE SUM OF THE DIGITS EXPOSED UPON OPERATION OF THE SLIDERS. TO DETERMINE THE REQUIRED SETTING, ADD 6 TO THE VOLUME UNIT(VU) LEVEL DESIRED AT THE T AND R TERMINALS. FOR EXAMPLE, IF THE DESIRED LEVEL IS -6VU THE ATTENUATOR SETTING SHOULD BE 0 dB (-6VU+6=0). THE OUTPUT RANGE IS -6VU TO -21VU. UNITS ARE SHIPPED WITH MAXIMUM ATTENUATOR SETTING.

EQUIPMENT NOTES:

201. DESIGNATIONS SHOWN IN BRACKETS [] SHALL BE SHOWN ON EQUIPMENT.
202. ALL CONTACTS SHOWN WITH $\xrightarrow{50}$ ARE PART OF PRINTED WIRING BOARD FINGER CONTACTS, DESIGNATED P1 FOR IDENTIFICATION ONLY.
209. WIRING FROM PINS 4 AND 5 OF IC1 TO CONNECTING RESISTORS SHALL BE LESS THAN 1 INCH IN LENGTH AND PAIRED. A GROUND PLANE CONNECTED TO 12 GROUND SHALL BE APPLIED TO BOTH SIDES OF PWB IN AREA OF IC1 AND ALL DIRECTLY CONNECTED COMPONENTS.
204. UNLESS OTHERWISE SPECIFIED: ALL RESISTORS ARE RS-20810, L1A.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED: ALL RESISTANCE VALUES ARE IN OHMS. ALL CAPACITANCE VALUES ARE IN MICROFARADS. ALL VALUES PRECEDED BY THE SYMBOLS +(PLUS) OR -(MINUS) ARE IN VOLTS.
302. CIRCUIT LEVELS SHOWN IN dBTLF ARE OBTAINED WITH STANDARD D3 CHANNEL ACCESS UNIT(CAU) TESTS. IF STANDARD PROGRAM TESTS ARE USED (APPLICATION OF 0dBm, 1KHZ TEST TONE AT A PROGRAM +8VU POINT) IN A PROPERLY ADJUSTED PROGRAM CIRCUIT THE LEVEL MEASURED AT THE RCV JACK, USING THE CAU WITH EXTERNAL DETECTOR WILL BE -15dBm. ALSO, FOR STANDARD PROGRAM TEST TONE INJECTION AT THE RCV JACK THE CAU WITH EXTERNAL OSCILLATOR SET AT -15DBM SHOULD BE USED.

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER

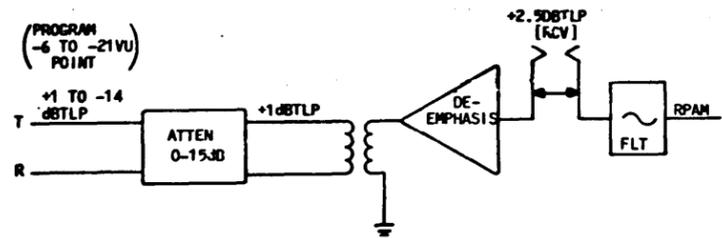
102.

FEATURE OR OPTION	PROVIDE	
	FIG.	QUANTITY

103.

RECORD OF FIGURES, WIRING AND APPARATUS CHANGES						
CHANGE ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD

104. FOR CONNECTING INFORMATION, SEE APPLICATION SCHEMATIC FOR THE D3 BANK SD-3C104-().



HIGHEST COMPONENTS USED ON THE DWG				
C10	CR1	Q1	R14	IC10
NOT USED				

SUPPORTING INFORMATION	
CATEGORY	NO.
EQUIPMENT DRAWING	J98718CD
X-SPEC	X-78908

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT

7N22

COMMON SYSTEMS
24 CHANNEL PCM S.A.R. TYPE D3
8KHZ PROGRAM CHANNEL
UNIT RECEIVER CIRCUIT

DSCP (PDCU-BR)

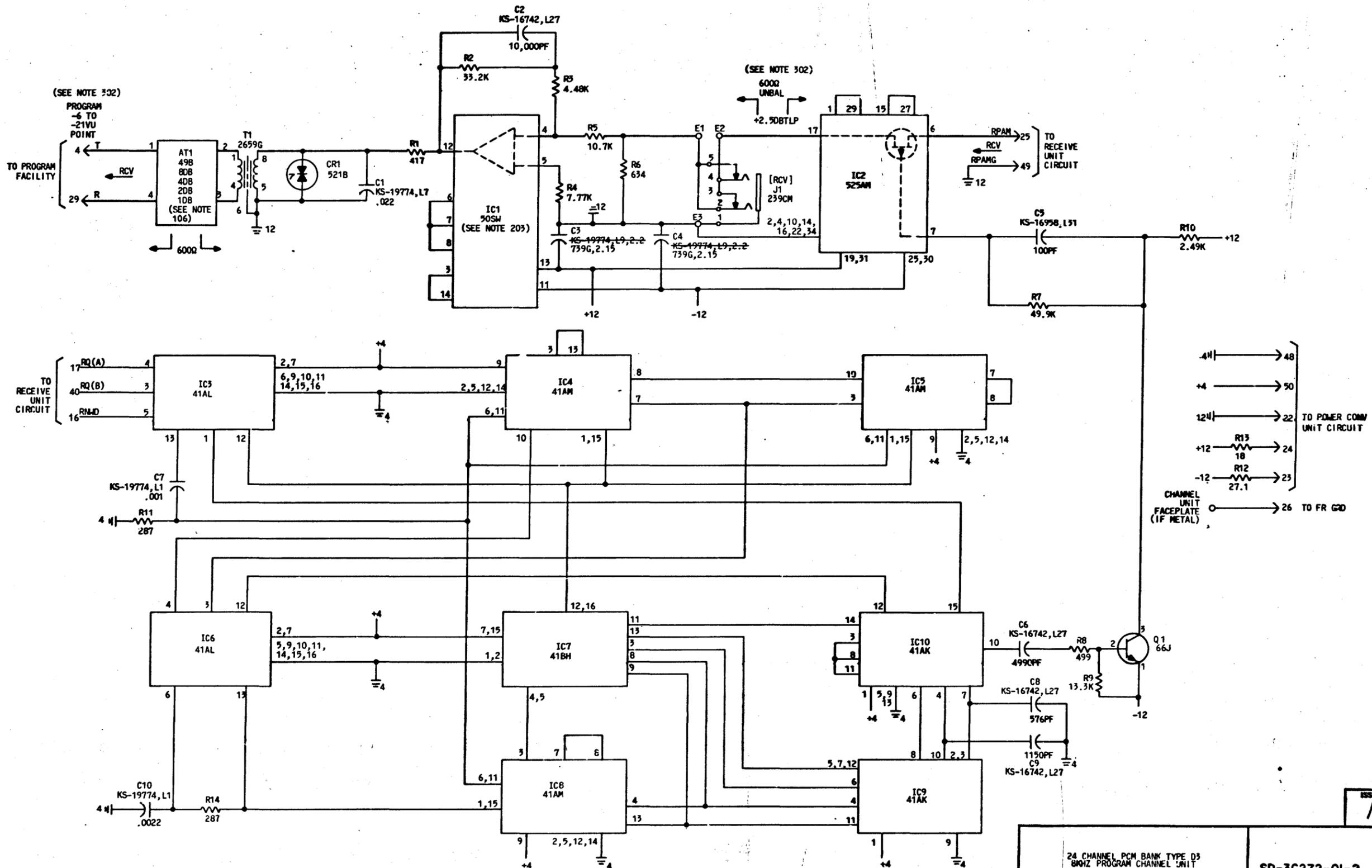
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SD-3C272-01-1
2 SHEETS

FIG. 1
80KHZ PROGRAM CHANNEL UNIT RECEIVER



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24 CHANNEL PCM BANK TYPE D3 80KHZ PROGRAM CHANNEL UNIT RECEIVER CIRCUIT		ISSUE /
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