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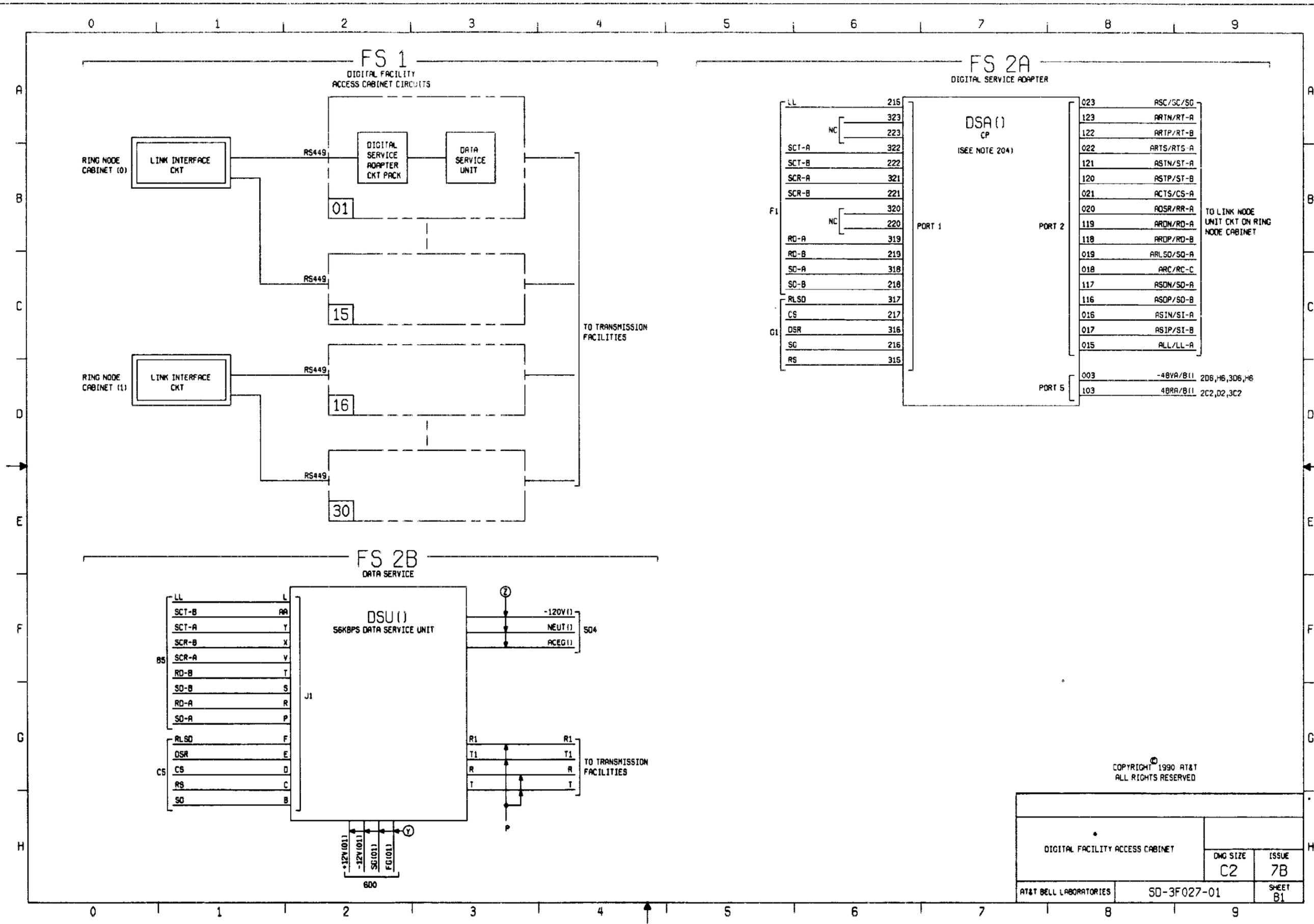
APPARATUS INDEX																							
DESIG	LOCATION			DESIG	LOCATION																		
	FS	APP FIG	EQPT		FS	APP FIG	EQPT																
CIRCUIT PACK						FUSE/FILTER PANEL			LINK NODE UNIT CKT			TRANSMISSION FACILITIES											
DSR 11	1A7	2		4A1, 5A6	5		ACTS/CS-A ADSR/RR-A ALL/LL-A	1B8 1B8 1B8	1C3 1C3 1C3	R R1	104 104	Z Y	DA 7B AY 3B	APP FIG 1,3 1F3,5A2 APP FIG 3 1A2									
56KBPS DATA SERVICE UNIT						LAMP			ARC/RC-C ARDN/RD-A AROP/RD-B ARLSO/SO-A			T T1			V U T S R O P N			DA 3B AY 3B DA 7B AY 7B DA 7B AY 7B DA 7B AY 7B			APP FIG 6 2C2,206, 3B2,306 206,2C2,3B2,306 APP FIG 4 APP FIG 4 APP FIG 6 2C5,305 APP FIG 6 2D5,305 APP FIG 8 6C1 APP FIG 6,8 2H5,3H5, 6C1,6D2		
DSU 11	1F2	3		FA (FRONT) FA (REAR)	4E4 4E4	5 5	ARTN/RT-A ARTP/RT-B ARTS/RTS-A	1B8 1B8 1B8	1C3 1C3 1C3														
COMPONENT ASSEMBLY						OPTO-ISOLATOR			ASC/SC/SG SSON/SO-A ASDP/SO-B														
CP1	SEE APP FIG	7		U1 U2 U3 U4	4E7 4D7 4B7 4A7	7 7 7 7	CP1 CP1 CP1 CP1	1B8 1B8 1B8 1B8	1C3 1C3 1C3 1C3														
CONNECTOR						PLUG/HOLD			ASTN/ST-A ASTP/ST-B														
P1 P2 P3	4A6 4C8 4A8	7 7 7	CP1 CP1 CP1	EFA EFB	- -	1 1																	
DATASET DC POWER CONVERTER						PGA PGB			5A2 5A2			1 1											
PHR 11	5A1	8		RESISTOR			RING NODE CABINET CKT																
DC POWER DIST CKT						R1 R2 R3 R4 R5 R6 R7 R8			4E7 4E6 4D7 4D6 4C7 4B6 4B7 4A6			7 7 7 7 7 7 7 7			CP1 CP1 CP1 CP1 CP1 CP1 CP1 CP1			SCA1N SCA1P 409 409 SCA2N SCA2P 409 409 1E3 1E3					
-48A0 -48B0 RTNA0 RTNB0	2A2 3A2 2A2 3A2			DFA UNIT			DFA0-3			-			4										
FUSE						A01 A03 A05 A07 A09 A11 A13 A15 A17 A19 A21 A23 A25 A27 A29			2A5 2B5 2B5 2C5 2C5 2D5 2E5 2E5 2E5 2F5 2F5 2G5 2G5 2G5 2H5 2H5			6 6 6 6 6 6 6 6 6 6 6 6 6 6 6			B02 B04 B06 B08 B10 B12 B14 B16 B18 B20 B22 B24 B26 B28 B30			3A5 3B5 3B5 3C5 3C5 3D5 3E5 3E5 3E5 3F5 3F5 3G5 3G5 3G5 3H5 3H5 3H5			6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6		

LEAD INDEX															
DESIG	LOCATION			DESIG	LOCATION			DESIG	LOCATION			APP OR WRG	RATED ON ISSUE	REF NOTES	LOCATION
	FS	APP FIG	EQPT		FS	APP FIG	EQPT		FS	APP FIG	EQPT				
ACTS/CS-A	1B8			ARC/RC-C	1B8			1C3							
ADSR/RR-A	1B8			ARDN/RD-A	1B8			1C3							
ALL/LL-A	1B8			AROP/RD-B	1B8			1C3							
				ARLSO/SO-A	1B8			1C3							
				ARTN/RT-A	1B8			1C3							
				ARTP/RT-B	1B8			1C3							
				ARTS/RTS-A	1B8			1C3							
				ASC/SC/SG	1B8			1C3							
				SSON/SO-A	1B8			1C3							
				ASDP/SO-B	1B8			1C3							
				ASTN/ST-A	1B8			1C3							
				ASTP/ST-B	1B8			1C3							

DWG ISS	CD ISS	DWG ISS	CD ISS	DWG ISS	CD ISS
1	1	2B	1	3B	2
4A	2	APP 1A	5B	2	APP 2B
DWG ISS	CD ISS	DATE ISSUED			
6A	2	APP 3A		1-23-90	
7B	2	APP 4B		1-23-90	

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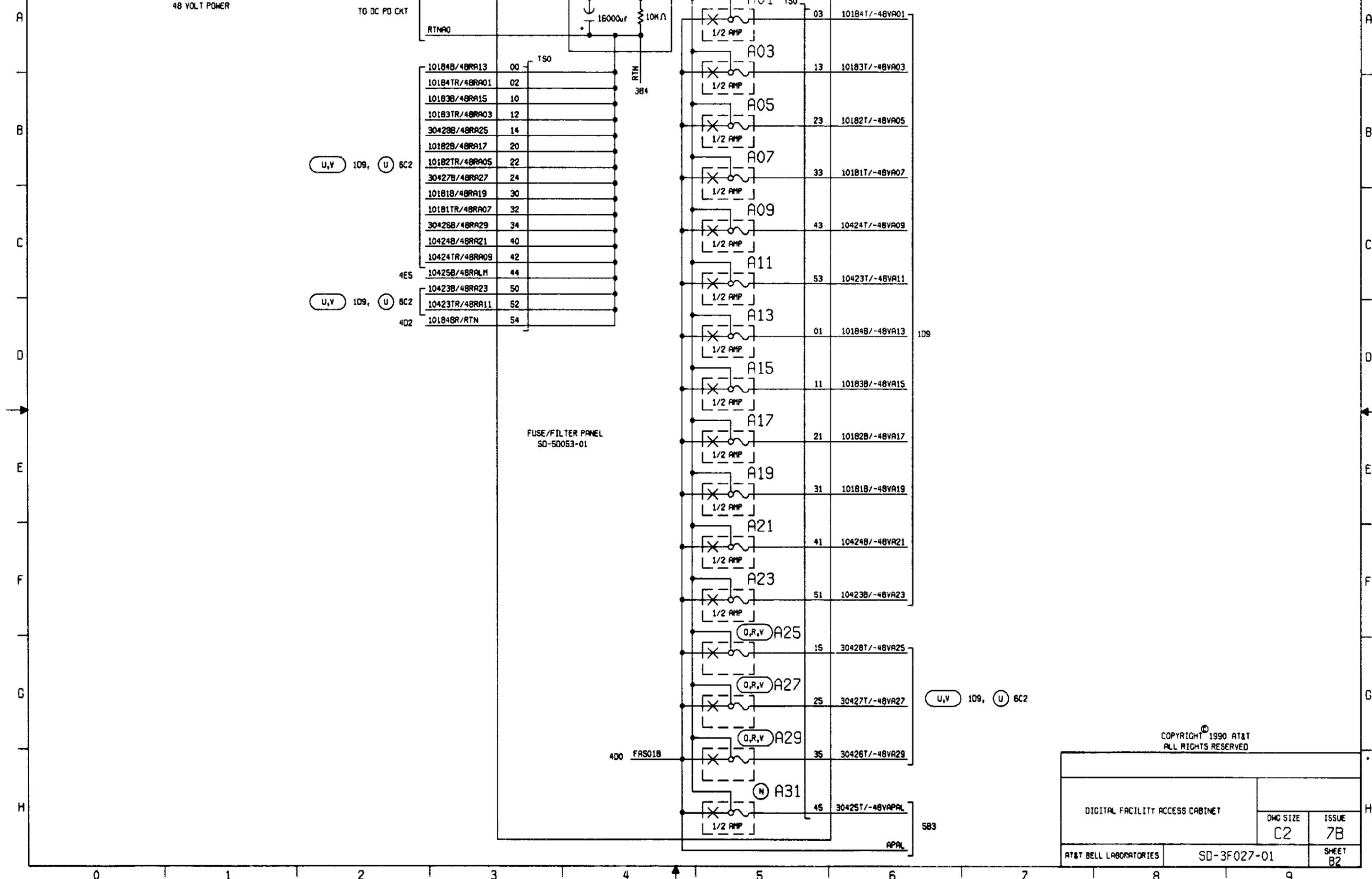
SUPPORTING INFORMATION		SHEET INDEX NOTES	
CATEGORY	NO.		
DFA CABINET	J3F010F	1. ONLY THE LATEST ISSUE, OR ISSUES IF CONCURRENT, ARE SHOWN IN THE INDEX. 2. FOR REISSUES, A CHANGED OR NEW SHEET IS ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1. 3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.	
DSA UNIT	J3F0100A		
DFA UNIT	J3F010FA		
		SIGNALING AND PACKET SWITCHING SYSTEM COMMON NETWORK INTERFACE DIGITAL FACILITY ACCESS CABINET CIRCUIT	
		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES		SD-3F027-01	
		SHEET A1 17 SHEETS	



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DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET B1	

P/O FS 3
48 VOLT POWER



U,V 109, U 6C2

U,V 109, U 6C2

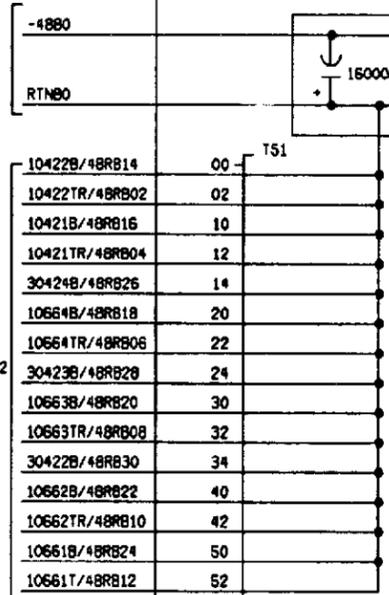
U,V 109, U 6C2

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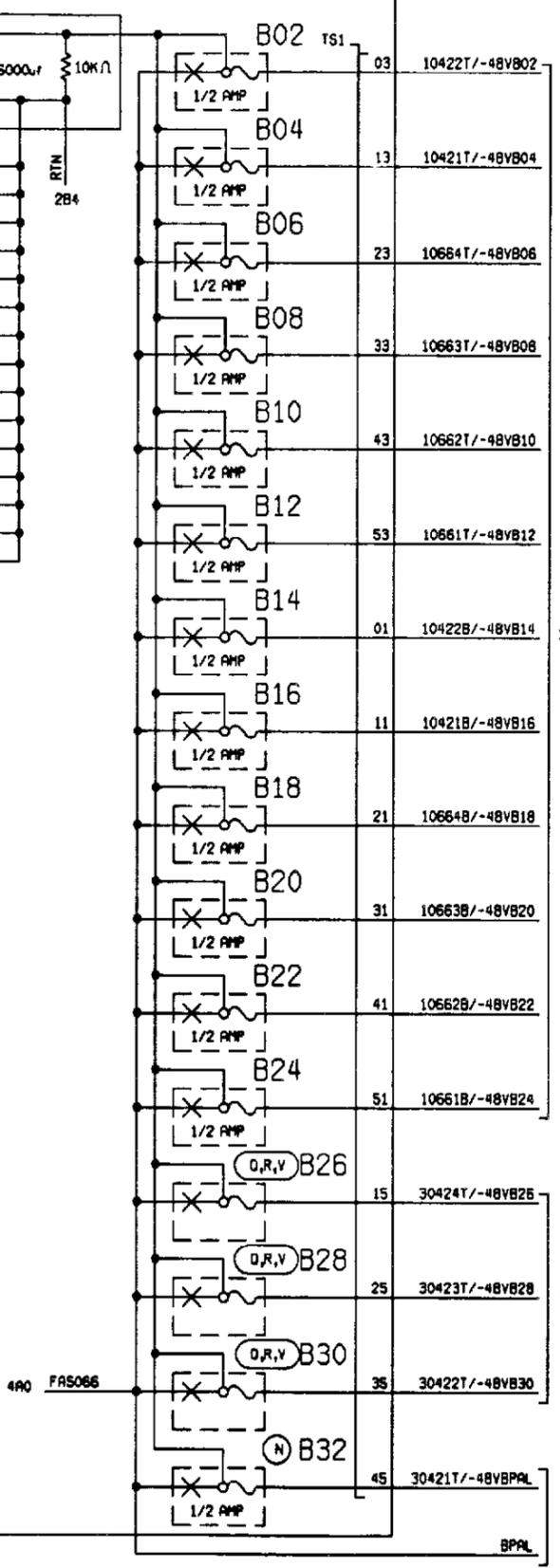
DIGITAL FACILITY ACCESS CABINET		OMC SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET B2	

P/O FS 3
48 VOLT POWER

TO DC PD DKT



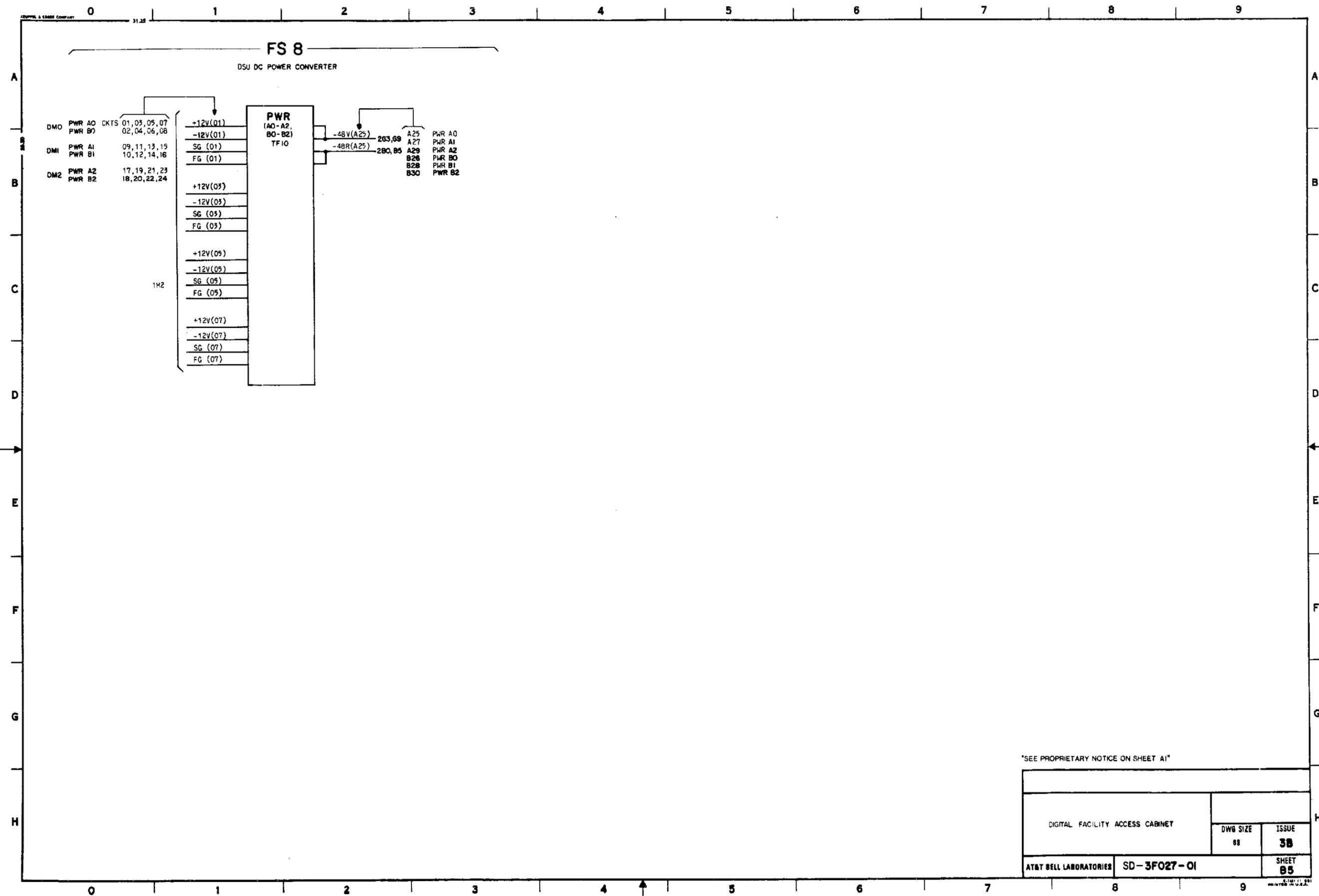
FUSE/FILTER PANEL
SD-50053-01



U,V 109, U 6C2

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DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET B3	



DMO PWR A0 CKTS 01, 03, 05, 07
PWR B0 02, 04, 06, 08

DMI PWR A1 09, 11, 13, 15
PWR B1 10, 12, 14, 16

DM2 PWR A2 17, 19, 21, 23
PWR B2 18, 20, 22, 24

PWR
(A0-A2,
B0-B2)
TF10

+12V(01)
-12V(01)
SG (01)
FG (01)

+12V(03)
-12V(03)
SG (03)
FG (03)

+12V(05)
-12V(05)
SG (05)
FG (05)

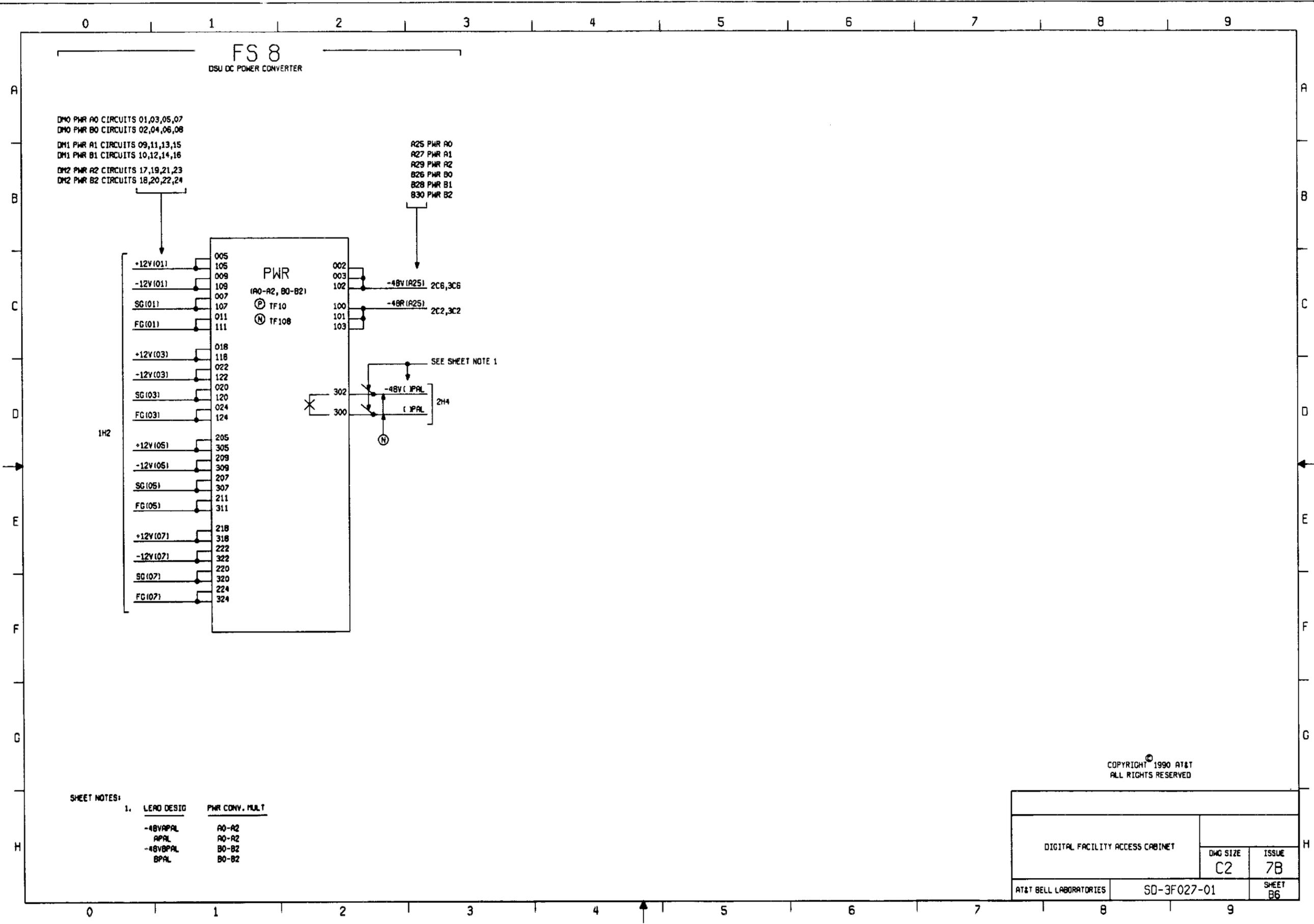
+12V(07)
-12V(07)
SG (07)
FG (07)

-48V(A25) 263, 69 A25
-48R(A25) 280, 85 A27
A29
B26
B28
B30

PWR A0
PWR A1
PWR A2
PWR B0
PWR B1
PWR B2

"SEE PROPRIETARY NOTICE ON SHEET A1"

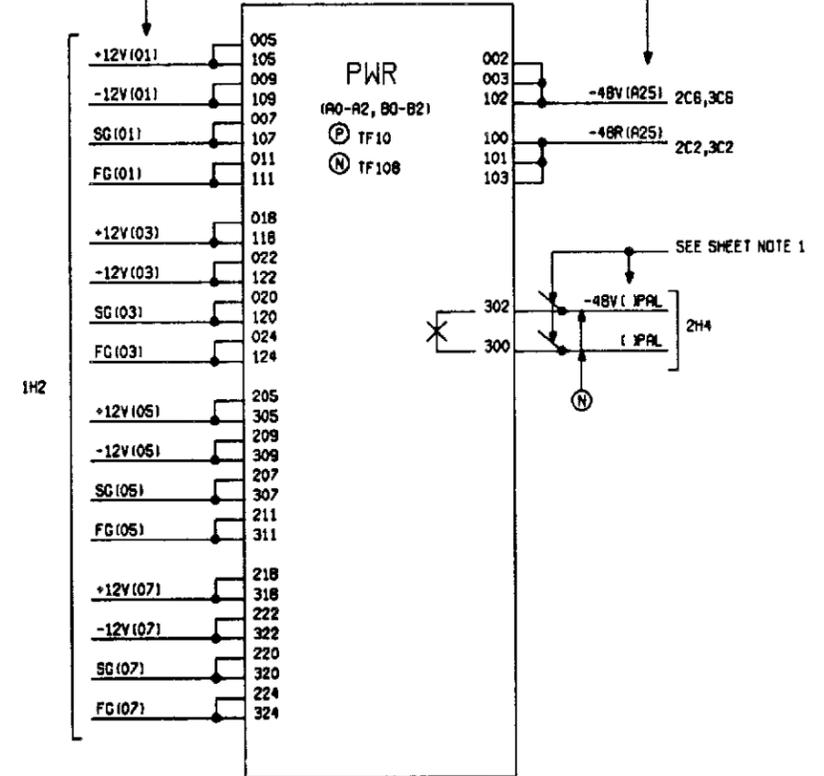
DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		88	38
AT&T BELL LABORATORIES	SD-3F027-01	SHEET 85	



FS 8
DSU DC POWER CONVERTER

DM0 PWR A0 CIRCUITS 01,03,05,07
 DM0 PWR B0 CIRCUITS 02,04,06,08
 DM1 PWR A1 CIRCUITS 09,11,13,15
 DM1 PWR B1 CIRCUITS 10,12,14,16
 DM2 PWR A2 CIRCUITS 17,19,21,23
 DM2 PWR B2 CIRCUITS 18,20,22,24

A25 PWR A0
 A27 PWR A1
 A29 PWR A2
 B26 PWR B0
 B28 PWR B1
 B30 PWR B2



SHEET NOTES:

1.	LEAD DESIG	PWR CONV. MULT
	-48VAPAL	A0-A2
	APAL	A0-A2
	-48VBPAL	B0-B2
	BPAL	B0-B2

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DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET B6	

APP FIG 1

PLUGHOLD			
OPT	DESIG	LOC	CODE
Z	I2 IPCA,B I2 IFA,B	5A2	20GB306 2010A

APP FIG 2

CIRCUIT PACK			
OPT	DESIG	LOC	CODE
	DSA(1)	1A7	TF9

APP FIG 3

56KBPS DATA SERVICE UNIT			
OPT	DESIG	LOC	CODE
Y	DSU(1)	1F2	2556,1,1A
Z	DSU(1)	1F2	2556A,1,1A/2

APP FIG 6

FUSE			
OPT	DESIG	LOC	CODE
	A01	2A5	70G, 1/2 AMP
	A03	2B5	70G, 1/2 AMP
	A05	2B5	70G, 1/2 AMP
	A07	2C5	70G, 1/2 AMP
	A09	2C5	70G, 1/2 AMP
	A11	2D5	70G, 1/2 AMP
	A13	2D5	70G, 1/2 AMP
	A15	2E5	70G, 1/2 AMP
	A17	2E5	70G, 1/2 AMP
	A19	2F5	70G, 1/2 AMP
	A21	2F5	70G, 1/2 AMP
	A23	2G5	70G, 1/2 AMP
	A25	2G5	70G, 1/2 AMP
V	A25	2G5	70G, 1/2 AMP
R	A25	2G5	70G, 1/2 AMP
O	A25	2G5	70G, 1/2 AMP
V	A27	2H5	70G, 1/2 AMP
R	A27	2H5	70G, 1/2 AMP
O	A27	2H5	70G, 1/2 AMP
V	A29	2H5	70G, 1/2 AMP
R	A29	2H5	70G, 1/2 AMP
O	A29	2H5	70G, 1/2 AMP
N	A31	2H5	70G, 1/2 AMP
	B02	3A5	70G, 1/2 AMP
	B04	3B5	70G, 1/2 AMP
	B06	3B5	70G, 1/2 AMP
	B08	3C5	70G, 1/2 AMP
	B10	3C5	70G, 1/2 AMP
	B12	3D5	70G, 1/2 AMP
	B14	3D5	70G, 1/2 AMP
	B16	3E5	70G, 1/2 AMP
	B18	3E5	70G, 1/2 AMP
	B20	3F5	70G, 1/2 AMP
	B22	3F5	70G, 1/2 AMP
	B24	3G5	70G, 1/2 AMP
V	B26	3G5	70G, 1/2 AMP
R	B26	3G5	70G, 1/2 AMP
O	B26	3G5	70G, 1/2 AMP
V	B28	3H5	70G, 1/2 AMP
R	B28	3H5	70G, 1/2 AMP
O	B28	3H5	70G, 1/2 AMP
V	B30	3H5	70G, 1/2 AMP
R	B30	3H5	70G, 1/2 AMP
O	B30	3H5	70G, 1/2 AMP
N	B32	3H5	70G, 1/2 AMP

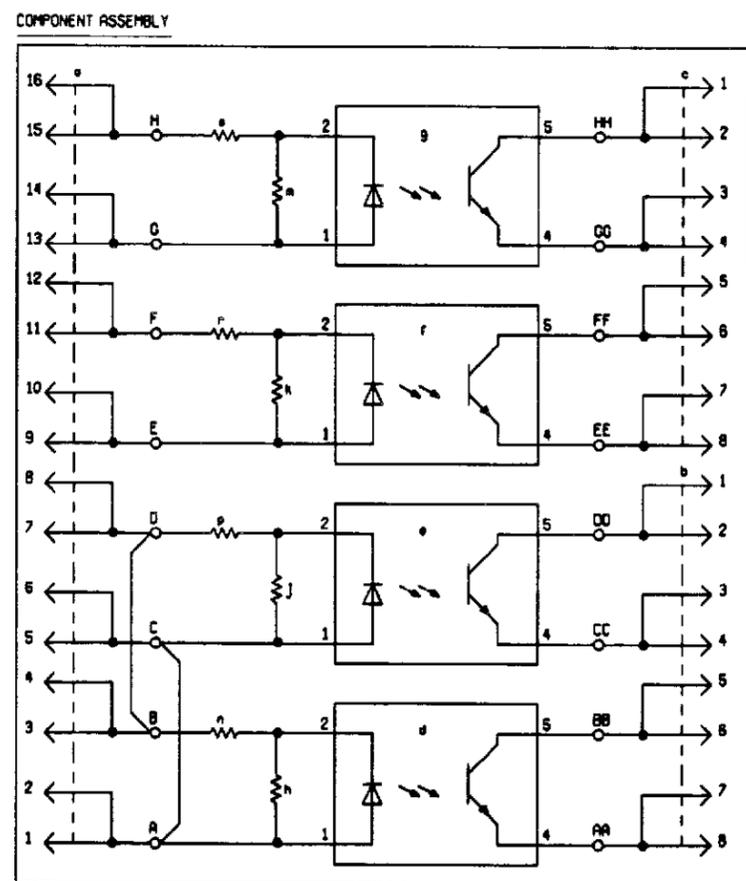
APP FIG 4

DFA UNIT			
OPT	DESIG	LOC	CODE
T	DM(1)	-	Z108A2
S	DM(1)	-	Z108A3

APP FIG 5

LAMP			
OPT	DESIG	LOC	CODE
	FA(FRONT)	4E4	ED-50011-(1)
	FA(REAR)	4E4	ED-50011-(1)

APP FIG 7



DESIG	CP1		
CODE	ED-3F041-1,G3		
OPTION			
COMPONENT	DESIG	LOC	CODE
CONNECTOR	a	P1	4A6 MOLEX 5045 TYPE ORDER NO. 22-04-1161
	b	P2	4C8 MOLEX 5045 TYPE ORDER NO. 22-04-1081
	c	P3	4A8 MOLEX 5045 TYPE ORDER NO. 22-04-1081
OPTO-ISOLATOR	d	U1	4E7
	e	U2	4D7
	f	U3	4B7
	g	U4	4A7
RESISTOR	h	R1	4E7
	i	R2	4E6
	k	R3	4D7
	m	R4	4D6
	n	R5	4C7
	p	R6	4B6
	r	R7	4B7
	s	R8	4A6

APP FIG 8

DATASET POWER CONVERTER			
OPT	DESIG	LOC	CODE
P	PHR(1)	6D1	TF10
N	PHR(1)	6D1	TF10B

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DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET C1	

CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER
A01-A23 ODD	1/2	-48V	1ST-23RD ODD APP FIG 2
A25-A29 ODD	1/2 (OPT V)		25TH-29TH ODD APP FIG 2 (OPT V)
	2 (OPT Y)		1ST-5TH ODD APP FIG 8 (OPT Y)
B02-B24 EVEN	1/2	-48V	2ND-24TH EVEN APP FIG 2
B26-B30 EVEN	1/2 (OPT V)		26TH-30TH EVEN APP FIG 2 (OPT V)
	2 (OPT Y)		2ND-6TH EVEN APP FIG 8 (OPT Y)
BATTERY SYMBOL		VOLTAGE RANGE	

102. I- POWER REQUIREMENTS -120VAC (Z)

- A) MAXIMUM DRAIN PER 2556 DSU
.15 = AMP PER DSU
MAXIMUM OF 30 DSU'S X .15 = 4.5 AMP PER CABINET
2 FEEDERS A&B - MAXIMUM CURRENT/FEEDER = 2.25 AMP
- B) MAXIMUM POWER DISSIPATION
7H X 30 DSU'S = 210W (120V) PER CABINET

II- POWER REQUIREMENTS -48VDC (Z)

- A) MAXIMUM DRAIN PER TF9 CKT PACK - 100mA MAXIMUM
OF 30 TF9'S X 100mA = 3 AMP
2 FEEDERS A&B - MAXIMUM CURRENT/FEEDER = 1.5 AMP
- B) MAXIMUM POWER DISSIPATION
3A X -48V = 144W (48V) PER CABINET

III- TOTAL AC+DC POWER DISSIPATION PER CABINET (Z)

- A) 210W (120V) + 144W (48V) = 344 WATTS PER CABINET

103. MAXIMUM CABLE RUN BETWEEN THE DIGITAL FACILITIES ACCESS CABINET AND THE RING NODE CABINET IS 200 FEET.

104. POWER REQUIREMENTS -48V DC (Y)

- L1 = 3.0A PER FEEDER
L2 = 3.5A PER FEEDER
(FULLY EQUIPPED CABINET)
(24 DSU/OSA CIRCUITS)

EQUIPMENT NOTES:
201. CABLE IDENTIFICATION.

CABLE NO.	EO-3F004-35, GROUP NUMBER
01	
1	17
1	
30	
31	100
32	*
33	
34	19A-19D
40	81
41	82
42	83
43	84
70	
1	75
1	
99	
100	50
101	90FA-90FR
102	82
103	83
104	94
105	95

* = SEE NOTES ON SD-3F002-03, SHEET 83.

202. AT&T 2556 LIA DSU OPTION SWITCH SETTINGS:

THE 2556-SERIES DSU'S ARE SHIPPED WITH THE MOST COMMONLY USED OPTIONS SELECTED. THE DSU HAS ONE 12 POSITION DUAL IN-LINE SWITCH AND FOUR STRAP PLUGS TO SELECT OPTIONS. THE OPTIONS SELECTED MUST BE COMPATIBLE WITH THE 'JOB CONDITIONS'. THESE OPTION SETTINGS ARE LISTED BELOW:

12 POSITION DIP-SWITCH		
POSITION	SETTING	FUNCTION
1	ENABLED	REMOTE DIGITAL LOOPBACK
2	DISABLED	ELASTIC STORE
3	ENABLED	DATA MODE
4	DISABLED	CIRCUIT ASSURANCE
5	DISABLED	SYSTEM STATUS
6	*	TIMING OPTIONS
7	*	TIMING OPTIONS
8	DISABLED	STREAMING
9	ENABLED	CONTINUOUS/SWITCHED RTS
10	ENABLED	SPEED OPTIONS
11	ENABLED	SPEED OPTIONS
12	ENABLED	SPEED OPTIONS

* = SWITCH POSITIONS 6 AND 7 ARE 'JOB CONDITIONS' THAT DETERMINE TIMING AS LISTED IN THE TABLE BELOW:

TIMING	POSITION 6	POSITION 7
DOS	DISABLE	DISABLE
MASTER	DISABLE	ENABLE
SLAVE	ENABLE	DISABLE

IF OPERATING IN THE LDM MODE (COLOCATED STATIONS, METALLIC) THE MASTER DSU MUST BE SET FOR MASTER TIMING, WHILE THE SLAVE DSU IS SET FOR SLAVE TIMING. WHEN OPERATING IN THE COLOCATED CONFIGURATION, THE STP END IS NORMALLY THE MASTER AND THE END-OFFICE IS THE SLAVE. IN DOS OPERATION (VIA CARRIER) BOTH DIP SWITCH POSITION, 6 AND 7 ARE DISABLED.

202. (CONT)

STRAP PLUG OPTIONS

STRAP PLUG	REQUIRED SETTING
SIGNAL/FRAME GROUND	DISABLED
TEST MODE INDICATION	ENABLED
LOCAL LOOPBACK/DTE	ENABLED
FRONT PANEL DISABLE	ENABLED

THE FOUR STRAP PLUGS ARE LOCATED ON THE MAIN CIRCUIT BOARD AND BEHIND THE FRONT PANEL. THESE STRAP PLUGS SHOULD NOT BE CONFUSED WITH THE UPGRADE PIN FIELDS WHICH ARE ADJACENT TO THE DIP-SWITCH (THE UPGRADE PIN FIELDS ARE FACTORY OPTIONS WHICH ARE NOT TO BE REMOVED OR ALTERED).

BY POSITIONING THE UNIT WITH THE CONNECTORS TO THE REAR AND THE DIP SWITCH TO THE FRONT RIGHT, THE STRAPS ARE LOCATED AND STRAPPED AS FOLLOWS: SIGNAL/FRAME GROUND (SG FG) AND TEST MODE INDICATION (TM) ARE REARMOST AND SHOULD HAVE THEIR UNCONNECTED PINS FRONTMOST AND REARMOST RESPECTIVELY, LOCAL LOOPBACK IS IN FRONT OF THE SG AND TM STRAP PLUGS AND IT SHOULD HAVE ITS UNCONNECTED PIN REARMOST, THE FRONT PANEL DISABLE STRAP IS LOCATED ON THE VERTICAL CIRCUIT BOARD BEHIND THE FRONT PANEL ASSEMBLY NEAR THE CENTER OF THE BOARD, IT SHOULD BE STRAPPED SO THE MAJOR AXIS OF THE PLUG IS ORIENTED PARALLEL TO THE PLANE OF THE CIRCUIT BOARD RATHER THAN PERPENDICULAR TO IT.

DETAILED INFORMATION MAY BE FOUND IN 'DATAPHONE II 2500 SERIES DATA SERVICE UNITS USER'S MANUAL', P.10 AND PP.35-88. ISSUE 3 JANUARY 1988, DOCUMENT NO. 999-100-188.

203. ESD GROUNDING POINT

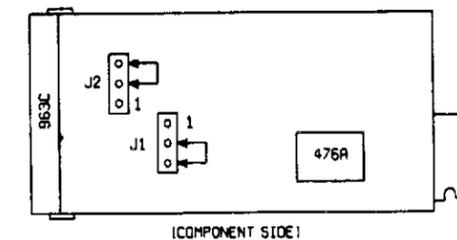
A GROUNDING LUG AND ESD LABEL ARE LOCATED AT THE TOP LEFT OF THE CABINET, BOTH FRONT AND REAR. THIS LUG PROVIDES A CONNECTION POINT FOR WRIST-STRAP GROUNDING BRACELETS WHEN INSERTING OR REMOVING EITHER BOARDS OR CONNECTORS.

204. TF9 OPTION PLUG STRAPPINGS

THE TWO OPTION STRAPS LOCATED ON THE TF9 CIRCUIT BOARD ARE FACTORY SET FOR PROPER OPERATION AND SHOULD NOT BE ALTERED. IN THE EVENT THAT THESE OPTIONS NEED TO BE VERIFIED, THE PROPER CONFIGURATION IS AS FOLLOWS:

- CONNECTOR J1 SHOULD HAVE PINS 2 AND 3 STRAPPED WITH SHORTING PLUG P1
- CONNECTOR J2 SHOULD HAVE PINS 2 AND 3 STRAPPED WITH SHORTING PLUG P2

REFER TO FIGURE BELOW (TF9 CIRCUIT PACK):



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DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET 01	

INFORMATION NOTES: (CONT)
 305. THIS TABLE DESIGNATES WHICH FUSE (VIEWED FROM FRONT) THAT IS WIRED TO ITS ASSOCIATED TERMINAL BLOCK, TERMINAL NUMBER AND RESPECTIVE TSO AND TS1 AND TERMINAL NUMBER. THESE TERMINAL NUMBERS ON TSO AND TS1 MUST BE USED FOR ANY CIRCUIT TO BE FUSED.

FUSE/FILTER PANEL				CABINET													
TERM BLOCK		FUSE TERM NO.	TERM STRIP	TERM NO.		-48V LEAD NAME	FUSE NO.		FUSE DESIG	UNIT TO BE FUSED	UNIT LOC	FUSE AMP	TYPE FUSE	LEAD NAME			
LEVEL	EDL			-48V	RET		LEFT UNIT	RIGHT UNIT						-48V	RET		
03T,B	03-042	1T	TS1	45	44		8T		B32	PWR ALM		.5	70G	BPAL			
		2T		35	34		7T		B30	PWRB2	37-74			B30	RB30		
		3T		25	24		6T		B28	PWRB1	49-74	*	*	B28	RB28		
		4T		15	14		5T		B26	PWRB0	60-74			B26	RB26		
		5T	45	44		4T		A31	PWR ALM			.5	70G	APAL			
		6T	35	34		3T		A29	PWRB2	37-14			A29	RA29			
		7T	25	24		2T		A27	PWRB1	49-14		*	*	A27	RA27		
		8T	15	14		1T		A25	PWRB0	60-14			A25	RA25			
01T	01-018	4T	TS0	03	02		1T		A01	DSA01	60-22			A01	RA01		
		3T		13	12		2T		A03	DSA03	60-38			A03	RA03		
		2T		23	22		3T		A05	DSA05	60-82			A05	RA05		
		1T		33	32		4T		A07	DSA07	60-98			A07	RA07		
	4T	43	42		5T		A09	DSA09	49-22			A09	RA09				
	3T	53	52		6T		A11	DSA11	49-38			A11	RA11				
	2T	03	02		7T		B02	DSA02	60-30			B02	RB02				
	1T	13	12		8T		B04	DSA04	60-46			B04	RB04				
	4T	23	22		9T		B06	DSA06	60-90		.5	70G	B06	RB06			
	3T	33	32		10T		B08	DSA08	60-106			B08	RB08				
	2T	43	42		11T		B10	DSA10	49-30			B10	RB10				
	1T	53	52		12T		B12	DSA12	49-46			B12	RB12				
01B	01-018	4B	TS0	01	00		1B		A13	DSA13	49-82			A13	RA13		
		3B		11	10		2B		A15	DSA15	49-98			A15	RA15		
		2B		21	20		3B		A17	DSA17	37-22			A17	RA17		
		1B		31	30		4B		A19	DSA19	37-38			A19	RA19		
	4B	41	40		5B		A21	DSA21	37-82			A21	RA21				
	3B	51	50		6B		A23	DSA23	37-98			A23	RA23				
	2B	01	00		7B		B14	DSA14	49-90			B14	RB14				
	1B	11	10		8B		B16	DSA16	49-106			B16	RB16				
	4B	21	20		9B		B18	DSA18	37-30			B18	RB18				
	3B	31	30		10B		B20	DSA20	37-46			B20	RB20				
	2B	41	40		11B		B22	DSA22	37-90			B22	RB22				
	1B	51	50		12B		B24	DSA24	37-106			B24	RB24				

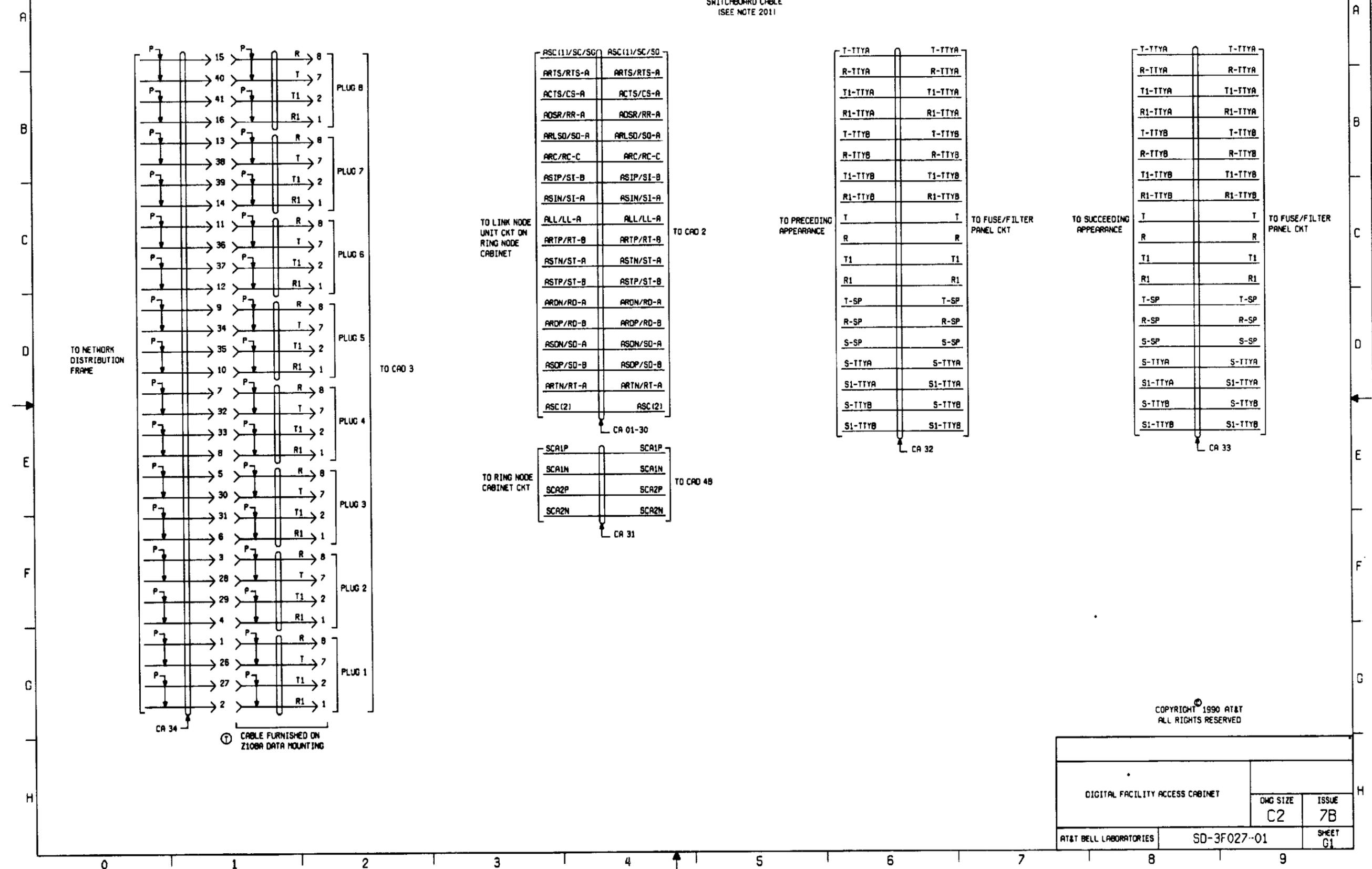
* .5 AMP 70G WITH Q OPTION, 2 AMP 70B WITH R OPTION

306. PRIOR TO ISSUE 7B 'R' OPTION WAS PART OF 'Y' OPTION.

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DIGITAL FACILITY ACCESS CABINET		
DWG SIZE	ISSUE	
C2	7B	
AT&T BELL LABORATORIES	SD-3F027-01	SHEET 03

CAD 1
SWITCHBOARD CABLE
(SEE NOTE 2011)

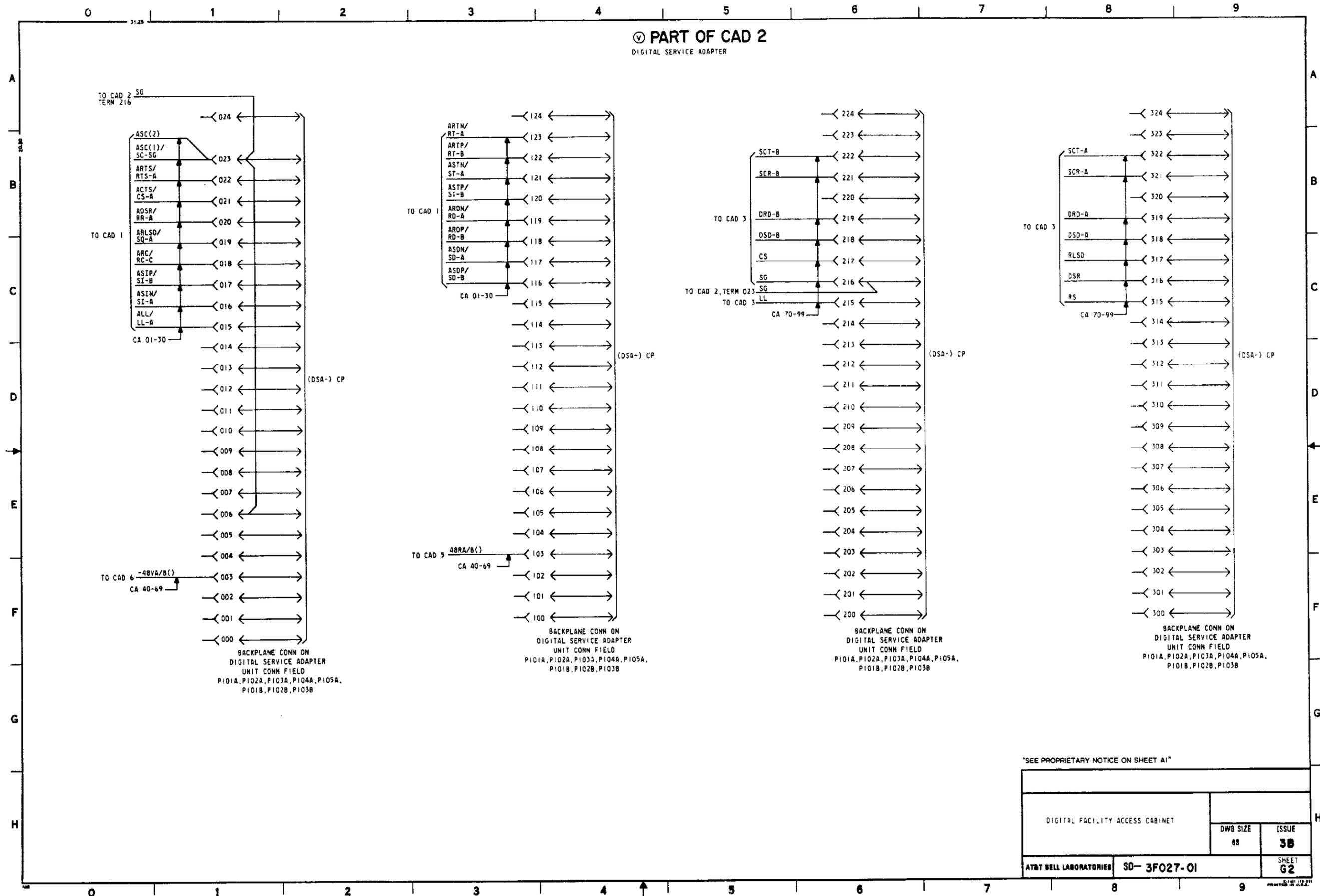


Ⓢ CABLE FURNISHED ON Z108A DATA MOUNTING

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DIGITAL FACILITY ACCESS CABINET		ONG SIZE C2	ISSUE 7B
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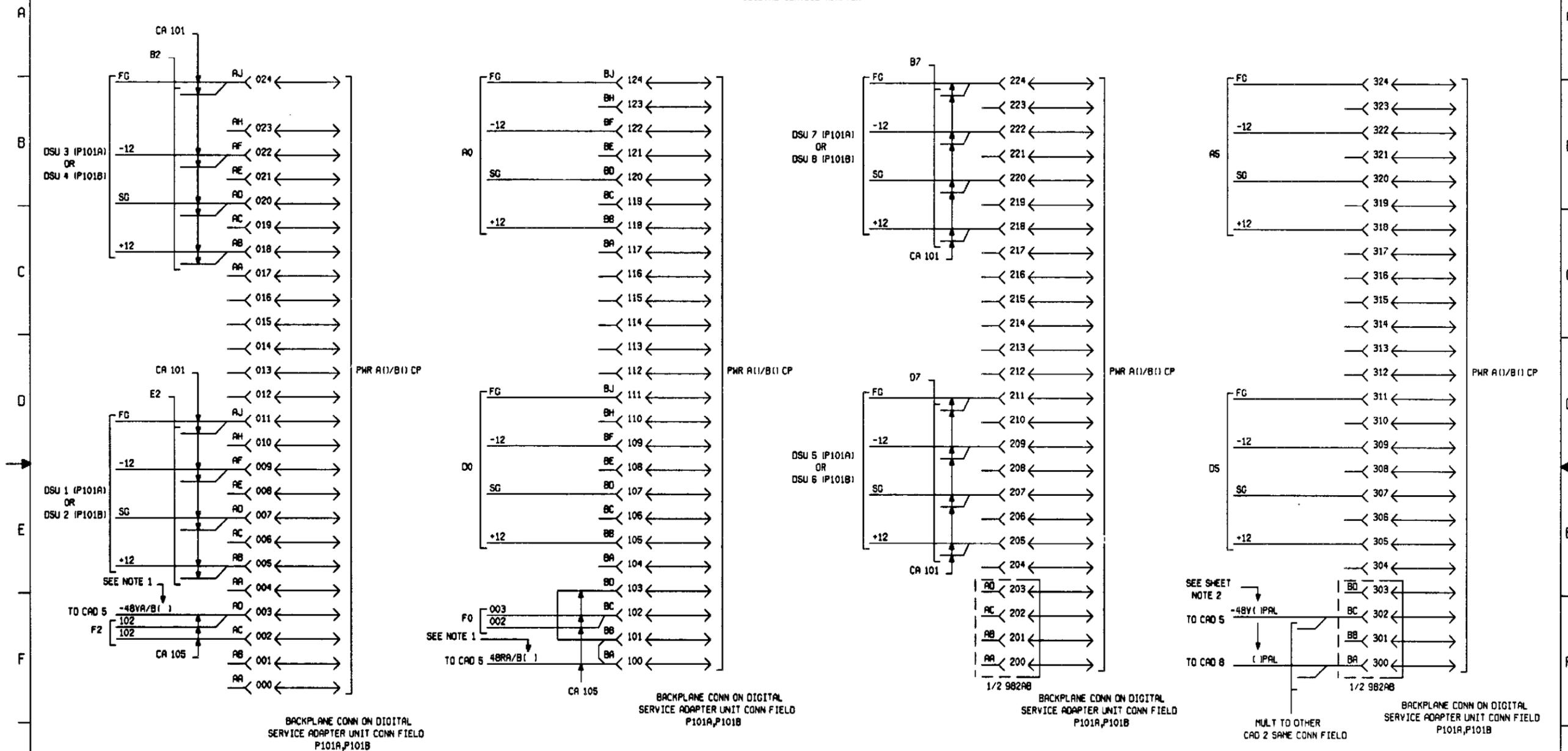
Ⓢ PART OF CAD 2
DIGITAL SERVICE ADAPTER



"SEE PROPRIETARY NOTICE ON SHEET A1"

DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
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AT&T BELL LABORATORIES	SD- 3F027-01	SHEET G2	

P/O CAD 2
DIGITAL SERVICE ADAPTER



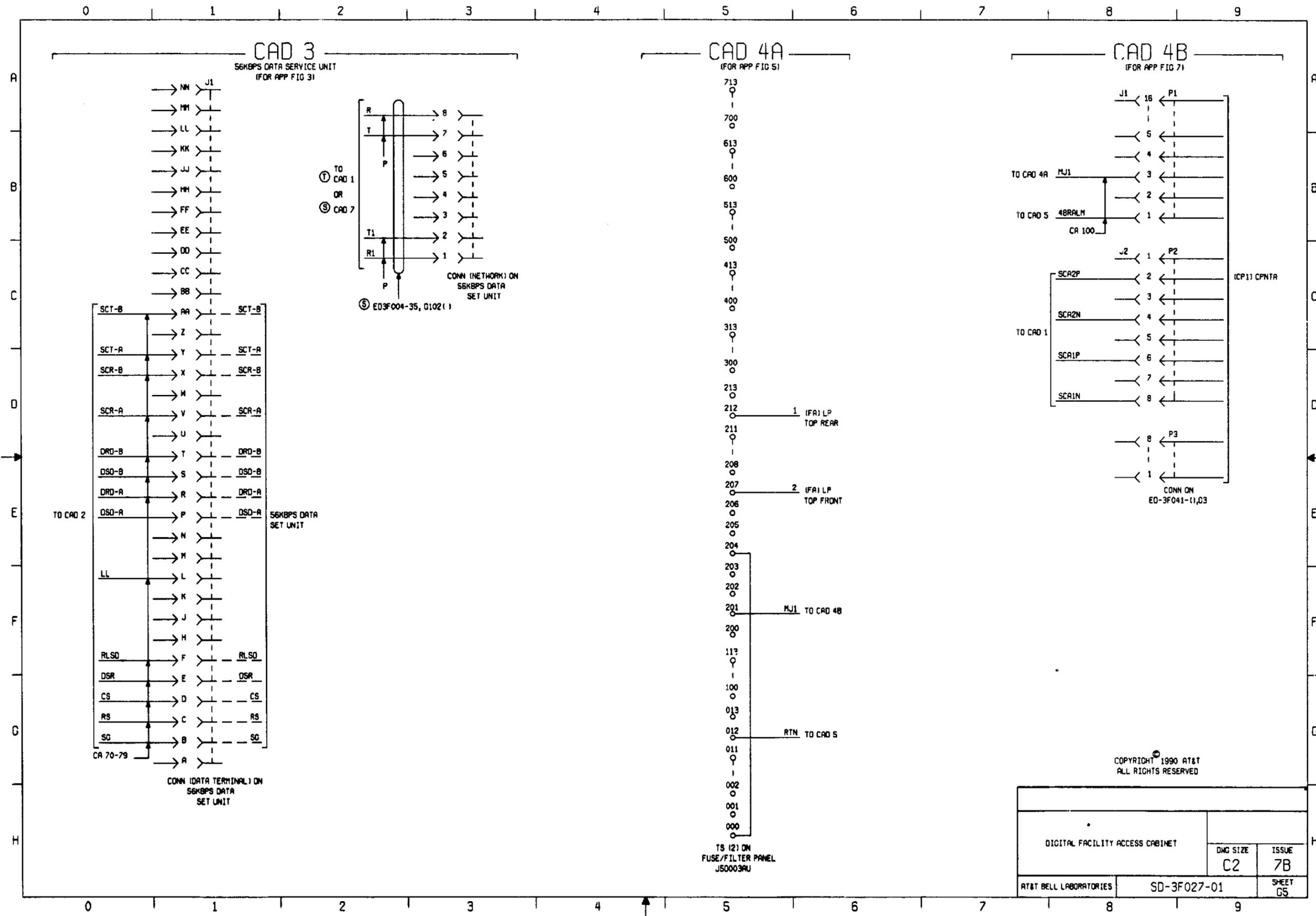
NOTES:

1. CP PWR A(1)/B(1)	BAT & LEAD DESIG
PWR A0	-48VA25, 48RA25
PWR B0	-48VB25, 48RB26
PWR A1	-48VA27, 48RA27
PWR B1	-48VB28, 48RB28
PWR A2	-48VA29, 48RA29
PWR B2	-48VB30, 48RB30

2. DESIG 'A' AT CONN FIELD P101A
DESIG 'B' AT CONN FIELD P101B

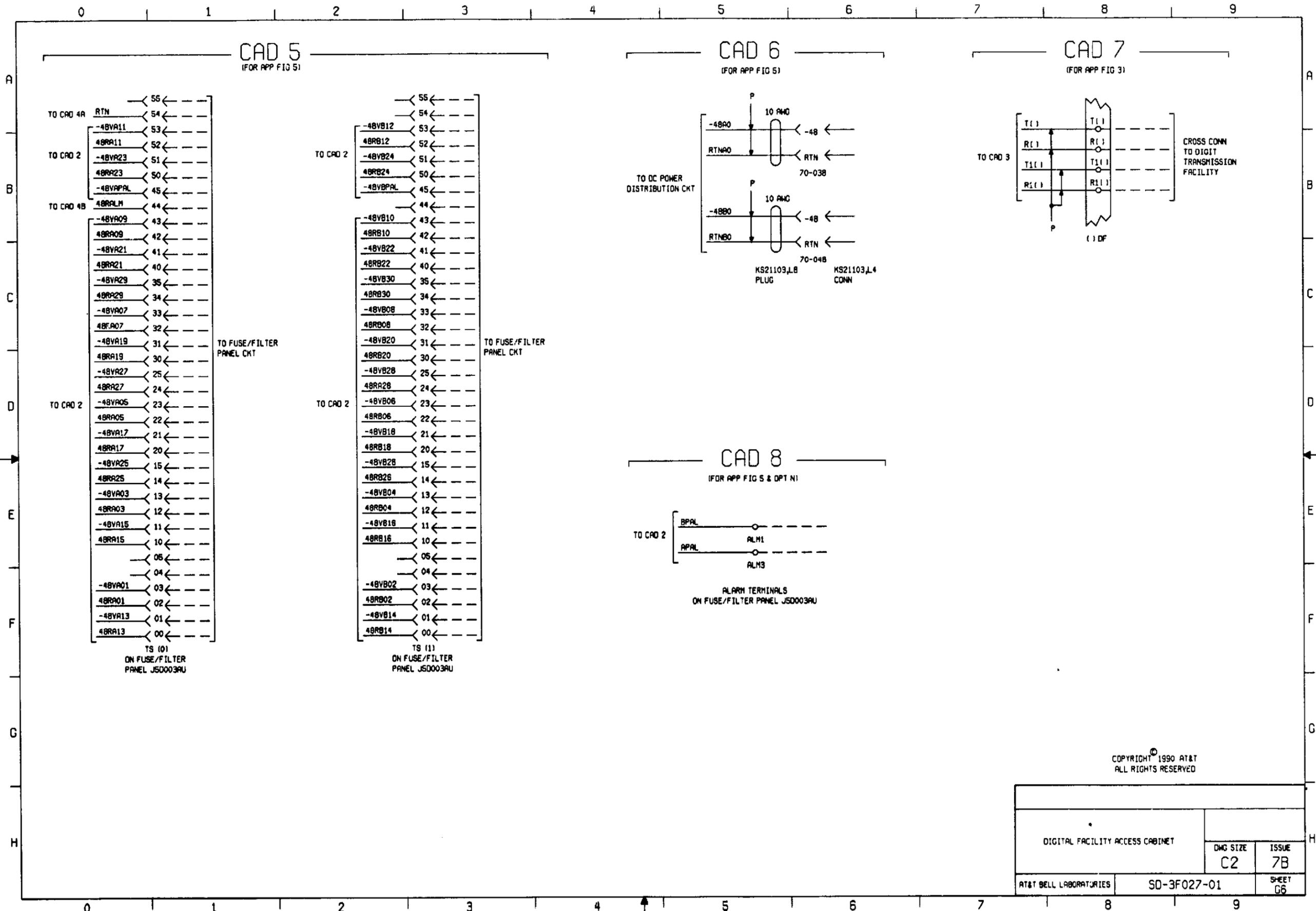
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AT&T BELL LABORATORIES	SD-3F027-01	SHEET 03	

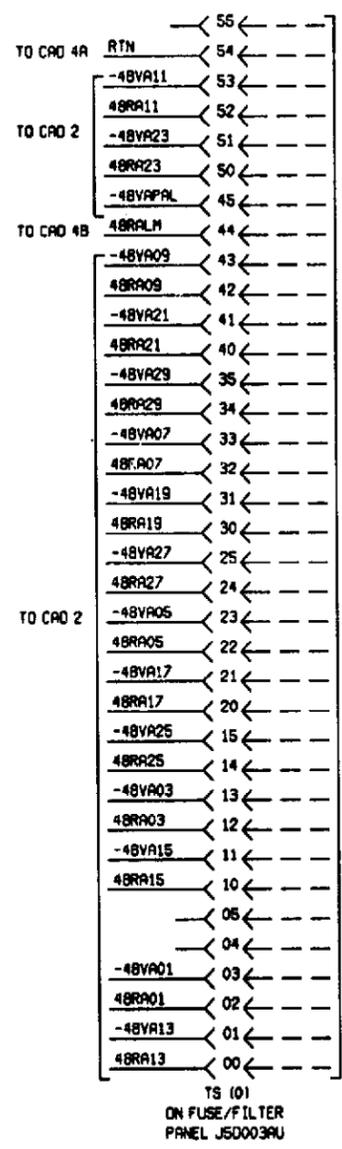


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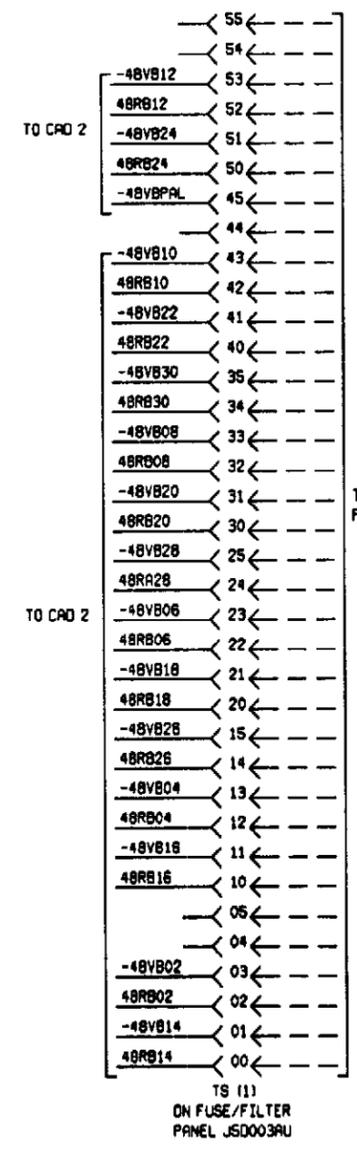
DIGITAL FACILITY ACCESS CABINET		DWG SIZE	ISSUE
		C2	7B
AT&T BELL LABORATORIES	SD-3F027-01	SHEET 05	



CAD 5
(FOR APP FIG 5)

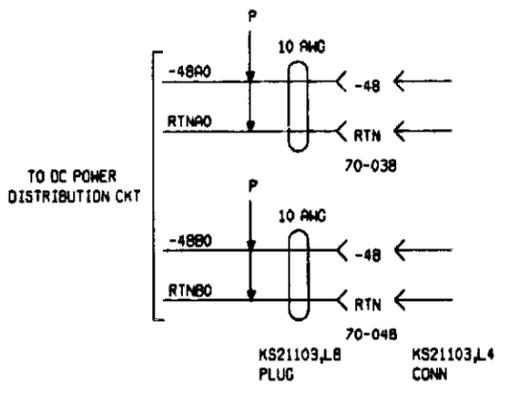


TO FUSE/FILTER
PANEL CKT

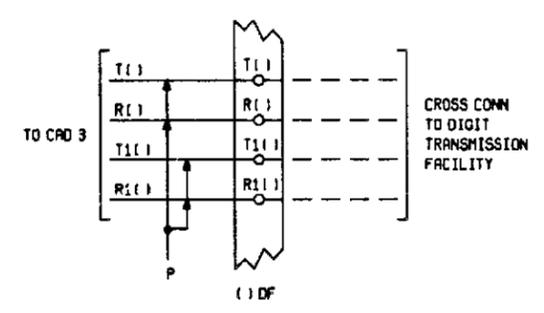


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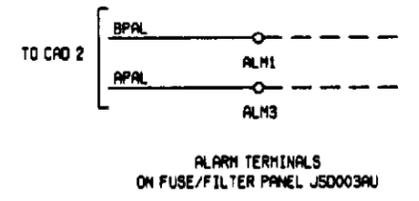
CAD 6
(FOR APP FIG 5)



CAD 7
(FOR APP FIG 3)



CAD 8
(FOR APP FIG 5 & OPT N)



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		C2	7B
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