CONTENTS	SHEET NO.	SHEET ISSUE NO.
SHEET INDEX ENGINEERING AND MANUFACTURING SUPPORTING INFORMATION OPTION INDEX	A1	10
FS 1 3820D MODEL 2 PROCESSOR FIXED FLOOR PLAN (300 MB DISK DRIVE)	B1AA	9
	B1AB	9
FS 2 3820D MODEL 2 PROCESSOR FIXED FLOOR PLAN ARRANGED TO RETROFIT T/DC (340 MB DISK DRIVES,00-07) -5ESS	B2AA	9
FS 3 3820D MODEL 3 PROCESSOR FIXED FLOOR PLAN ARRANGED WITH (340 MB DISK DRIVES, 00-03) AND TAPE UNIT 0 -5ESS	B3AA	9
FS 4 3820D MODEL 3 PROCESSOR FIXED FLOOR PLAN ARRANGED WITH (340 MB DISK DRIVES, 05-15) AND TAPE UNIT 1 -5ESS	B4AA	9
FS 5 3820D MODEL 3 PROCESSOR FIXED FLOOR PLAN ARRANGED WITH (340 MB DISK DRIVES 04-15) -5ESS	B5AA	9
FS 6 3820D MODEL 3 PROCESSOR FIXED FLOOR PLAN ARRANGED WITH PERIPHERAL INTERFACE CABINET J1C215A-1	B6AA	9
APT. FIG (MODEL 2) (RETROFIT)	C1	10
APP. FIG. (MODEL 3)	C2	10
APP. FIG. (MODEL 3)	C3	10
CIRCUIT NOTES	D1	9
	D2	9
	D3	9
	D4	9
	D5	9
	D6	10
	D7	10
	D8	9

CONTENTS	SHEET NO.	SHEET ISSUE NO.
INFORMATION NOTES	D9	10
	D10	10
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	D12	9
	D13	9
	D14	9
	D15	9
	D16	9
	D17	9
	D18	9
	D19	9
	D20	9
	D21	9

OPTION INDEX

APP OR WRG	RATED ON ISSUE	REF NOTES	LOCATION

DWG ISS	CD ISS	DATE ISSD	DRW	APP
1	1	10-12-83		
2B	1 APPX 1B	05-14-84		
3B	1 APPX 3B	11-06-84		
4B	1 APPX 3B	04-29-85		
5A	1 APPX 5A	04-29-85		
6A	1 APPX 5A	04-29-85		
7AC	1 APPX 5AC	04-29-85		
8B	1 APPX 7B	01-29-86		
9B	2B	06-27-90		
10B	2B APPX 1B	02-23-93		

SUPPORTING INFORMATION

SYSTEM USED ON	DESIGN CONTROL	CATEGORY	NO.
5ESS	IH	EQUIPMENT DRAWING	J1C173A J1C174A J1C175A J1C176A J1C175B J1C176B J1C192A J1C215A J1C215B

SHEET INDEX NOTES

1. ONLY THE LATEST ISSUE, OR ISSUES IF CONCURRENT, ARE SHOWN IN THE INDEX.
2. FOR REISSUES, A CHANGED OR NEW SHEET IS ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.
3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

RN88

COMMON SYSTEMS

3B20D MODEL 2 & 3 PROCESSOR

PROCESSOR SYSTEM CABINET

FOR 5ESS ®

DWG SIZE	ISSUE
C2	10B

AT&T	SD-4C122-01	SHEET 32	A1 SHEETS

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PART OF FS I

3B20D MODEL 2 PROCESSOR
FIXED FLOOR PLAN (309) (MB DISK DRIVE)
INTERCONNECTION AND FLOW DIAGRAM
(SEE NOTE 309)

FLOOR PLAN LAYOUT FOR 3B20D MODEL 2 FOR 5 ESS.

TAPE CABINET J1C174A-1 SD-4C120-01

PROCESSOR CONTROL CABINET J1C173A-1 SD-4C119-01
BAY 0 | BAY 1
FRONT

DISK POWER CABINET J1C175A-1 SD-4C119-01

DISK DUCT WORK

DISK TRANS KS-22707 L1 OR L2 — MHD0 DISK DRIVE

DISK TRANS KS-22707 L1 OR L2 — MHD2 DISK DRIVE

DISK TRANS KS-22707 L1 OR L2 — MHD1 DISK DRIVE

(AC & DC)

TO RMP

TO MTTY TERM

TO AC & DC PWR DIST CKT (BUS A)

TO AC & DC PWR DIST CKT (BUS B) (PORT SWITCH ONLY)

TO AC & DC PWR DIST CKT (BUS B)

ZA64

ZA65

ZA65

FS 9/SYM 11 SD-4C119-01 (NOTE 309) POWER

033-086-550 SD-4C119-01 CAD 205 SCAN/SD

033-086-537 SD-4C119-01 CAD 205 SCAN/SD

FS 9/SYM 8 SD-4C119-01 (NOTE 309) POWER

133-093-037 SD-4C119-01 CAD 208 SCAN/SD

012-075-408 SD-4C121-01 CAD 08 SCAN/SD

050-075-408 SD-4C121-01 CAD 08 SCAN/SD

051-075-408 SD-4C121-01 CAD 08 SCAN/SD

FS 9/SYM 6 SD-4C119-01 (NOTE 309) POWER

009-040-515 SD-4C119-01 CAD 202 MTTY

009-056-515 SD-4C119-01 CAD 203 RBP

TAPE UNIT CABINET J1C174A-1

036-090-1 SD-4C120-01 CAD 1 TAPE CABLE

ZA165

035-078-532
035-078-532
035-078-132
SD-4C119-01
(NOTE 306.2)
TAPE CABLE

PROCESSOR CONTROL CABINET J1C173A-1 BAY 0

PROCESSOR CONTROL CABINET J1C175A-1 BAY 1

DISK POWER CABINET J1C175A-1

051-064-532 SD-4C119-01 CAD 111 B01

051-044-500 SD-4C119-01 CAD 110 A0

051-064-515 SD-4C119-01 CAD 111 B00

151-044-500 SD-4C119-01 CAD 110 A0

151-064-515 SD-4C119-01 CAD 111 B00

SD-4C121-01 CAD 02 PWR IN UNIT

010-125-A SD-4C121-01 CAD 07 AC POWER

029-125-A SD-4C121-01 CAD 07 AC POWER

048-125-A SD-4C121-01 CAD 07 AC POWER

J4

J4

J4

1AB/CO

ZA161B OR ZA161E
ZA172B OR ZA172E
ZA161 OR ZA161C
ZA172 OR ZA172C
ZA161A OR ZA161D

1AB/CO

TO AC & DC POWER DIST CKT

PROCESSOR SYSTEM CABINET

DWG SIZE	ISSUE
6S	9B

AT&T | SD-4C122-01 | SHEET B1AA

PART OF FS I

3B20D MODEL 2 PROCESSOR
FIXED FLOOR PLAN (300 MB DISK DRIVE)
INTERCONNECTION AND FLOW DIAGRAM
(SEE NOTE 308)

1AA/F9 J4
J4
J4

J01
SD-4C056-02
CAD 07
AC POWER

MHD0
DISK DRIVE
KS-22707
L1 OR L2

A P3 SD-4C056-02 CAD 10 A0
B P4 SD-4C056-02 CAD 11 A0
P2 SD-4C056-02 CAD 12 B00

MHD2
DISK DRIVE
KS-22707
L1 OR L2

A
B
P2 SD-4C056-02 CAD 12 B01

MHD1
DISK DRIVE
KS-22707
L1 OR L2

A
B
P2 SD-4C056-02 CAD 12 B00

ZA172 OR ZA172C
ZA161 OR ZA161C
ZA172A OR ZA172D
1AA/G6 ZA161A OR ZA161D
ZA172B OR ZA172E
ZA161B OR ZA161E

TG BTR BTR0
BTR1

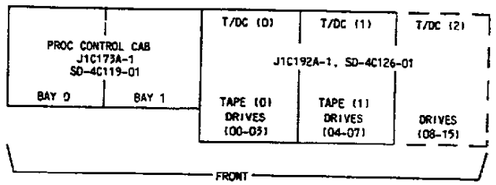
PROCESSOR SYSTEM CABINET

DWG SIZE B5 | ISSUE 9B

AT&T | SD-4C122-01 | SHEET B1AB

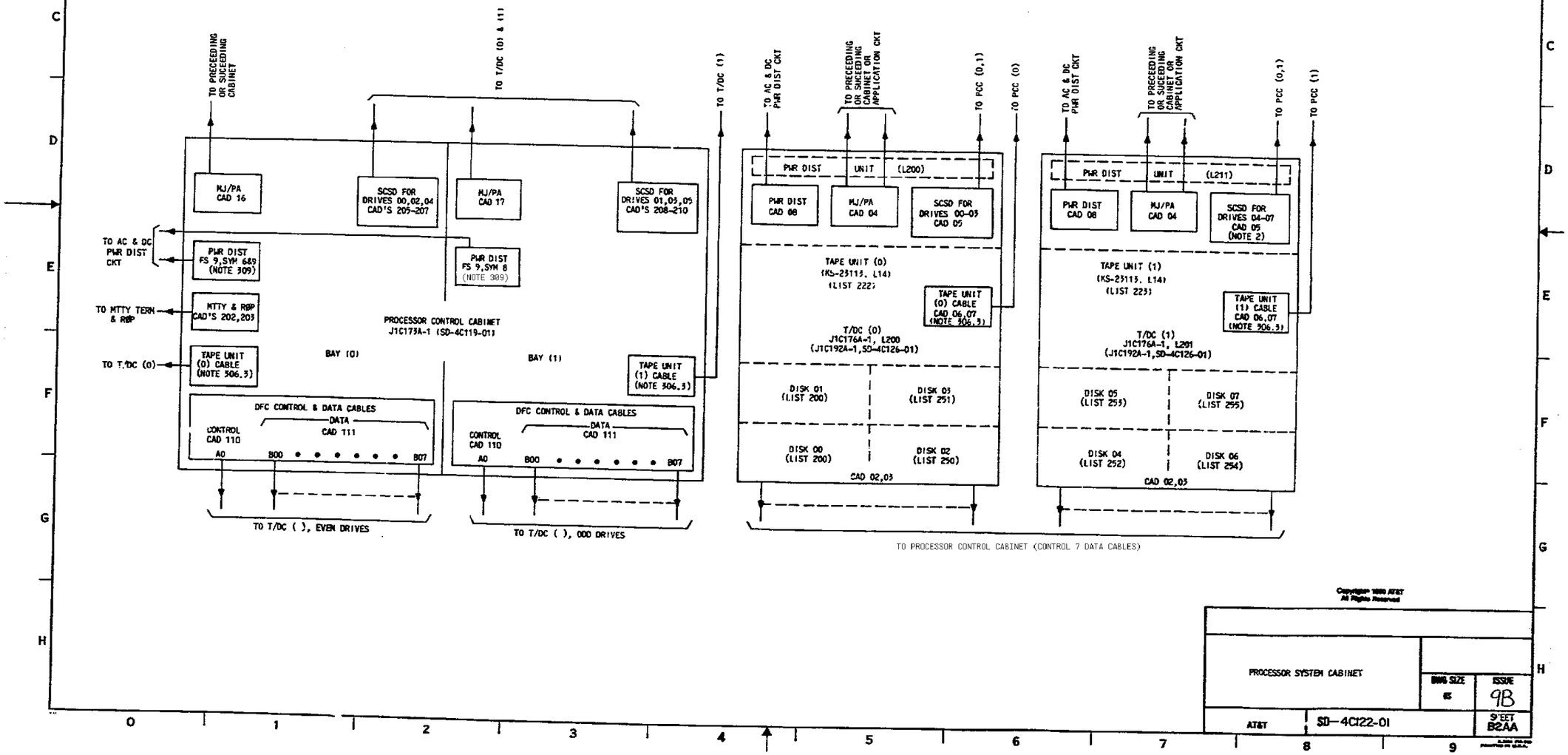
PART OF FS 2

3B20D MODEL 2 PROCESSOR
FIXED FLOOR PLAN ARRANGED TO RETROFIT
T/DC (340 MB DISK DRIVES, 00-07) - 5ESS
(FPO 820-800-000-17)
INTERCONNECTION AND FLOW DIAGRAM



NOTES:

1. SEE NOTE 310 & 311 FOR CABLING INFORMATION.
2. SCSD CABLING FOR DRIVE 06(BAY 0) & 07(BAY 1) SHALL BE JOB ENGR.

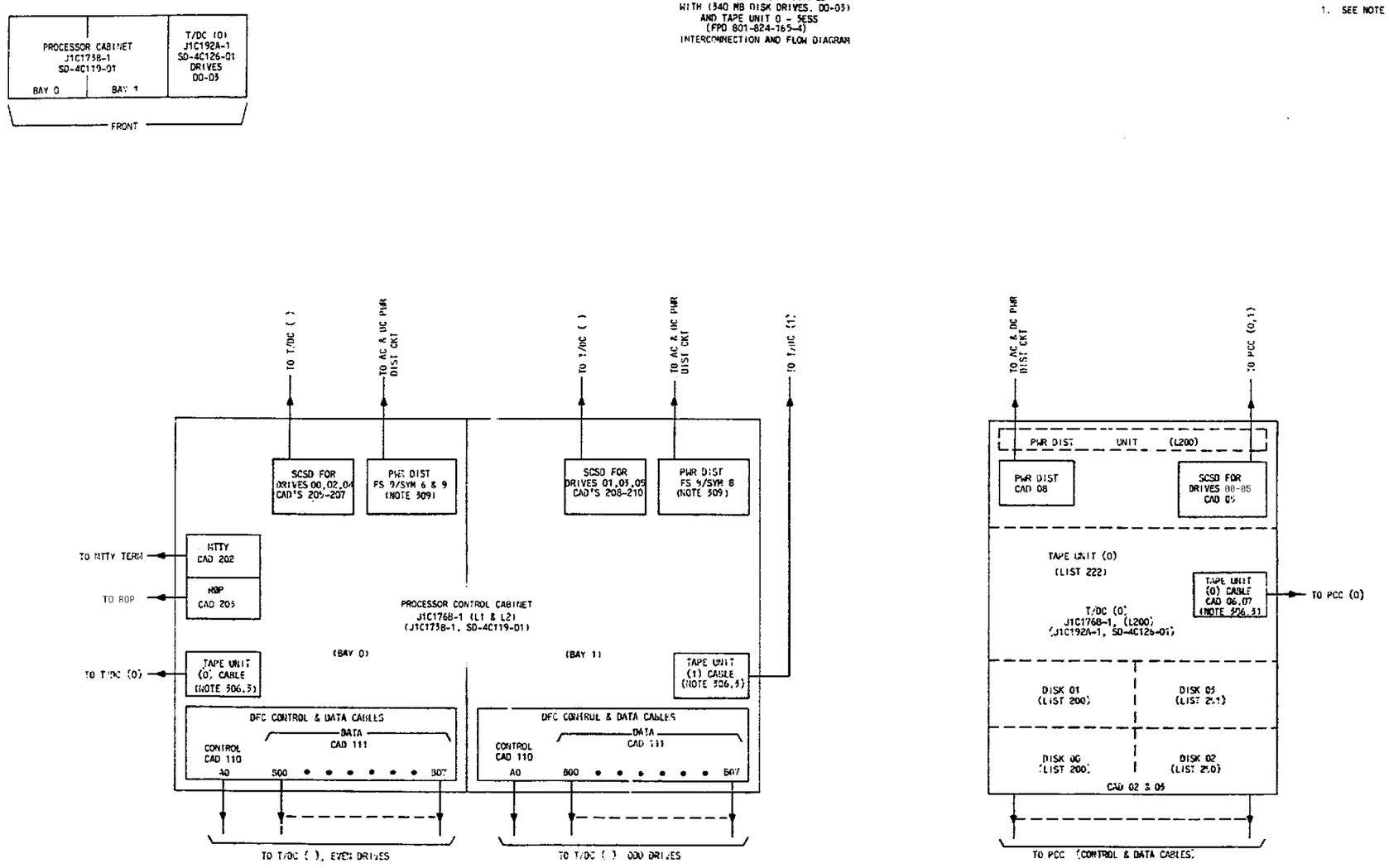


PROCESSOR SYSTEM CABINET		
AT&T	SD-4C122-01	SIZE B; ISSUE 9B; SHEET B2AA

PART OF FS 3

3B20D MODEL 3 PROCESSOR
FIXED FLOOR PALN ARRANGED
WITH (340 MB DISK DRIVES, 00-03)
AND TAPE UNIT 0 - 5ESS
(FPD 801-824-165-4)
INTERCONNECTION AND FLOW DIAGRAM

NOTES:
1. SEE NOTE 311 & 312 FOR CABLING INFORMATION.

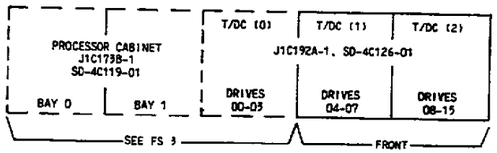


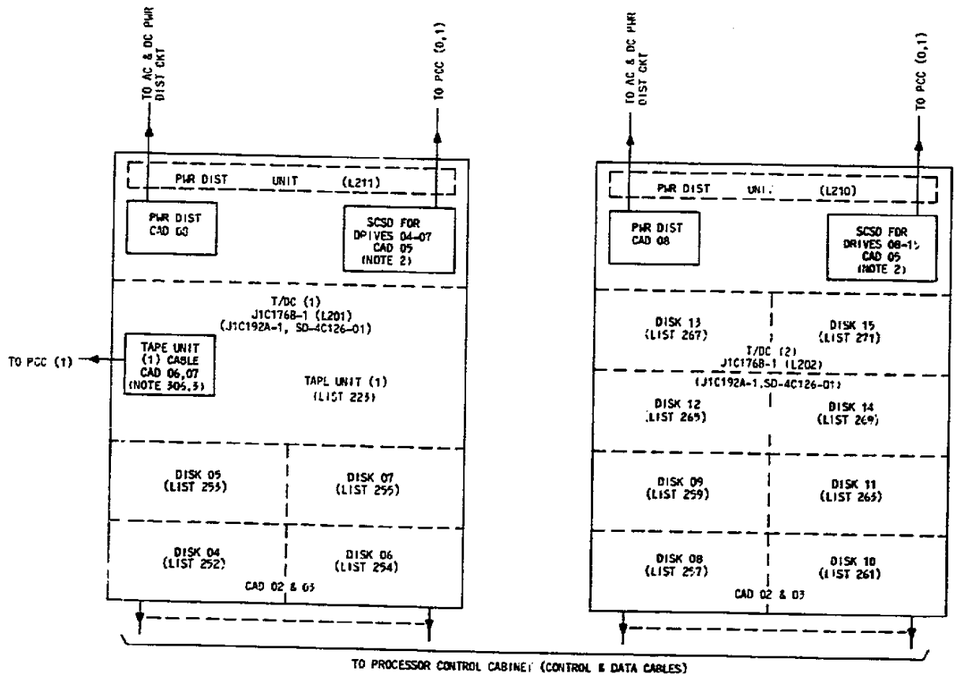
PROCESSOR SYSTEM CABINET		DWG SIZE: 6S	ISSUE: 9B
AT&T	SD-4C122-01		SHEET: B3AA

PART OF FS 4

3B20D MODEL 3 PROCESSOR
FIXED FLOOR PLAN ARRANGED
WITH (340 MB DISK DRIVES, 04-15)
AND TAPE UNIT 1 - 5ESS
(FPD 801-824-165-4)
INTERCONNECTION AND FLOW DIAGRAM

NOTES:
1. SEE NOTES 311 & 312 FOR CABLING INFORMATION.
2. SCSD CABLING FOR DRIVES 6-15 SHALL BE JOB ENGR.



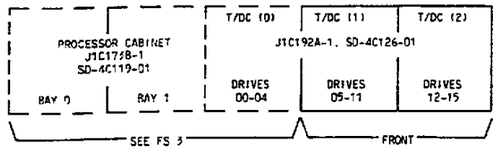


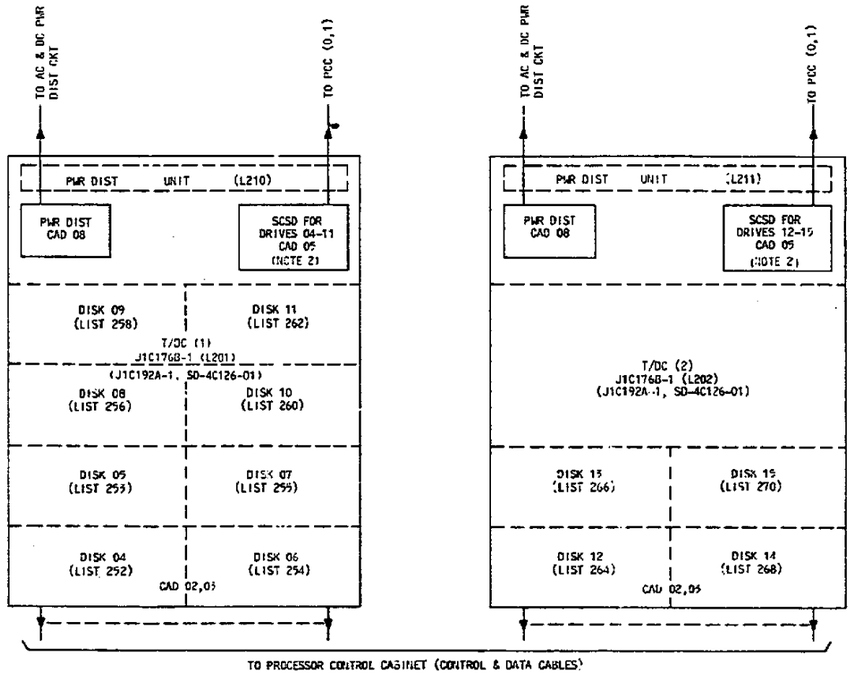
PROCESSOR SYSTEM CABINET		
	DWG SIZE: B5	ISSUE: 9B
AT&T	SD-4C122-01	SHEET: B4AA

PART OF FS 5

3B20D MODEL 3 PROCESSOR
FIXED FLOOR PLAN ARRANGED
WITH (340 MB DISK DRIVES, 04-15) - 5ESS
(FPD-B20-B00-000-17)
INTERCONNECTION AND FLOW DIAGRAM

NOTES:
1. SEE NOTES 311 & 312 FOR CABLING INFORMATION.
2. SCSD CABLING FOR DRIVES 6-15 SHALL BE JOB ENGR.





PROCESSOR SYSTEM CABINET

DWG SIZE	ISSUE
6S	9B

AT&T	SD-4C122-01	SHEET
		B5AA

PART OF FS 6

3820D MODEL 3 PROCESSOR
FIXED FLOOR PLAN ARRANGED
WITH PERIPHERAL INTERFACE CABINET
J1C215A-1, GROWTH I&P
INTERCONNECTION AND FLOW DIAGRAM

PIC 0
J1C215A-1

PROCESSOR CABINET
J1C173B-1
SD-4C119-01
BAY 0
BAY 1

T/DC 0
J1C192A-1
SD-4C126-01
DRIVES
00-03

FRONT
SEE FS 5

POWER DISTRIBUTION UNIT
J1C147BE-1

2ND I&P GROWTH UNIT
J1C161BC-1

60-076-100 SD-4C115-01 CAD 5 IOP
60-076-113 SD-4C115-01 CAD 5 IOP
60-076-132 SD-4C115-01 CAD 5 IOP
60-070-100 SD-4C115-01 CAD 5 IOP
60-070-113 SD-4C115-01 CAD 5 IOP
60-070-132 SD-4C115-01 CAD 5 IOP

TO APPLICATION CKT (JOB ENGINEERED)
51-154-106 SD-4C115-01 CAD 12 DSCH (NOTE 102.3)
51-076-113 SD-4C115-01 CAD 4 IOP
51-076-132 SD-4C115-01 CAD 4 IOP
51-076-145 SD-4C115-01 CAD 4 IOP
51-070-113 SD-4C115-01 CAD 4 IOP
51-070-132 SD-4C115-01 CAD 4 IOP
51-070-145 SD-4C115-01 CAD 4 IOP
51-162-306 SD-4C115-01 CAD 16 SCSD

TO PROCESSOR CABINET

TO APPLICATION CKT (JOB ENGINEERED)
51-154-306 SD-4C115-01 CAD 12 DSCH (NOTE 102.3)

TO APPLICATION CKT OR PRECEDING CAB.
51-178-316 SD-4C115-01 CAD 15 ALM

51-178-309 SD-4C115-01 CAD 13 ALM & START

2ND I&P BASIC UNIT
J1C161BB-1

1ST I&P GROWTH UNIT
J1C161BC-1

42-076-100 SD-4C115-01 CAD 5 IOP
42-076-113 SD-4C115-01 CAD 5 IOP
42-076-132 SD-4C115-01 CAD 5 IOP
42-070-100 SD-4C115-01 CAD 5 IOP
42-070-113 SD-4C115-01 CAD 5 IOP
42-070-132 SD-4C115-01 CAD 5 IOP

FROM PROCESSOR CABINET
33-076-113 SD-4C115-01 CAD 4 IOP
33-076-132 SD-4C115-01 CAD 4 IOP
33-076-145 SD-4C115-01 CAD 4 IOP
33-070-113 SD-4C115-01 CAD 4 IOP
33-070-132 SD-4C115-01 CAD 4 IOP
33-070-145 SD-4C115-01 CAD 4 IOP
33-178-319 SD-4C119-01 CAD 14 ALM
33-178-309 SD-4C115-01 CAD 13 ALM & START

TO APPLICATION CKT (JOB ENGINEERED)
33-154-106 SD-4C115-01 CAD 12 DSCH (NOTE 102.3)

TO PROCESSOR CABINET
33-162-306 SD-4C115-01 CAD 16 SCSD

TO APPLICATION CKT (JOB ENGINEERED)
33-154-306 SD-4C115-01 CAD 12 DSCH (NOTE 102.3)

1ST I&P BASIC UNIT
J1C161BB-1

COOLING UNIT
ED-4C497-30

22-110-105 EPS-4C367-01 START

PROCESSOR SYSTEM CABINET

DWG SIZE: 8S
ISSUE: 9B

AT&T | SD-4C122-01 | B6AA

APP FIG SUMMARY

(SEE ALSO NOTE 302)

3B20D MODEL 2

FEATURE OR OPTION	APP FIG.	QTY	CABINET/UNIT EQPT DATA	PROCESSOR SYS DATA
PROCESSOR CONTROL CABINET (CACHE STRAP BOARD) DISK POWER CABINET	1	1	J1C173A-1, L1 & L2	J1C176A-1, L1
		2	UN30B CP	
		1	J1C175A-1, L1	
300 MEGABYTE DISK DRIVE (SEE NOTE 304)	2	3	KS-22707,L1	J1C176A-1, L2
	3	3	KS-22707, L2	J1C176A-1, L3
TAPE UNIT CABINET (SEE NOTE 306)	4	1	J1C174A-1,L1	J1C176A-1, L4
256K (1MB) OF ADDITIONAL MEMORY (TN28) (SEE NOTE 305)	5	2	TN28 CP	J1C176A-1, L5
CACHE MEMORY	6	4	UN10B CP	J1C176A-1, L6
		2	UN11B CP	
		OMIT 2	UN30B CP	
4K OF ADD'L WRITEABLE MICROSTORE	7	2	UN48B CP	J1C176A-1, L7
UTILITY CIRCUIT	8	2	UN21B CP	J1C176A-1, L8
PWR CONTROL FOR COMM 3 (PC SLOTS 30-33)	9	2	TN9 CP	J1C176A-1, L9
VIDIO TERM (COLOR)	10	1	KS-22921, L3	J1C176A-1, L10
READ ONLY PRINTER	11	1	#R040P2F TTY CORP OR EQUIV	J1C176A-1, L11
DMAC 1	12	2	UN46 CP	J1C176A-1, L12
IO DUAL SERIAL CHANNEL, (DSCH) (SEE NOTE 306)	13	2	UN9B CP	J1C176A-1, L13
PORT SWITCH UNIT (SEE NOTE 102.2)	14	2	TF4 CP	J1C176A-1, L14
ADD'L 5 VOLT POWER FOR CPU UNIT	15	2	495FA CP	J1C176A-1, L15
ADD'L 5 VOLT POWER FOR GROWTH UNIT	16	2	495FA CP	J1C176A-1, L16
4K PROM MC4C077A1B, (UN28B) (UNIX RTR RELEASE 1)	17	1	J1C173A-1, L3	J1C176A-1, L17
16K EPROM WMCS, MC4C127A1 (TN19) & UN55 (UNIX RTR RELEASE 1)	18	1	J1C173A-1, L4	J1C176A-1, L18
1ST 2MB MSM (TN5) (MFR DISC)	19	1	J1C173A-1, L5	J1C176A-1, L19
2MB OF ADD'L MSM (TN56) (SEE NOTE 305)	20	2	TN56	J1C176A-1, L20
MTTY CONTROLLER WITH EAI PAGE ENHANCEMENTS, MC4C132A1, (TN983)	21	1	J1C173A-1, L6	J1C176A-1, L21
TAPE UNIT CABINET (SEE NOTE 306)	22	1	J1C174A-1, L2	J1C176A-1, L22
PWR CONV FOR TAPE CABINET	23	1	J1C174A-1, L3	J1C176A-1, L23
MEMORY EXPANSION CAPABILITY (UNIX RTR RELEASE 1)	24	1	J1C173A-1, L8	J1C176A-1, L24
1ST 2MB OF MSM	25	2	TN56	J1C176A-1, L25

3B20D MODEL 2

FEATURE OR OPTION	APP FIG.	QTY	CABINET/UNIT EQPT DATA	PROCESSOR SYS DATA
IMPROVED DFC FIRMWARE MC4C061B1C (TN68)	26		J1C173A-1, L7	J1C176A-1, L26
MEMORY EXPANSION ASSOCIATED WITH CACHE MEMORY	27	4	UN10C CP	J1C176A-1, L27
		4	OMIT UN10B	
		2	UN11C CP	
		2	OMIT 11B	
STORE ADDRESS TRANSLATOR (SAT)	28	1	J1C173A-1, L9	J1C176A-1, L28
BACKPLANE WIRING FOR IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE, UN45C AND UNIX RTR RELEASE 1 OR LATER SOFTWARE REQUIRED.	29	1	J1C173A-1, L11	J1C176A-1, L29
SYSTEM SANITY ALARM FEATURE	30		OMIT ALARM CABLES: ED-4C410-35,G16, G16R,G16S AND G16T	J1C176A-1, L
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	100	1	UN33B	J1C176A-1, L100
2 CHANNEL (2 PORTS PER CH) TTY ASYNCHRONOUS LINK PC (SEE NOTE 306)	101	1	MC4C011A1B (TN74B) CP	J1C176A-1, L101
2 CHANNEL SYNCHRONOUS LINK PC (SEE NOTE 306)	102	1	MC4C048A1B (TN75C) CP	J1C176A-1, L102
SCANNER/SIGNAL DISTRIBUTOR INTERFACE	103	1	TF2 CP	J1C176A-1, L103 (MFR DISC)
HIGH SPEED TAPE PC (SEE NOTE 306)	104	1	UN52 CP	J1C176A-1, L104
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	105	1	MC4C052A1 (TN82)	J1C176A-1, L105
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	107	1	UN33C	J1C176A-1, L107
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	108	1	MC4C052A1C (TN82B) CP	J1C176A-1, L108
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	109	1	MC4C052A1D (TN82B) CP	J1C176A-1, L109
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	110	1	UN33D	J1C176A-1, L110
1600 BPI HIGH SPEED TAPE (SEE NOTE 306)	111	1	UN52B	J1C176A-1, L111
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	112	1	MC4C052A1E (TN82B) CP	J1C176A-1, L112
2 CHANNEL SYNCHRONOUS LINK (TN1839-NSI APPLICATION ONLY) (SEE NOTE 306)	113	1	TN1839	J1C176A-1, L113
BX.25 HIGH SPEED (64 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	114	1	TN1420	J1C176A-1, L114
PROVIDE TN19 MC4C127A1B	M		TN19 MC4C127A1B	J1C176A-1, LIST M

TAPE/340 MB DISK DRIVE CABINET, T/D( ), RETROFIT

FEATURE OR OPTION			APP FIG.	QTY	CABINET/UNIT EQPT DATA	PROCESSOR SYS DATA
1ST TAPE/DISK CABINET ARRANGED FOR 1ST TAPE UNIT, FOUR (4) 340 MB DISK DRIVES & PWR DIST UNIT (RETROFIT)	CABINET ASSY		200	1	J1C192A-1, L1	J1C176A-1, L200
	PWR DIST UNIT			1	J1C192A-1, L5A	J1C176A-1, L200
	340 MB DISK DRIVES (KS-22875,L14-L18 E/W L51)	NO: 00		1	J1C192A-1, L2	J1C176A-1, L200
		01		1	J1C192A-1, L2A	J1C176A-1, L200
		02	250	1	J1C192A-1, L3	J1C176A-1, L250
		03	251	1	J1C192A-1, L3A	J1C176A-1, L251
	TAPE TRANSPORT		222	1	J1C192A-1, L4	J1C176A-1, L222
1ST GROWTH TAPE/DISK CABINET ARRANGED FOR 2ND TAPE, FOUR (4) 340 MB & PWR DIST UNIT (RETROFIT)	CABINET ASSY		201	1	J1C192A-1, L1	J1C176A-1, L201
	PWR DIST UNIT		211	1	J1C192A-1, L5A	J1C176A-1, L211
	340 MB DISK DRIVES (KS-22875,L14-L18 E/W L51)	NO: 04	252	1	J1C192A-1, L2	J1C176A-1, L252
		05	253	1	J1C192A-1, L2A	J1C176A-1, L253
		06	254	1	J1C192A-1, L3	J1C176A-1, L254
		07	255	1	J1C192A-1, L3A	J1C176A-1, L255
	TAPE TRANSPORT		223	1	J1C192A-1, L4	J1C176A-1, L223

PROCESSOR SYSTEM CABINET

DWG SIZE	ISSUE
C2	10B

AT&T | SD-4C122-01 | SHEET C1

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APP FIG SUMMARY

(SEE ALSO NOTE 302)

3B20D MODEL 3

FEATURE OR OPTION	APP FIG.	QTY	CABINET/UNIT EQPT DATA	SYSTEM EQPT DATA
PROCESSOR CONTROL CABINET (BAY 0 & 1). EACH BAY ARRANGED FOR 8MB OF 1MB (TN28) MAIN STORE MEMORY (MSM) OR 16MB (TN56) MSM; 16K EPROM (TN19) WRITABLE MICRO CONTROL STORE (WMCS); 4K PROM MICROCODE (UN28B); 4K WRITABLE MICROCODE (UN48B); MAINTENANCE CHANNEL (MCH); UTILITY CIRCUIT (UC); CACHE MEMORY (CACHE);DISK FILE CONTROLLER (DFC); DIRECT MEMORY ACCESS 0 & 1 (DMA0 , DMA1); 2 IO CHANNELS; IO PROCESSOR (IOP) ARRANGED FOR 2 COMMUNITIES (COMM 0 & 1) EACH COMM ARRANGED FOR 4 PC; EACH BAY E/W 1ST 1MB (TN28) MSM, 16K EPROM, MC4C127A1 (TN19) WMCS; 4K PROM MICROCODE (UN28B), 4K WRITABLE MICROCODE (UN48B)), MCH, STORE ADDRESS TRANSLATOR (UN45C), DFC, DMA0 E/W 11 & WIRED FOR CH12, IOP (COMM 0 & 1) E/W PC00 (MTTY) & PC02 (SCSD); PORT SWITCH UNIT (BAY 0 ONLY) ARRANGED FOR 2 PORT SW COMM (0 & 1) & E/W COMM 0 (3-TF4 CP)	1	1	J1C173B-1, L1	J1C176B-1, L1
MAIN STORE & IOP GROWTH UNIT ARRANGED FOR 8MB OF 1MB (TN28) MSM OR 16MB (TN56) MSM & IOP (COMM 2 & 3, 4-PC'S EACH & 4 IO CHANNELS)	2	1	J1C173B-1, L2	J1C176B-1, L2
NOT USED	3			
NOT USED	4			
1MB OF ADD'L MAIN STORE MEMORY (SEE NOTE 305)	5	2	TN28	J1C176B-1, L5
CACHE MEMORY	6	4	UN10	J1C176B-1, L6
		2	UN11	J1C176B-1, L6
		2	OMIT UN30	J1C176B-1, L6
NOT USED	7			
PWR FOR COMM 2 (PC SLOTS 20-23)	8	1	J1C176B-1, L8 & TN9, 495FA	J1C176B-1, L8
PWR FOR COMM 3 (PC SLOTS 30-33)	9	1	J1C176B-1, LC & TN9	J1C176B-1, L9
MTCE TTY TERM (COLOR)	10	1	KS-22921, L2	J1C176B-1, L10
READ ONLY PRINTER	11	1	RO40P2F	J1C176B-1, L11
NOT USED	12			
IO DUAL SERIAL CHANNEL (DSCH)	13	2	UN66	J1C176B-1, L13
NOT USED	14			
ADD'L 5 VOLT PWR FOR M.S. IO & DFC UNIT	15	2	495FA	J1C176B-1, L15
ADD'L 5 VOLT PWR FOR GROWTH UNIT	16	2	495FA	J1C176B-1, L16
NOT USED	17			
NOT USED	18			
1ST 2MB MSM (TN56) (MFR DISC)	19	1	J1C173B-1, L3	J1C176B-1, L19
2MB OF ADD'L MSM (TN56) (MAX 15) (SEE NOTE 305)	20		TN56	J1C176B-1, L20
MTCE TTY CONTROLLER WITH EAI PAGE ENHANCEMENTS (MC4C132A1, TN963)	21	1	J1C173B-1,L4	J1C176B-1, L21

3B20D MODEL 3

FEATURE OR OPTION	APP FIG.	QTY	CABINET/UNIT EQPT DATA	SYSTEM EQPT DATA
MEMORY EXPANSION CAPABILITY (UNIX RTR RELEASE 1)	22	1	J1C173B-1, L5	J1C176B-1, L22
1ST 2MB OF MSM	23	1	J1C173B-1, L6	J1C176B-1, L23
MEMORY EXPANSION CAPABILITY ASSOC WITH CACHE MEMORY	24	4	UN10	J1C176B-1, L24
		4	OMIT UN10	J1C176B-1, L24
		2	UN11	J1C176B-1, L24
		2	OMIT UN11	J1C176B-1, L24
FAST BACKUP CAPABILITY	25	1	J1C173B-1, L7	J1C176B-1, L25
STORE ADDRESS TRANSLATOR (SAT) (UN45C)	26	1	J1C173B-1, L8	J1C176B-1, L26
BACKPLANE WIRING FOR IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE, UN45C AND UNIX RTR RELEASE 1 OR LATER SOFTWARE REQUIRED	27	1	J1C173B-1, L9	J1C176B-1, L27
SYSTEM SANITY ALARM FEATURE	30		J1C173B-1, L	J1C176B-1, L
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	100	1	UN33B	J1C176B-1, L100
2 CHANNEL (2 PORTS PER CH) TTY ASYNCHRONOUS LINK PC (SEE NOTE 306)	101	1	MC4C011A1B (TN74B) CP	J1C176B-1, L101
2 CHANNEL SYNCHRONOUS LINK PC (SEE NOTE 306)	102	1	MC4C048A1B (TN75C) CP	J1C176B-1, L102
NOT USED	103			
1600 BPI HIGH SPEED TAPE PC (SEE NOTE 306)	104	1	UN52 CP	J1C176B-1, L104
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	105	1	MC4C052A1 (TN82)	J1C176B-1, L105
1600/6250 BPI HIGH SPEED TAPE PC (SEE NOTE 306)	106	1	MC4C160A1 (UN145)	J1C176B-1, L106
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	107	1	UN33C	J1C176B-1, L107
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	108	1	MC4C052A1C (TN82B) CP	J1C176B-1, L108
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	109	1	MC4C052A1D (TN82B) CP	J1C176B-1, L109
SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)	110	1	UN33D	J1C176B-1, L110
1600 BPI HIGH SPEED TAPE (SEE NOTE 306)	111	1	UN52B	J1C176B-1, L111
BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	112	1	MC4C052A1E (TN82B) CP	J1C176B-1, L112
2 CHANNEL SYNCHRONOUS LINK (TN1839-NSI APPLICATION ONLY) (SEE NOTE 306)	113	1	TN1839	J1C176B-1, L113
BX.25 HIGH SPEED (64 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)	114	1	TN1420	J1C176B-1, L114

3B20D MODEL 3

FEATURE OR OPTION			APP FIG.	QTY	CABINET DATA	SYSTEM DATA
1ST TAPE/DISK CABINET ARRANGED FOR 1ST TAPE UNIT, & 4 DISK DRIVES & PWR DIST UNIT T/DC (0)	CABINET ASSY		200	1	J1C192A-1, L1 & L6	J1C176B-1, L200
	340 MB DISK DRIVES (KS-22875,L14Q L18 E/W L51	NO:				
		00	200	1	J1C192A-1, L2	J1C176B-1, L200
		01	200	1	J1C192A-1, L2A	J1C176B-1, L200
		02	250	1	J1C192A-1, L3	J1C176B-1, L250
		03	251	1	J1C192A-1, L3A	J1C176B-1, L251
	PWR DIST UNIT		200	1	J1C192A-1, L5A	J1C176B-1, L200
	TAPE UNIT KS-22762, L3		220	1	J1C192A-1, L4 & L7 (MFR DISC)	J1C176B-1, L220 (MFR DISC)
	TAPE UNIT KS-23112, L14		222	1	J1C192A-1, L4A & L7A	J1C176B-1, L222
	50HZ TAPE UNIT T/DC (0)		224	1	J1C192A-1, L4B,7A	J1C176B-1, L224
				1	J1C173B-1, LK	
	60 HZ KEYSTONE III TAPE UNIT T/DC (0)		229	1	J1C192A-1, L4D,7A	J1C176B-1, L226
				1	J1C173B-1, LK	
	50 HZ KEYSTONE III TAPE UNIT T/DC (0)		228	1	J1C192A-1, L4E,7A	J1C176B-1, L228
				1	J1C173B-1, LK	
1ST GROWTH TAPE/DISK CABINET ARRANGED FOR 2ND TAPE UNIT & 4 DISK DRIVES (04-07) & PWR DIST UNIT OR ARRANGED FOR 8 DISK DRIVES (04-11) & PWR DIST UNIT T/DC (1)	CABINET ASSY		201	1	J1C192A-1, L1	J1C176B-1, L201
	340 MB DISK DRIVES (KS-22875,L14Q L18 E/W L51	NO:				
		04	252	1	J1C192A-1, L1 & L6	J1C176B-1, L252
		05	253	1	J1C192A-1, L2A & L6	J1C176B-1, L253
		06	254	1	J1C192A-1, L3	J1C176B-1, L254
		07	255	1	J1C192A-1, L3A	J1C176B-1, L255
		08	256	1	J1C192A-1, L2B & L6	J1C176B-1, L256
		09	258	1	J1C192A-1, L2C & L6	J1C176B-1, L258
		10	260	1	J1C192A-1, L3B	J1C176B-1, L260
		11	262	1	J1C192A-1, L3C	J1C176B-1, L262
	PWR DIST UNIT (4 DRIVES)		211	1	J1C192A-1, L5A	J1C176B-1, L211
	PWR DIST UNIT (8 DRIVES)		210	1	J1C192A-1, L5	J1C176B-1, L210
	TAPE UNIT KS-22762, L3		221	1	J1C192A-1, L4 & L7 (MFR DISC)	J1C176B-1, L221 (MFR DISC)
	TAPE UNIT KS-23113, L14		223	1	J1C192A-1, L4A & L7A	J1C176B-1, L223
	50HZ TAPE UNIT T/DC (0)		225	1	J1C192A-1, L4B,7A	J1C176B-1, L225
	60 HZ KEYSTONE III TAPE UNIT T/DC (0)		227	1	J1C192A-1, L4D,7A	J1C176B-1, L227
	50 HZ KEYSTONE III TAPE UNIT T/DC (0)		229	1	J1C192A-1, L4E,7A	J1C176B-1, L229

PROCESSOR SYSTEM CABINET

DWG SIZE C2 | ISSUE 10B | SHEET C2

AT&T | SD-4C122-01

APP FIG SUMMARY

(SEE ALSO NOTE 302)

3B20D MODEL 3

FEATURE OR OPTION			APP FIG.	QTY	CABINET DATA	SYSTEM DATA
2ND GROWTH TAPE/DISK CABINET ARRANGED FOR 4 DISK DRIVES (12-15) & PWR DIST UNIT OR ARRANGED FOR 8 DISK DRIVES (08-15) & PWR DIST UNIT	CABINET ASSY		202	1	J1C192A-1, L1	J1C176B-1, L202
	340 MB DISK DRIVES (KS-22875,ECG. L18 E/W L51	NO:				
		12	264	1	J1C192A-1, L2 & L6	J1C176B-1, L264
		13	266	1	J1C192A-1, L2A & L6	J1C176B-1, L266
		14	268	1	J1C192A-1, L3	J1C176B-1, L268
		15	270	1	J1C192A-1, L3A	J1C176B-1, L270
		08	257	1	J1C192A-1, L2 & L6	J1C176B-1, L257
		09	259	1	J1C192A-1, L2A & L6	J1C176B-1, L259
		10	261	1	J1C192A-1, L3	J1C176B-1, L261
		11	263	1	J1C192A-1, L3A	J1C176B-1, L263
		12	265	1	J1C192A-1, L2B & L6	J1C176B-1, L265
		13	267	1	J1C192A-1, L2C & L6	J1C176B-1, L267
		14	269	1	J1C192A-1, L3B	J1C176B-1, L269
		15	271	1	J1C192A-1, L3C	J1C176B-1, L271
	PWR DIST UNIT (4 DRIVES)		211	1	J1C192A-1, L5A	J1C176B-1, L211
	PWR DIST UNIT (8 DRIVES)		210	1	J1C192A-1, L5	J1C176B-1, L210
CABLE & DIODES FOR MAIN STORE MEMORY			A	1	J1C173B-1, LA	J1C176B-1, LA
WIRING FOR UN33B			B	1	J1C173B-1, LE	J1C176B-1, LB
POWER STRAP			C	1	J1C173B-1, LG	J1C176B-1, LC
625F JACK			D			
MEMORY EXPANSION			E	1	J1C173B-1, LD	J1C176B-1, LE
PROVIDE TN19 MC4C127A1B			H	1	J1C173B-1, LL	J1C176B-1, LH
PERIPHERAL INTERFACE CABINET PICO ARRANGED FOR 2 IOP BASIC UNITS, 2 IOP GROWTH UNITS, PWR DIST UNIT AND COOLING UNIT	CABINET PWR DIST UNIT, COOLING UNIT AND 1ST IOP BASIC UNIT		401	1	J1C215A-1, L1	J1C176B-1, L401
	1ST IOP GROWTH UNIT		402	1	J1C215A-1, L2	J1C176B-1, L402
	2ND IOP BASIC UNIT		403	1	J1C215A-1, L3	J1C176B-1, L403
	2ND IOP GROWTH UNIT		404	1	J1C215A-1, L4	J1C176B-1, L404

3B20D MODEL 3

FEATURE OR OPTION		APP FIG.	QTY	CABINET/UNIT EQPT DATA	SYSTEM EQPT DATA
PERIPHERAL INTERFACE CABINET, PICO ARRANGED FOR 2 IOP BASIC UNITS, 2 IOP GROWTH UNITS, PWR DIST UNIT AND COOLING UNIT	CABINET PWR DIST UNIT, COOLING UNIT AND 1ST IOP BASIC UNIT	451	1	J1C215B-1, L1	J1C176B-1, L451
	1ST IOP GROWTH UNIT	452	1	J1C215B-1, L2	J1C176B-1, L452
	2ND IOP BASIC UNIT	453	1	J1C215B-1, L3	J1C176B-1, L453
	2ND IOP GROWTH UNIT	454	1	J1C215B-1, L4	J1C176B-1, L454
VERY LARGE MAINSTORE MEMORY (VLMM)					
		501	1	J1C173B-1, L101 (UN133C & UN480)	J1C176B-1, L501
		502	1	J1C173B-1, L102 (UN608 MC3T001) (UN609 MC3T002)	J1C176B-1, L502
		503	1	J1C173B-1, L103 (UN248)	J1C176B-1, L503
	(VLMM NON-INTERFERING WIRING)	504	1	J1C173B-1, L104	J1C176B-1, L504
	(VLMM NON-INTERFERING WIRING)	505	1	J1C173B-1, L105	J1C176B-1, L505
		506	1		J1C176B-1, L506 (UN616, UN617)
		507	1	J1C173B-1, L107 (UN618)	J1C176B-1, L507
		508	1	J1C173B-1, L108 (1ST TN2012)	J1C176B-1, L508
		509	1		J1C176B-1, L509 (GROWTH TN2012)
		510	1	J1C173B-1, L110 (UN611, UN612 & UN288 /MC3T003A1)	J1C176B-1, L510
		511	1		J1C176B-1, L511 (UN615)
		512	1	J1C173B-1, LZ (UN3C)	J1C176B-1, LBA
		513		UNASSIGNED	UNASSIGNED
		514		UNASSIGNED	UNASSIGNED
	(ED-4C4561-45, G16 CABLE & R1 VLMM NON-INTERFERING WIRING)	515	1	J1C173B-1, L115	J1C176B-1, L515
	(R1 VLMM NON-INTERFERING WIRING)	516	1	J1C173B-1, L116	J1C176B-1, L516
		517		UNASSIGNED	UNASSIGNED
	(R6 VLMM INTERFERING WIRING)	518	1	J1C173B-1, L118	J1C176B-1, L518
		519	1	J1C173B-1, LWB	J1C176B-1, L519

PROCESSOR SYSTEM CABINET

DWG SIZE	ISSUE
C2	10B

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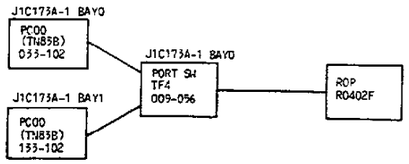
CIRCUIT NOTES:

101.

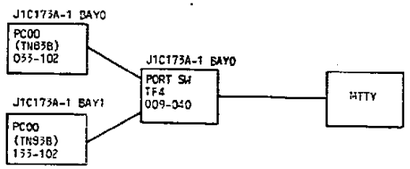
DESIG	FUSE AMP	POTENTIAL	ONE PER
BATTERY SYMBOL			VOLTAGE RANGE

102. TF4 CIRCUIT PACK RECEIVES CABLES FROM THE TN83B FOR CONNECTION TO THE MTTY AND READ ONLY PRINTER.



THE CONNECTION TO THE TF4 IS A 2 X 6 BACKPLANE CONNECTOR (982AC) WITH SWITCHBOARD CABLE TO A RS-232C INTERFACE AT THE ROP - CABLE DWG - ED-4C410-35.

LOCATION	LEAD NAME
009-056-213	ER
-214	RXDO
-314	CTSO
-215	DCDO
-315	DSRO
-216	RTSO
-316	TXDO
009-056-318	DTRO



102. (CONT)

THE CONNECTION TO THE TF4 IS A 2 X 6 BACKPLANE CONNECTOR (982 AC) WITH A SWITCHBOARD CABLE TO A RS-232C INTERFACE AT THE MTTY-CABLE DWG- ED-4C410-35.

LOCATION	LEAD NAME
009-040-213	ER
-214	RXDO
-314	CTSO
-215	DCDO
-315	DSRO
-216	RTSO
-316	TXDO
009-040-318	DTRO

102.1 TF2 CP'S ARE JOB (APPLICATION) ENGINEERED. THE CIRCUIT PACK PROVIDES OPTICALLY ISOLATED INPUTS FOR EXTERNAL SCAN FUNCTIONS AS WELL AS OPTICALLY ISOLATED OUTPUTS FOR EXTERNAL SIGNAL DISTRIBUTOR FUNCTIONS.

102.2 ADDITIONAL TF4 CP'S ARE JOB (APPLICATION) ENGINEERED. THIS APPLICATION PROVIDES FOR SWITCHING RS-232C TYPE INTERFACE.

102.3 5ESS APPLICATION ENGINEERED DUAL SERIAL CHANNEL CABLES SHALL CONFORM TO THE FOLLOWING RULES:

1. THE CABLE SHIELD SHALL BE GROUNDED AT BOTH ENDS FOR ALL CABLES GREATER THAN 10 FEET IN LENGTH.
2. CABLE ROUTING MUST BE WITHIN 12 INCHES OF THE INTER-AISLE GROUND CONNECTION.
3. DUAL SERIAL CHANNEL CABLES SHALL NOT EXCEED 100 FEET IN LENGTH.
4. NO CIRCUIT PACK MODIFICATIONS TO TN69B ARE REQUIRED.

103. THERE ARE TWO TYPES OF PERIPHERAL GROUNDING DESIGNS:

ISOLATED - WHERE THE PERIPHERAL CONTROLLER SEPERATES +5V LOGIC FROM EIA SIGNAL GROUND.

NON-ISOLATED - WHERE THE PERIPHERAL CONTROLLER CONNECTS +5V LOGIC GROUND TO EIA SIGNAL GROUND.

MIXING NON-ISOLATED AND ISOLATED PERIPHERAL CONTROLLERS IN THE SAME COMMUNITY SHOULD BE AVOIDED. IF THIS IS NOT POSSIBLE THE FOLLOWING GROUND SCHEMES SHOULD BE USED. PERIPHERAL DEVICES (TERMINALS, DATA SETS, PRINTERS ...ETC.) THAT ARE CABLED TO ISOLATED PERIPHERAL CONTROLLERS SHOULD NOT CONNECT AC GROUND TO EIA SIGNAL GROUND. THIS SCHEME INSURES THAT NO AC FAULT CURRENTS CAN ENTER THE ESS GROUND.

THE 3B200 MODEL 2 PROCESSOR IOP BASIC UNIT AND THE IOP GROWTH UNIT ARE ARRANGED AND WIRED FOR TWO COMMUNITIES OF FOUR PERIPHERAL CONTROLLERS EACH. PC 00-03 & 10-13 ARE LOCATED IN THE IOP BASIC UNIT; PC 20-23 & 30-33 ARE LOCATED IN THE IOP GROWTH UNIT. THE PERIPHERAL CONTROLLER CIRCUIT PACKS ARE JOB ENGINEERED PER USER REQUIREMENTS EXCEPT PC00 & 02.

FRAME #	SLOT #	LOCATION BAY 0	LOCATION BAY 1
J1C173A-1	PC00-03	033-102,094,086,078	133-102,094,086,078
J1C173A-1	PC10-13	033-062,054,046,038	133-062,054,046,038
J1C173A-1	PC20-23	042-080,074,068,062	142-080,074,068,062
J1C173A-1	PC30-33	042-046,040,054,028	142-046,040,054,028

THE FOLLOWING ARE FIXED ASSIGNED PERIPHERAL CONTROLLER CIRCUIT PACKS:

SLOT #	LOCATION	CODE	
00	033-102	MTTY - MC4CD41A18(TN83B)	NOTE 2
00	133-102	MTTY - MC4CD41A18(TN83B)	
02	033-086	SSD - UN53B	
02	133-086	SSD - UN53B	
05	033-078	TAPE CONTROLLER UN52	NOTE 1

NOTES: 1. WHEN THE J1C174A-1 TAPE CABINET IS USED, ITS PC(UN52) MUST BE LOCATED IN SLOT 03 OF BAY 0 DUE TO 20 FOOT CABLE LENGTH REQUIREMENT.

2. CONNECTION TO THE SCC COMES OF LOCATION 033-102-332 AND/OR 133-102-332. SEE NOTE 105 FOR LEAD NAMES.

104. 3B20D MODEL 2 & 3 UN52 HIGH SPEED TAPE CONTROLLER CIRCUIT PACK CONTAINS CIRCUITRY FOR READING, WRITING AND CONTROLLING UP TO FOUR TAPE TRANSPORTS. THIS PACK CONNECTS TO A TAPE TRANSPORT BY A FLAT RIBBON CABLE DESIGN WITH 3 BACKPLANE CONNECTORS ACCORDING TO TABLE BELOW. SIGNAL TYPE IS TTL. WITH A MAXIMUM CABLE LENGTH OF 20 FEET. THIS IS AN ISOLATED PC.

	5	4	3	2	1	0	
56							56
55			ØNL	ØNLG	CER	CERG	55
54	GØ	GØG	RWD	RWDG	HER	HERG	54
53	REW	REWG	FPT	FPTG	FMK	FMKG	53
52			LPW	LPWG	FEN	FENG	52
51			EOT	EOTG	DBY	DBYG	51
50	ERASE	ERASEG	REPØ	REPØG	SPD	SPDG	50
49	LØØP	LØØPG	WST	WSTG	IDEN	IDENG	49
48	WFM	WFMG	RST	RSTG	RDY	RDYG	48
47	WRT	WRTG	FADø	FADG	SPDEN	SPDENG	47
46	REV	REVG	TAD1	TAD1G	ØFL	ØFLG	46
45							45
44							44
43			R0	R0G	W0	W0G	43
42			R1	R1G	W1	W1G	42
41					W2	W2G	41
40					W3	W3G	40
39	R2	R2G	R4	R4G	W4	W4G	39
38	R3	R3G	R5	R5G	W5	W5G	38
37	FBY	FBYG	R6	R6G	W6	W6G	37
36	TAD0	TAD0G	R7	R7G	W7	W7G	36
35	LØL	LØLG	RP	RPG	WP	WPG	35
34	SENSE	SENSEG			LWD	LWDG	34
33							33
32							32
	5	4	3	2	1	0	

104.1 SEE NOTE 308 FOR TYPICAL CABLING INFO (ED-4C410-35).

104.2 3B20D MODEL 3 MC4C160A1 (UN145) DUAL DENSITY STREAMING TAPE CONTROLLER CIRCUIT PACK IS USED WITH KS-23113,L14 RECORDER (TAPE UNIT, 6250 BPI/1600 BPI, 25/75 IPS, STREAMER) AND CONTAINS CIRCUITRY FOR READING, WRITING, AND CONTROLLING UP TO FOUR TAPE UNITS. THE MAXIMUM CABLE LENGTH IS 20 FEET.

104.3 SEE NOTE 312 FOR TYPICAL CABLING INFORMATION (ED-4C508-40 & ED-4C564-30).

104.4 A SET OF BUS TERMINATING RESISTORS (BTRS) ED-3T035-30 ARE TO BE INSTALLED IN BOTH UNBUFFERED KIII STU WHEN TWO UNBUFFERED KIII,S ARE DAISY-CHAINED FROM ONE TAPE CONTROLLER MC4C160A1 (UN145). MANUFACTURER,S SET OF BTR'S IC'S MUST BE REMOVED FROM THE STANDARD INTERFACE PWA BOARD ON EACH OF THE TWO UNBUFFERED KIII STU DRIVES. WHEN MORE THAN TWO UNBUFFERED KIII STU'S ARE DAISY-CHAINED FROM ONE TAPE CONTROLLER, THEN IT IS NECESSARY TO INSTALL A BTR SET INTO THE LAST TWO (TAPE DRIVES-2,3) DRIVES.
TERMINATORS- THE BTR TERMINATION FOR THE BALANCED BUFFERED KIII STU IS IDENTICAL TO THAT DESCRIBED FOR THE UNBUFFERED KIII STU.

PROCESSOR SYSTEM CABINET	DWG SIZE: B5	ISSUE: 9B
AT&T	SD-4C122-01	SHEET: D1

CIRCUIT NOTES:(CONT)

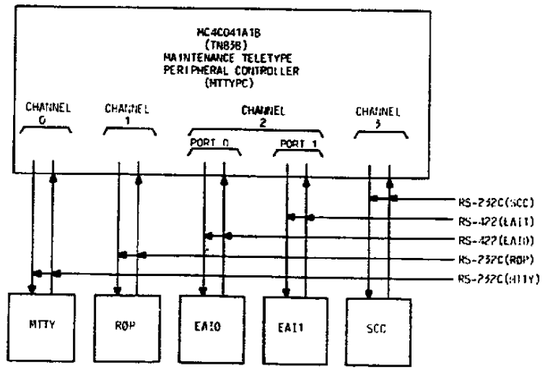
105. MC4C041AB1(TN838) MAINTENANCE TTY PERIPHERAL CONTROLLER CIRCUIT PACK

THIS PACK INTERFACES THE MAINTENANCE TERMINAL TO THE 3B 10P AND IS ALWAYS REQUIRES IN PC 00 FOR 3B DUPLEX APPLICATIONS. THIS CIRCUIT CAN ACCOMODATE ONE TTY VIDEO TERMINAL, ONE TTY PRINTER AND CONNECT TO THE SCC VIA A DATA SET. IT ALSO PROVIDES ACCESS. VIA THE EAI (TN10). TO CCO AND CC1 AND CONNECTED TO THE POWER CONVERTER PACK (TN9) IN THE 10P. TERMINAL INTERFACES CONFORM TO STANDARD RS-232C CONNECTION TO A PERIPHERAL DEVICE FROM THE TN838 EITHER DIRECTLY OR VIA THE PORT SWITCH UNIT. THIS IS AN ISOLATED PC.

CONNECTION TO A PERIPHERAL DEVICE IS LIMITED TO:

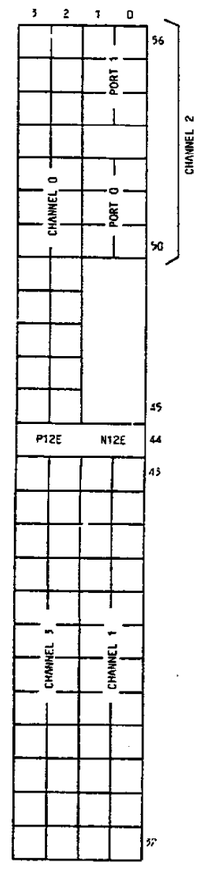
PERIPHERAL DEVICE	CABLE LENGTH RESTRICTIONS	GRP NO.
VIDEO TERMINAL KS-22921	120 FEET VIA PORT SWITCH	*G30
R0P		*G32
SCC	50 FEET	*G35
VIDEO TERMINAL KS-22921	250 FEET	*G44
R0P		*G45

* ED-4C258-30.G__ OR EQUIVALANT



105. (CONT)
MC4C041A1B (TN838) CONNECTIONS, CHANNEL/PORT ASSIGNMENTS:
(CAD'S CAN BE FOUND IN SD-4C119-01 PROC CONT CABINET)

3	2	1	0	
		EAIORXDO	EAIOPXD1	56
CRTDTRO		EAIOTXDO	EAIOTXD1	55
	P12REFA	EAIOSPOO	EAIOSPO1	54
CRTTXDO		GRD	GRD	53
CRTDCDO	CRTRTSO	EAI1RXDO	EAI1RXD1	52
CRTDSRO		EAI1TXDO	EAI1TXD1	51
CRTRXDO		EAI1SPOO	EAI1SPO1	50
CRTCTSO		GRD	GRD	49
		RETALMO	500HSCKO	48
			TINC31	47
		TØUTC41	TINC21	46
ER	ER	ER	ER	45
P12E	P12E	N12C	N12E	44
	SCCTXCO			43
SCCDRTO		PRTDTRO		42
	P12REFB		P12REFC	41
SCCTXDO	SCCRXCO	PRTTXDO		40
SCCDCDO	SCCRTSO	PRTDCDO	PRTRTSO	39
SCCDSRO		PRTDSRO		38
SCCRXDO		PRTRXDO		37
SCCCTSO		PRTCTSO		36
				35
				34
				33
ER	ER	ER	ER	32
3	2	1	0	



PROCESSOR SYSTEM CABINET		DWG SIZE: B5	ISSUE: 9B
AT&T	SD-4C122-01		SHEET: D2

CIRCUIT NOTES: (CONT)

106. UN33B SCANNER/SIGNAL DISTRIBUTOR CONTROLLER CIRCUIT PACK

CONSISTS OF CIRCUITRY FOR MONITORING 48 SCAN POINTS AND CONTROLLING 32 DISTRIBUTE POINTS. CONNECTION TO OTHER CIRCUITS IS LIMITED TO 1000 FEET. CONNECTION FROM THE UN33B TO NON-3B S/D POINTS SHOULD BE MADE THROUGH AN S/D INTERFACE CIRCUIT PACK (TF2) IN THE PORT SWITCH UNIT. THIS IS AN ISOLATED PC.

PC UN33B SCSD TERMINAL STRIP CABLING OPTIONS:

2 SC POINTS, 2 SCAN POINTS
USE 2X4 982AB

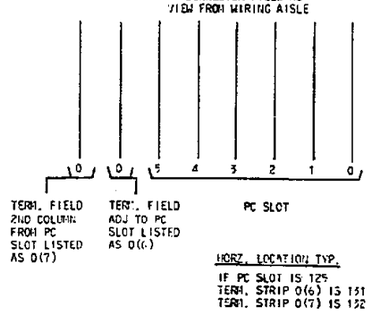
133,533,533,0(7)33
137,337,537,0(7)37
146,346,546,0(7)46
150,350,550,0(7)50

2 SD POINTS, 4 SCAN POINTS
USE 2X6 982AC
157,357,557,0(7)57
150,350,550,0(7)50

4 SCAN POINTS
USE 2X4 982AB

139,339,539,0(7)39
152,352,552,0(7)52

NUMBERING OF PC SLOTS



HORZ. LOCATION TYP.

IF PC SLOT IS 125
TERM. STRIP 0(6) IS 151
TERM. STRIP 0(7) IS 152

106. (CONT)
THE FOLLOWING SD-SCAN POINTS ARE NOT FIXED FOR 3B20D MODEL 2 OPERATION - CODE UN33B.

J1C176A-1

BAY 0

	0	0	5	4	3	2	1	0	
56									56
55	SC47P	SC47N	SC45P	SC45N			SC35P	SC35N	55
54	SC46P	SC46N	SC42P	SC42N			SC34P	SC34N	54
53									53
52					SC14P	SC14N			52
51					SD15P	SD15N			51
50					SD14P	SD14N			50
49									49
48									48
47									47
46									46
45									45
44									44
43									43
42			SC41P	SC41N			SC33P	SC33N	42
41			SC40P	SC40N			SC32P	SC32N	41
40	SC27P	SC27N							40
39	SC26P	SC26N			SC10P	SC10N			39
38	SD27P	SD27N			SD11P	SD11N			38
37	SD26P	SD26N			SD10P	SD10N			37
36	SC25P	SC25N					SC01P	SC01N	36
35	SC24P	SC24N					SC00P	SC00N	35
34	SD25P	SD25N					SD01P	SD01N	34
33	SD24P	SD24N					SD00P	SD00N	33
32									32
	0(7)	0(6)	5	4	3	2	1	0	

FRAME EQL -033-086
TERM FIELD 0(6)-033-092
0(7)-033-093

BAY 1

	0	0	5	4	3	2	1	0	
56									56
55	SC47P	SC47N	SC45P	SC45N	SC39P	SC39N	SC35P	SC35N	55
54	SC46P	SC46N	SC42P	SC42N	SC38P	SC38N	SC34P	SC34N	54
53							SC07P	SC07N	53
52							SC06P	SC06N	52
51							SD07P	SD07N	51
50							SD06P	SD06N	50
49									49
48									48
47									47
46									46
45									45
44									44
43									43
42	SC45P	SC45N	SC41P	SC41N	SC37P	SC37N	SC33P	SC33N	42
41	SC44P	SC44N	SC40P	SC40N	SC36P	SC36N	SC32P	SC32N	41
40			SC19P	SC19N			SC03P	SC03N	40
39			SC18P	SC18N			SC02P	SC02N	39
38			SD19P	SD19N			SD03P	SD03N	38
37			SD18P	SD18N			SD02P	SD02N	37
36			SD17P	SD17N	SC09P	SC09N			36
35			SC16P	SC16N	SC08P	SC08N			35
34			SD17P	SD17N	SD09P	SD09N			34
33			SD16P	SD16N	SC08P	SD08N			33
32									32
	0(7)	0(6)	5	4	3	2	1	0	

FRAME EQL -133-086
TERM FIELD 0(6)-133-092
0(7)-133-093

PROCESSOR SYSTEM CABINET	DWG SIZE 65	ISSUE 9B
AT&T SD-4C122-01		SHEET D3

CIRCUIT NOTES: (CONT)

107. HC4C011A1B (TN74B) ASYNCHRONOUS PERIPHERAL CONTROLLER

PROVIDES ASYNCHRONOUS HALF, FULL DUPLEX. TWO CHANNELS. PORT 0 EACH, WITH ISOLATED RS-232C INTERFACE COVERS ALL PRIVATE LINE (FULL, HALF DUPLEX) AND FULL DUPLEX DDD LEADS.

ASCII IN/OUT BINARY IN/OUT STANDARD DATA RATES TO 9.6K TOTAL BIT RATE BOTH CHANNELS, BOTH DIRECTIONS NOT TO EXCEED 20K BPS (HALF, FULL DUPLEX): THIS IS AN ISOLATED PC.

PERIPHERAL DEVICE		MAX CA LENGTH	CRP NO.
RS-232C INTERFACE	PORT 0 - VT100 (RC&V) PORT 1 - RØP	100 FEET	*G22
	PORT 0 - 202T OR 212A DS	100 FEET	*G23
	PORT 0 - CDI, MOD. 1203 TERM.	100 FEET	*G24

* SEE ED-4C258-30,C__ OR EQUIVALENT

107. (CONT)

HC4C011A1B (TN74B) BACKPLANE CONNECTIONS FOR FULL DUPLEX DATA SETS ARE:

BACKPLANE								202T PRIVATE LINE OR 212A DDD	
CHANNEL 0				CHANNEL 1					
PORT 0		PORT 1		PORT 0		PORT 1		EIA - NOTES 1,2 & 5	
PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	DESIG	PIN
355	DTR 00P0	155	DTR 01P0	342	DTR 10P0	142	DTR 11P0	CD	20
255	SCA 00P0	055	SCA 01P0	242	SCA 10P0	042	SCA 11P0		
254	EPULL 000	054	EPULL 010	241	EPULL 100	041	EPULL 110		
353	BA 00P1	153	BA 01P1	340	BA 10P1	140	BA 11P1	BA	2
352	CFR 00P0	152	CFR 01P0	339	CFR 10P0	139	CFR 11P0	CF	8
252	RTS 00P0	052	RTS 01P0	239	RTS 10P0	039	RTS 11P0		
351	CCR 00P1	151	CCR 01P1	338	CCR 10P1	138	CCR 11P1	CC	6
251	CER 00P0	051	CER 01P0	238	CER 10P0	038	CER 11P0		
350	BBRC 00P1	150	BBRC 01P1	337	BBRC 10P1	137	BBRC 11P1	BB	3
349	CBR 00P0	149	CBR 01P0	336	CBR 10P0	136	CBR 11P0		
348	SCFR 00P0	148	SCFR 01P0	335	SCFR 10P0	135	SCFR 11P0		
345	ER8	145	ER4	332	ER7	132	ER5	AB	7
245	ER6	045	ER2	232	ER5	032	ER1		

HC4C011A1B (TN74B) BACKPLANE CONNECTIONS FOR A MODEL 40/2 TTY ARE:

BACKPLANE								MODEL 40/2 TELETYPE	
CHANNEL 0				CHANNEL 1					
PORT 0		PORT 1		PORT 0		PORT 1		EIA-NOTES 1,2,3,4&6	
PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	DESIG	PIN
355	DTR 00P0	155	DTR 01P0	342	DTR 10P0	142	DTR 11P0	CC	6
255	SCA 00P0	055	SCA 01P0	242	SCA 10P0	042	SCA 11P0		
254	EPULL 000	054	EPULL 010	241	EPULL 100	041	EPULL 110		
353	BA 00P1	153	BA 01P1	340	BA 10P1	140	BA 11P1	BA	3
352	CFR 00P0	152	CFR 01P0	339	CFR 10P0	139	CFR 11P0		
252	RTS 00P0	052	RTS 01P0	239	RTS 10P0	039	RTS 11P0		
351	CCR 00P1	151	CCR 01P1	338	CCR 10P1	138	CCR 11P1	CD	20
251	CER 00P0	051	CER 01P0	238	CER 10P0	038	CER 11P0		
350	BBRC 00P1	150	BBRC 01P1	337	BBRC 10P1	137	BBRC 11P1	BA	2
349	CBR 00P0	149	CBR 01P0	336	CBR 10P0	136	CBR 11P0		
348	SCFR 00P0	148	SCFR 01P0	335	SCFR 10P0	135	SCFR 11P0		
345	ER8	145	ER4	332	ER7	132	ER5	AB	7
245	ER6	045	ER2	232	ER5	032	ER1		

107. (CONT)

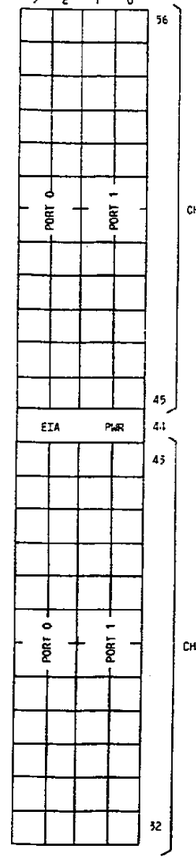
HC4C011A1B (TN74B) BACKPLANE CONNECTIONS FOR A MODEL 1203 CDI ARE:

BACKPLANE								COMPUTER DEVICES INC. MODEL 1203	
CHANNEL 0				CHANNEL 1					
PORT 0		PORT 1		PORT 0		PORT 1		EIA-NOTES 1,5	
PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	PIN	TERM MOD	DESIG	PIN
355	DTR 00P0	155	DTR 01P0	342	DTR 10P0	142	DTR 11P0		
255	SCA 00P0	055	SCA 01P0	242	SCA 10P0	042	SCA 11P0		
254	EPULL 000	054	EPULL 010	241	EPULL 100	041	EPULL 110		
353	BA 00P1	153	BA 01P1	340	BA 10P1	140	BA 11P1	BA	2
352	CFR 00P0	152	CFR 01P0	339	CFR 10P0	139	CFR 11P0		
252	RTS 00P0	052	RTS 01P0	239	RTS 10P0	039	RTS 11P0		
351	CCR 00P1	151	CCR 01P1	338	CCR 10P1	138	CCR 11P1	CC	6
251	CER 00P0	051	CER 01P0	238	CER 10P0	038	CER 11P0		
350	BBRC 00P1	150	BBRC 01P1	337	BBRC 10P1	137	BBRC 11P1	BB	3
349	CBR 00P0	149	CBR 01P0	336	CBR 10P0	136	CBR 11P0		
348	SCFR 00P0	148	SCFR 01P0	335	SCFR 10P0	135	SCFR 11P0		
345	ER8	145	ER4	332	ER7	132	ER5	AB	7
245	ER6	045	ER2	232	ER5	032	ER1		

NOTES: (X= CHANNEL, Y=PORT)

1. STRAP RTSXYP0 TO CBRXYP0 ON BACKPLANE CONNECTOR.
2. STRAP CA (PIN 4) TO CB (PIN 5) OF EIA CONNECTOR.
3. STRAP CFRXYP0 TO CCRXYP1 ON BACKPLANE CONNECTOR.
4. STRAP CF (PIN 8) TO CC (PIN 6) OF EIA CONNECTOR.
5. PRIVATE LINE FAR END DATA SET MUST BE SWITCHED TO CONTINOUS CARRIER FOR DETECTION OF TERMINAL POWER OFF.
6. THESE CONNECTIONS ALSO APPLY FOR VT100, VT52, (DIGITAL EQUIPMENT CORP.) AND ADM-3A (LEAR-SIEGLER) TERMINALS. SEE TERMINAL SPECS. FOR TYPE OF TERMINAL CONNECTOR.

HC4C11A1B (TN74B) BACKPLANE PORT CONNECTIONS ARE:



PROCESSOR SYSTEM CABINET

DWG SIZE: B5

ISSUE: 9B

AT&T

SD-4C122-01

SHEET: D4

CIRCUIT NOTES: (CONT)

107. (CONT)

VT 100-AA
VT XXX-AB
DEC OR RØP 40

TO AC PWR: ±120V AC, GRD, N

VIDIO TERM COMM: 20, 8, 7, 6, 5, 4, 3, 2

ED-4C258-30.G22

TERM OR RØP

DF, BK, DE, AJ, AA, BA, BH, HJ

HC4C011A1B(TN74B)

FIGURE A
(TYPICAL)

47D2 DM E/W
212A DS

TO AC PWR: ±120V AC, GRD, N

T → 7, R → 8

ED-4C258-30.G23

DF: T, R, GRD

DDD NET

RC/VFY DIAL UP DS

AJ, HE, HH, AJ, AA, BA, HH, DF, BK

HC4C011A1B(TN74B)

FIGURE B
(TYPICAL)

107. (CONT)

THE FOLLOWING SHOWS THE GRAPHICAL REPRESENTATION OF THE (ED-4C258-30.G OR EQUIVALENT) CONNECTION BETWEEN HC4C011A1B(TN74B) AND EIA RS-232C INTERFACE.

TO VIDEO TERM, 212A DATA SET OR CDI 1205 TERM (FIG. A OR D)

PORT 0, CH 0
HC4C011A1B(TN74B)
JACK/CP

ER5	(982AE)	245	ER5
	NC	246–250	NC
CER00P0		251	CER00P0
RTS00P0		252	RTS00P0
	NC	253	NC
EPULL000		254	EPULL000
SCA00P0		255	SCA00P0
	NC	256	NC
ER8		345	ER8
	NC	346	NC
	NC	347	NC
SCFR00P0		348	SCFR00P0
CBR00P0		349	CBR00P0
BBRC00P1		350	BBRC00P1
CCR00P0		351	CCR00P0
CFR00P0		352	CFR00P0
BA00P1		353	BA00P1
	NC	354	NC
DTR00P0		355	DTR00P0
	NC	356	NC

(CA)
ED-4C258-30.G22. G23 OR G24
OR
EQUIVALENT

TO RØP (FIG. A)

PORT 1, CH 0
HC4C011A1B(TN74B)
JACK/CP

ER2	(982AE)	045	ER2
	NC	046–050	NC
CER01P0		051	CER01P0
RTS01P0		052	RTS01P0
	NC	053	NC
EPULL010		054	EPULL010
SCA01P0		055	SCA01P0
	NC	056	NC
ER4		145	ER4
	NC	146	NC
	NC	147	NC
SCFR01P0		148	SCFR01P0
CBR01P0		149	CBR01P0
BBRC01P1		150	BBRC01P1
CCR01P0		151	CCR01P0
CFR01P0		152	CFR01P0
BA01P1		153	BA01P1
	NC	154	NC
DTR01P0		155	DTR01P0
	NC	156	NC

(CA)
ED-4C258-30.G22
OR
EQUIVALENT

HC4C011A1B
(TN74B)
ASYNCHRONOUS
DATA LINK
CONTROLLER

CHANNEL 0: PORT 0, PORT 1
CHANNEL 1: PORT 0, PORT 1

RS-232C INTERFACE (FIGS. A, B OR EQUIV)

TO VIDEO TERM, 212A DATA SET OR CDI 1205 TERM (FIG. A OR D)

PORT 0, CH 1
HC4C011A1B(TN74B)
JACK/CP

ER5	(982AE)	232	ER5
	NC	233–237	NC
CER10P0		258	CER10P0
RTS10P0		259	RTS10P0
	NC	240	NC
EPULL100		241	EPULL100
SCA10P0		242	SCA10P0
	NC	243	NC
ER7		332	ER7
	NC	333	NC
	NC	334	NC
SCFR10P0		335	SCFR10P0
CBR10P0		336	CBR10P0
BBRC10P1		337	BBRC10P1
CCR10P0		338	CCR10P0
CFR10P0		339	CFR10P0
BA10P1		340	BA10P1
	NC	341	NC
DTR10P0		342	DTR10P0
	NC	343	NC

(CA)
ED-4C258-30.G22. G23 OR G24
OR
EQUIVALENT

TO RØP (FIG. A)

PORT 1, CH 1
HC4C011A1B(TN74B)
JACK/CP

ER1	(982AE)	032	ER1
	NC	033–037	NC
CER11P0		058	CER11P0
RTS11P0		059	RTS11P0
	NC	040	NC
EPULL110		041	EPULL110
SCA11P0		042	SCA11P0
	NC	043	NC
ER3		132	ER3
	NC	133	NC
	NC	134	NC
SCFR11P0		135	SCFR11P0
CBR11P0		136	CBR11P0
BBRC11P1		137	BBRC11P1
CCR11P0		138	CCR11P0
CFR11P0		139	CFR11P0
BA11P1		140	BA11P1
	NC	141	NC
DTR11P0		142	DTR11P0
	NC	143	NC

(CA)
ED-4C258-30.G22
OR
EQUIVALENT

PROCESSOR SYSTEM CABINET		DWG SIZE: 8S	ISSUE: 9B
AT&T	SD-4C122-01		SHEET: D5

CIRCUIT NOTES: (CONT)

108. MC4C048A1B (TN75C/TN1839) SYNCHRONOUS DATA LINK PERIPHERAL CONTROLLER.

THIS CIRCUIT PACK RESIDES IN AN INPUT/OUTPUT PROCESSOR AND IS A BX.25, LEVEL 2, SYNCHRONOUS LINK PERIPHERAL CONTROLLER ARRANGED FOR FULL DUPLEX PRIVATE LINE DIAL BACKUP OPERATION. IT PROVIDES TWO INDEPENDENT CHANNELS.

CHANNEL 0 SUPPLIES AN AUTOMATIC CALL UNIT (ACU) PORT FOR DIAL BACKUP OPERATIONS. THIS ACU IS RS-366 COMPATIBLE. THIS CONFIGURATION IS SUPPORTED WHEN EQUIPPED WITH DATAPHONE II GENERATION DATA SETS.

MC4C048A1B (TN75C) CAPACITY IS 9600 BPS FULL DUPLEX. A TYPICAL CONFIGURATION WOULD BE ONE CHANNEL OPERATING AT 9600 BPS OR TWO CHANNELS OPERATING AT 4800 BPS.

EACH OF THE TWO BX.25, LEVEL CHANNELS HAS AN ASSOCIATED LEVEL 1 INTERFACE THAT IS RS-232C/RS-449 (BALANCED) COMPATIBLE. THIS IS A NON-ISOLATED PC.

THE TN1839 IS EQUIVALENT TO THE TN75C BUT IS ONLY USED IN NSI APPLICATIONS. IT HAS A FASTER SLEW RATE ON TIMING AND INTERCHANGE CIRCUITS TO MEET THE NET 2 INTERNATIONAL INTERFACE STANDARDS.

CONNECTION TO A PERIPHERAL DEVICE IS LIMITED TO:

PERIPHERAL DEVICE	CABLE LENGTH RESTRICTION	GRP NO.
RS-232C	50 FEET	*G39
RS-449(BAL.)	200 FEET	*G40 OR G43
RS-336A(ACU)	50 FEET	*G47

* ED-4C258-30,G_ _ OR EQUIVALENT

MC4C048A1B (TN75C/TN1839) DATA RATES ARE:

SPEED BPS	1200	2400	4800	9600
PRIVATE LINE FDX DATA SET		201C	208A	209A
DIAL-UP FDX	212A			
DATAPHONE II TYPE DATA-SET		2024A	2028A	2096A

108. (CONT)

MC4C048A1B (TN75C/TN1839) BACKPLANE CONNECTIONS FOR A EIA RS-232C DATA SET (201C,2034,209A, 212A) ARE:

BACKPLANE CONNECTION PORT 0 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
056	RR0S0		CF	8
055	CS0S0		CB	5
054	DM0S0		CC	6
053	RS0S0		CA	4
050	TR0S0		CD	20
049	RT0S0		DD	17
048	ST0S0		DB	15
047	RD0S1	NOTE A	BB	3
046	SD0S1	NOTE A	BA	2
045	GRD		AB	7
156	RR0R1	NOTE B		
155	CS0R1	NOTE B		
154	DM0R1	NOTE B		
152		NOTE B		
151	GRD			
150	GRD	NOTE C		
149	RT0R1	NOTE B		
148	ST0R1	NOTE B		
147	RD0R0	NOTE B		
146	GRD	NOTE B		
145	GRD			

BACKPLANE CONNECTION PORT 1 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
043	RR1S0		CF	8
042	CS1S0		CB	5
041	DM1S0		CC	6
040				
039				
038	RS1S0		CA	4
037	TR1S0		CD	20
036	RT1S0		DD	17
035	ST1S0		DB	15
034	RD1S1	NOTE A	BB	3
033	SD1S1	NOTE A	BA	2
032	GRD		AB	7
143	RR1R1	NOTE B		
142	CS1R1	NOTE B		
141	DM1R1	NOTE B		
140				
139				
138	GRD			
137	GRD			
136	RT1R1	NOTE B		
135	ST1R1	NOTE B		
134	RD1R0	NOTE B		
133	GRD	NOTE B		
132	GRD			

NOTES:

A. PAIRED WITH GROUND WIRE FROM TERMINAL 045.

B. STRAPPED TO GROUND ON BACKPLANE CONNECTOR PIN 146.

C. PIN 150 IS NOT USED FOR 208A-L1B DATA SET.

103. (CONT)

MC4C048A1B (TN75C/TN1839) BACKPLANE CONNECTIONS FOR A RS-449 (BALANCED) DATA SET (2024,2048,2096) ARE:

BACKPLANE CONNECTION PORT 0, POS. 1 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
056	RR0S0		RR	13
055	CS0S0		CS	9
054	DM0S0		DM	11
053				
052	SB0S0		SB	36
051	RS0S0		RS	7
050	TR0S0		TR	12
049	RT0S0		RT	8
048	ST0S0		ST	5
047	RD0S1		RD	6
046				
045	GRD		SC	37
156	RR0R1		-RR-	31
155	CS0R1		-CS-	27
154	DM0R1		-DM-	29
153				
152	RC		RC	20
151	GRD		-RS-	25
150	GRD		-TR-	30
149	RT0R1		-RT-	26
148	ST0R1		-ST-	23
147	RD0R0		-RD-	24
146				
145	GRD		SG	19

BACKPLANE CONNECTION PORT 0, POS. 2 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
256				
255				
254				
253				
252				
251				
250	SS0S0		SS	32
249				
248				
247	ASD0R0		-SD-	22
246	ASD0R1		SD	4
245				
356				
355				
354				
353				
352				
351				
350				
349				
348				
347				
346				
345				

133. (CONT)

BACKPLANE CONNECTION PORT 1, POS. 1 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
043	RR1S0		RR	13
042	CS1S0		CS	9
041	DM1S0		DM	11
040				
039				
038	RS1S0		RS	7
037	TR1S0		TR	12
036	RT1S0		RT	8
035	ST1S0		ST	5
034	RD1S1		RD	6
033				
032	GRD		SC	37
143	RR1R1		-RR-	31
142	CS1R1		-CS-	27
141	DM1R1		-DM-	29
140				
139	RC		RC	20
138	GRD		-RS-	25
137	GRD		-TR-	30
136	RT1R1		-RT-	26
135	ST1R1		-ST-	23
134	RD1R0		-RD-	24
133				
132	GRD		SG	19

BACKPLANE CONNECTION PORT 1, POS. 2 PIN	TERM. MOD	NOTES	DATA SET EIA DESIG	EIA PIN
243				
242				
241				
240				
239				
238				
237				
236				
235				
234	ASD1R0		-SD-	22
233	ASD1S1		SD	4
232				
343				
342				
341				
340				
339				
338				
337				
336				
335				
334				
333				
332				

SEE PROPRIETARY NOTICE ON COVER SHEET

PROCESSOR SYSTEM CABINET

DWG SIZE 6S

ISSUE 10B

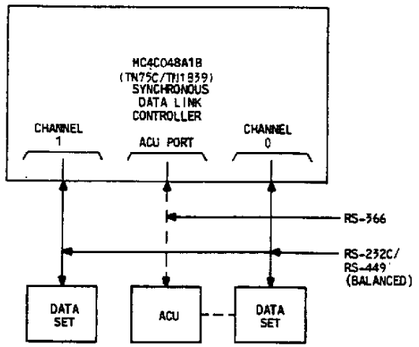
AT&T

SD-4C122-01

SHEET D6

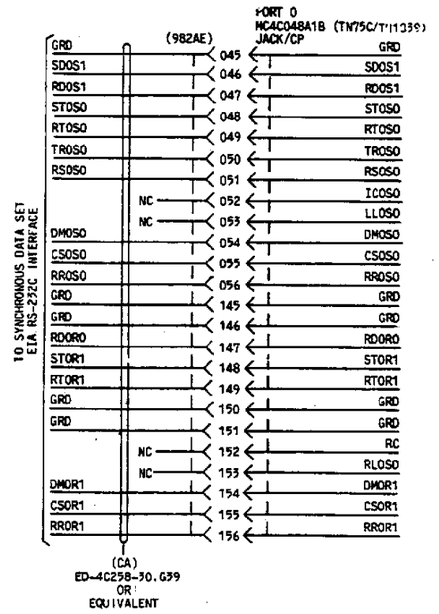
CIRCUIT NOTES: (CONT)

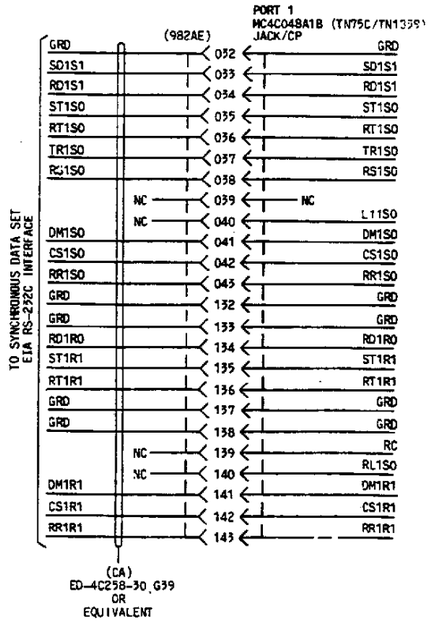
108. (CONT)



108. (CONT)

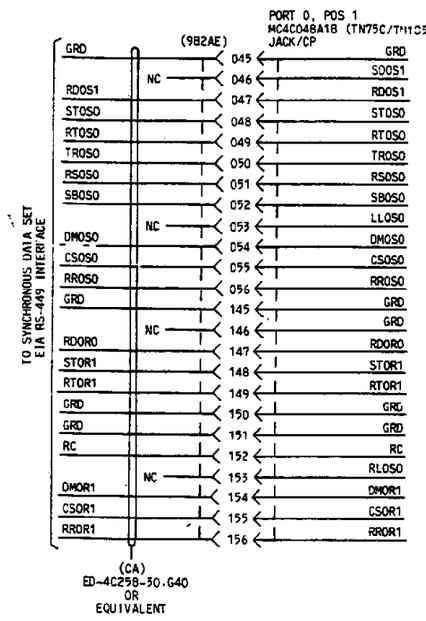
THE FOLLOWING SHOWS THE GRAPHICAL REPRESENTATION OF THE (ED-4C258-30.G39 OR EQUIVALENT) CONNECTION BETWEEN MC4C048A1B (TN75C/TN1839) AND EIA RS-232C INTERFACE

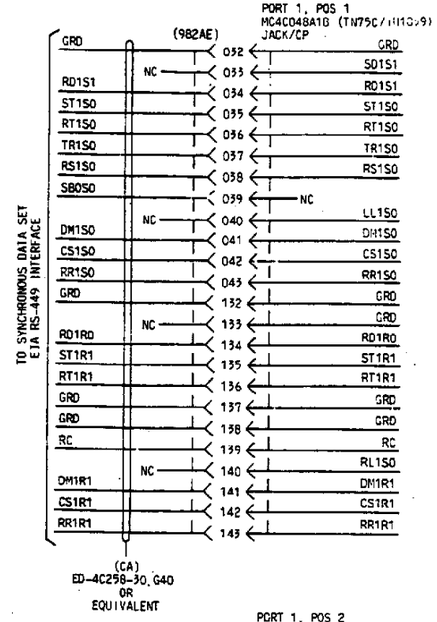


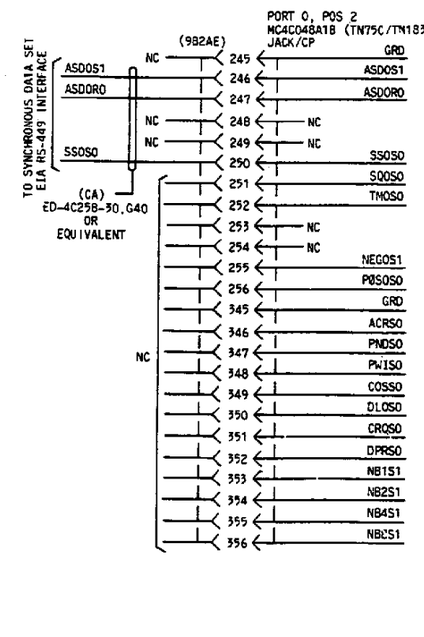


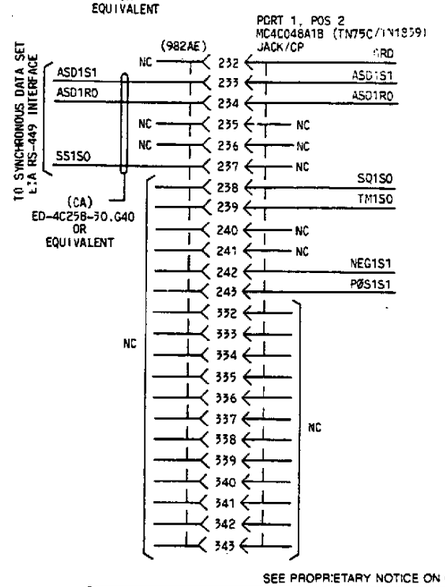
108. (CONT)

THE FOLLOWING SHOWS THE GRAPHICAL REPRESENTATION OF THE (ED-4C258-30.G40 OR EQUIVALENT) CONNECTION BETWEEN MC4048A1B (TN75C/TN1039) PORT 0 OR PORT 1 AND EIA RS-449 (BALANCED) INTERFACE.









PROCESSOR SYSTEM CABINET		DWG SIZE 6S	ISSUE 10B
AT&T	SD-4C122-01		SHEET D7

CIRCUIT NOTES: (CONT)

109. MC4C051A1 (TN82) BX.25 DIRECT USER INTERFACE DATA LINK CONTROLLER. (DUIC) PROVIDES ONE CHANNEL. ONE PORT OF EITHER AN RS-232C/RS-449 (UNBALANCED) INTERFACE OR A CCITT V.35 INTERFACE. THE RS-232C/RS-449 PORT CAN HANDLE DATA LINK SPEEDS UP TO 9.6K BPS. THE V.35 PORT HANDLES DATA LINK SPEEDS UP TO 56K BPS. CHOICE OF WHICH PORT TO BE SUPPORTED IS DETERMINED BY THE CUSTOMER IN THE EQUIPMENT CONFIGURATION DATA BASE.

THE MC4C051A1 (TN82) INTERFACES WITH THE 3B20D CENTRAL CONTROL (CC) VIA THE IOP AND SHARES A COMMON INTERFACE WITH OTHER PERIPHERAL CONTROLLERS LOCATED IN THE INPUT/OUTPUT PROCESSOR (IOP). IT PROVIDES ERROR MESSAGE REPORTING, SHOULD ANY ERROR OCCUR DURING NORMAL PROCESSING. THIS IS A NON-ISOLATED PC.

CONNECTION TO A PERIPHERAL DEVICE IS LIMITED TO:

PERIPHERAL DEVICE	CABLE LENGTH RESTRICTION	GRP NO.
RS-232C	50 FEET	#G41
RS-449 (UNBAL.)	200 FEET	#G46
CCITT V.35	50 FEET	#G42

* ED-4C258-30,G_ _ OR EQUIVALENT

CONNECTOR MAP (TOP HALF) FOR TN82

	3	2	1	0	
56	BAD0191	BAD0181	RRR1	RRS0	56
55	BAD0171	BAD0161	CSR1	CSS0	55
54	BAD0151	BAD0141	DHR1	DHS0	54
53	BAD0131	BAD0121	GRD	P@S1	53
52	BAD0111	BAD0101	RC	RLS0	52
51	BAD0091	BAD0081	GRD	RSS0	51
50	BAD0071	BAD0061	GRD	TRS0	50
49	BAD0051	BAD0041	SCRB	SCRA	49
48	BAD0031	BAD0021	SCTB	SCTA	48
47	BAD0011	ASDR0	RDB	RDA	47
46	BAD0001	ASDS1	SDB	SDA	46
45	GRD	GRD	GRD	GRD	45
44	P12E	P12E	N12E	N12E	44
43	BDATA15	BDATA14	RRR1	RRRS0	43
42	BDATA13	BDATA12	CSR1	CSS0	42
41	BDATA11	BDATA10	DHR1	DHS0	41
40	BDATA09	BDATA08	GRD	P@S1	40
39	BDATA07	BDATA06	RC	LLS0	39
38	BDATA05	BDATA04	GRD	RSS0	38
37	BDATA03	BDATA02	GRD	TRS0	37
36	BDATA01	BDATA0C	RTR1	RTS0	36
35	BDATAPH	BDATAPL	STR1	STS0	35
34	BRHD0	BAHWR0	RDR0	RDS1	34
33	BISTATE0	@FFBHE0	GRD	SDS1	33
32	GRD	GRD	GRD	GRD	32

NOTE: ALL UNUSED PINS RESERVED FOR LATER USE.

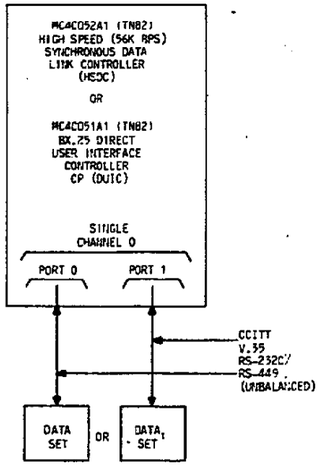
109. (CONT)

MC4C052A1 (TN82) BX. 25 HIGH SPEED (56K BPS) SYNCHRONOUS DATA LINK CONTROLLER (HSDC). PROVIDES ONE CHANNEL. ONE PORT OF EITHER AN RS-232C/RS-449(UNBALANCED) INTERFACE OR A CCITT V.35 INTERFACE. THE RS-232C/RS-449 PORT CAN HANDLE DATA LINK SPEED UP TO 9.6K BPS. THE V.35 PORT HANDLES DATA LINK SPEEDS UP TO 56K BPS. CHOICE OF WHICH PORT TO BE SUPPORTED IS DETERMINED BY THE CUSTOMER IN THE EQUIPMENT CONFIGURATION DATA BASE.

THE MC4C052A1 (TN82) INTERFACES WITH THE 3B20D CENTRAL CONTROL (CC) VIA THE IOP AND SHARES A COMMON INTERFACE WITH OTHER PERIPHERAL CONTROLLERS LOCATED IN THE INPUT/OUTPUT PROCESSOR (IOP). IT PROVIDES ERROR MESSAGE REPORTING, SHOULD ANY ERROR OCCUR DURING NORMAL PROCESSING. THIS IS A NON-ISOLATED PC.

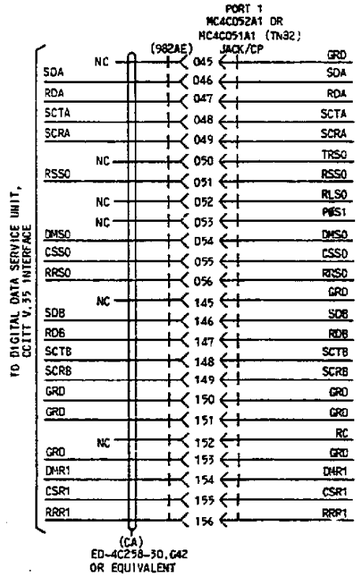
MC4C052A1 OR MC4C051A1 (TN82) DATA RATES ARE:

SPEED BPS			2400	4800	9600	56K
PRIVATE LINE FOX DATA SET (RS-232C)			201C	208A	209A	
DATA PHONE II TYPE DATA SET (RS-449)			2024A	2048A	2096A	
DIGITAL DATA SERVICE UNIT (V.35)						5008 OR EQUIV. LI/S

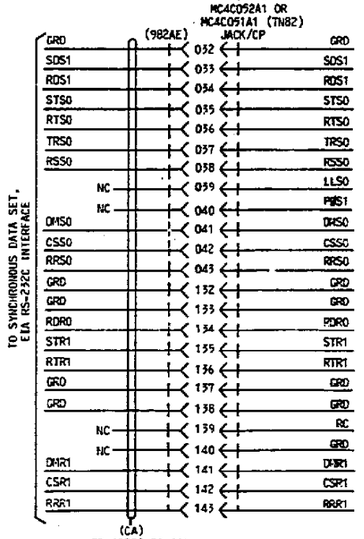


109. (CONT)

THE FOLLOWING SHOWS THE GRAPHICAL REPRESENTATION OF THE (ED-4C258-30,G42 OR EQUIVALENT) CONNECTION BETWEEN MC4C052A1 OR MC4C051A1 (TN82) AND CCITT V.35 INTERFACE.

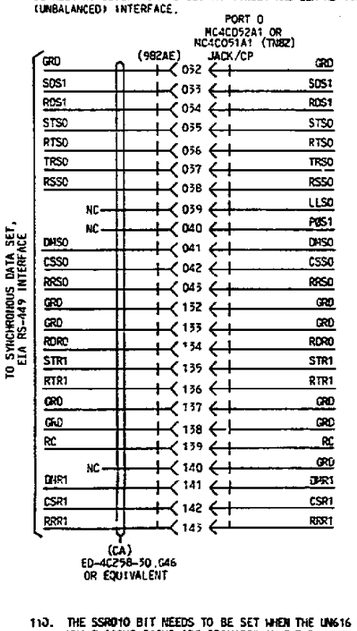


THE FOLLOWING SHOWS A GRAPHICAL REPRESENTATION OF THE (ED-4C258-30,G41 OR EQUIVALENT) CONNECTION BETWEEN MC4C052A1 OR MC4C051A1 (TN82) AND EIA RS-232C INTERFACE.



109. (CONT)

THE FOLLOWING SHOWS A GRAPHICAL REPRESENTATION OF THE (ED-4C258-30,G46 OR EQUIVALENT) CONNECTION BETWEEN MC4C052A1 OR MC4C051A1 (TN82) AND EIA RS-449 (UNBALANCED) INTERFACE.



110. THE SSR010 BIT NEEDS TO BE SET WHEN THE UN616 & UN617 CACHE PACKS ARE PROVIDED IN THE R1 VLMM OR WHEN THE UN618 CONTROLLER IS PROVIDED IN THE VLMM R6 ENVIRONMENT.

111. WHEN THE UN59C CONTROLLER IS PROVIDED IN THE VLMM R6 ENVIRONMENT EQUIPPED WITH TN56S THE SSR010 BIT IS NOT WIRED.

PROCESSOR SYSTEM CABINET

DWG SIZE: 8S

ISSUE: 9B

AT&T

SD-4C122-01

SHEET: D8

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.

302.

FEATURE OR OPTION			APP FIG.	APP OR WRG	QTY
BASIC PROCESSOR SYSTEM 3B20D MODEL 2	PROCESSOR CONTROL CABINET		1		1 PER CKT
	DISK POWER CABINET				
	300 MB DISK DRIVE (SEE NOTE 304)	KS-22707, L1	2		3 PER CKT
		KS-22707, L2	3		3 PER CKT
	TAPE UNIT CABINET		4		1 PER CKT
	256K (1MB) OF ADDITIONAL MEMORY (TN28) (SEE NOTE 305)		5		MAX 15 PER CKT
	CACHE MEMORY (UN10 & 11; OMIT UN30B)		6		1 PER CKT
	4K OF ADDITIONAL WRITABLE MICROSTORE (UN48B)		7		1 PER CKT
	UTILITY CIRCUIT (UN21B)		8		1 PER CKT
	PWR CONT FOR COMM 3 (2C SLOTS 30-33) (TN9)		9		1 PER CKT
	VIDIO TERM (COLOR) (KS-22921, L3)		10		1 PER CKT
	READ ONLY PRINTER (#R040P2F, TTY CORP OR EQUIV)		11		1 PER CKT
	DMAC 1 (UN4B)		12		1 PER CKT
	IO DSCH (UN9B) (SEE NOTE 306)		13		AS REQUIRED
	PORT SW UNIT (TF4) (SEE NOTE 102.2)		14		1 PER CKT
	ADDITIONAL 5 VOLT PWR FOR MS, IO &DFC UNIT (495FA)		15		1 PER CKT
	ADDITIONAL 5 VOLT PWR FOR GROWTH UNIT (495FA)		16		1 PER CKT
	4K PROM MC4C077A1B, (UN28B) (UNIX RTR RELEASE 1)		17		1 PER CKT
	16K PROM WMCS, MC4C127A1 (TN19) & UN55 (UNIX RTR RELEASE 1)		18		1 PER CKT
	1ST 2MB MSM (TN56) (UNIX RTR RELEASE 1)	(MFR DISC)	19		1 PER CKT
	2MB OF ADD'L MSM (TN56) (SEE NOTE 305) (UNIX RTR RELEASE 1)		20		MAX 15 PER CKT
	MTTY CONTROLLER WITH EIA PAGE ENHANCEMENTS, MC4C132A1, (TN963) (UNIX RTR RELEASE 1)		21		1 PER CKT
	TAPE UNIT CABINET (SEE NOTE 305)		22		1 PER CKT
	PWR CONV FOR TAPE CABINET		23		1 PER CKT
	MEMORY EXPANSION CAPABILITY (UNIX RTR RELEASE 1)		24		1 PER CKT
	1ST 2MB OF MSM		25		1 PER CKT

INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION		APP FIG.	APP OR WRG	QTY
PROCESSOR CONTROL CABINET 3B20D MODEL 2 (CONT)	MEMORY EXPANSION ASSOCIATED WITH CACHE MEMORY	27		1 PER CKT
	STORE ADDRESS TRANSLATOR (SAT)	28		1 PER CKT
	BACKPLANE WIRING FOR IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE, UN45C AND UNIX RTR RELEASE 1 OR LATER SOFTWARE REQUIRED.	29		1 PER CKT
	PROVIDE TN19 MC4C127A1B	M		1 PER CKT
	SYSTEM SANITY ALARM FEATURE	30		1 PER CKT
PC CIRCUIT PACKS 3B20D MODEL 2 & MODEL 3	SCANNER/SIGNAL DISTRIBUTOR (UN33B) (SEE NOTES 108,308.1)	100		AS REQUIRED
	2 CHANNEL (2 PORTS PER CH) TTY ASYNCHRONOUS LINK, MC4C011A12, (TN748) (SEE NOTES 107, 306)	101		AS REQUIRED
	2 CHANNEL SYNCHRONOUS LINK, MC4C048A1B, (TN75C) (SEE NOTES 106, 306)	102		AS REQUIRED
	SCANNER/SIGNAL (MFR DISC) DISTRIBUTER INTERFACE (TF2) (SEE NOTE 102.1)	103		AS REQUIRED
	HIGH SPEED TAPE (UN52) (SEE NOTES 104, 306)	104		AS REQUIRED
	BX.25 HIGH SPEED (56K BPS) DATA LINK CONTROLLER, MC4C052A1, (TN82)	105		AS REQUIRED
	SCANNER/SIGNAL DISTRIBUTOR (UN33C) (SEE NOTES 108, 308.1)	107		AS REQUIRED
	BX. 25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1C (TN828)	108		AS REQUIRED
	BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1D (TN828)	109		AS REQUIRED
	SCANNER/SIGNAL DISTRIBUTOR (UN33D) (SEE NOTES 108, 308.1)	110		AS REQUIRED
	1600 BPI HIGH SPEED TAPE (UN52B) (SEE NOTES 104, 308)	111		AS REQUIRED
	BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1E (TN828)	112		AS REQUIRED
	2 CHANNEL SYNCHRONOUS LINK (TN1839 - NSI APPLICATION ONLY) (SEE NOTES 106, 306.1)	113		AS REQUIRED
	BX.25 HIGH SPEED (64 KBPS) DATA LINK CONTROLLER (TN1420) (SEE NOTES 109,306)	114		AS REQUIRED
	IMPROVED DFC FIRMWARE MC4C091B1C (TN88)	26		1 PER CKT

INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION				APP FIG.	APP OR WRG	QTY
BASIC PROCESSOR ARRANGED FOR TAPE/ 340 MB DRIVES CABINET T/DC ( ) RETROFIT	T/DC (0) ARRANGED FOR TAPE UNIT (0) & 4 DRIVES (00-03)	CABINET & PWR DIST UNIT				
		340 MB DRIVES (KS-22875)	NO: 00	200		1 PER CKT
			01			
			02	250		1 PER CKT
			03	251		1 PER CKT
		TAPE UNIT		222		1 PER CKT
	T/DC (1) ARRANGED FOR TAPE UNIT (1) & 4 DRIVES (04-07)	CABINET		201		1 PER CKT
		PWR DIST UNIT		211		1 PER CKT
		340 MB DRIVES (KS-22875)	NO: 04	252		1 PER CKT
			05	253		1 PER CKT
			06	254		1 PER CKT
			07	255		1 PER CKT
		TAPE UNIT		223		1 PER CKT
3B20D MODEL 3 PROCESSOR ARRANGED FOR 340 MB DISK DRIVES	PROCESSOR CONTROL CABINET			1		1 PER CKT
	MAIN STORE & IOP GROWTH UNIT			2		1 PER CKT
	NOT USED			3		
	NOT USED			4		
	1 MB OF ADD'L MEMORY (TN28) (SEE NOTE 305)			5		MAX 15 PER CKT
	CACHE MEMORY (UN10 & 11; OMIT UN30B)			6		1 PER CKT
	NOT USED			7		

INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION		APP FIG.	APP OR WRG	QTY
3B20D MODEL 3 PROCESSOR ARRANGED FOR 340 MB DISK DRIVES	POWER FOR COMM 2 (2C SLOTS 20-23)	8		1 PER CKT
	POWER FOR COMM 3 (2C SLOTS 30-33)	9		1 PER CKT
	MTCE TTY TERM, COLOR (KS-22921, L3)	10		1 PER CKT
	READ ONLY PRINTER (#R040P2F, OR EQUIV)	11		1 PER CKT
	NOT USED	12		
	IO DSCH (UN9B)	13		1 PER CKT
	NOT USED	14		1 PER CKT
	ADD'L 5 VOLT PWR FOR MS, IO & DFC UNIT	15		1 PER CKT
	ADD'L 5 VOLT PWR FOR GROWTH UNIT	16		1 PER CKT
	NOT USED	17		
	NOT USED	18		
	1ST 2MB MSM (TN56) (MFR DISC)	19		1 PER CKT
	2MB OF ADD'L MEMORY (TN56) (SEE NOTE 305)	20		MAX 15 PER CKT
	MTTY CONTROLLER WITH EIA PAGE ENHANCEMENTS	21		1 PER CKT
	MEMORY EXPANSION CAPABILITY (UNIX RTR RELEASE 1)	22		1 PER CKT
	1ST 2MB OF MSM	23		1 PER CKT
	MEMORY EXPANSION CAPABILITY ASSOCIATED WITH CACHE MEMORY	24		1 PER CKT
	FAST BACKUP CAPABILITY	25		1 PER CKT
	STORE ADDRESS TRANSLATOR (SAT) (UN45C)	26		1 PER CKT
	BACKPLANE WIRING FOR IMPROVED OPERATING SYSTEM PERFORMANCE FEATURE, UN45C AND UNIX RTR RELEASE 1 OR LATER SOFTWARE REQUIRED.	27		1 PER CKT
	SYSTEM SANITY ALARM FEATURE	30		1 PER CKT

PROCESSOR SYSTEM CABINET

DWG SIZE C2 | ISSUE 10B

AT&T | SD-4C122-01 | SHEET D9

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INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION			APP FIG.	APP OR WRG	QTY
PC CIRCUIT PACKS 3B20D MODEL 3	SCANNER/SIGNAL DIST (SCSD) (SEE NOTE 306)		100		AS REQUIRED
	2 CHANNEL (2 PORTS PER KCH) TTY ASYNCHRONOUS LINK PC (SEE NOTE 306)		101		AS REQUIRED
	2 CHANNEL SYNCHRONOUS LINK PC (SEE NOTE 306)		102		AS REQUIRED
	NOT USED		103		AS REQUIRED
	HIGH SPEED TAPE PC (SEE NOTE 306)		104		AS REQUIRED
	BX.25 KHIGH SPEED (56 KBPS) DATA LINK CONTROLLER (SEE NOTE 306)		105		AS REQUIRED
	SCANNER/SIGNAL DISTRIBUTOR (UN33C) (SEE NOTES 108, 306.1)		107		AS REQUIRED
	BX. 25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1C (TN828)		108		AS REQUIRED
	BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1D (TN828)		109		AS REQUIRED
	SCANNER/SIGNAL DISTRIBUTOR (UN33D) (SEE NOTES 108, 306.1)		110		AS REQUIRED
	1600 BPI HIGH SPEED TAPE (UN528) (SEE NOTES 104, 306)		111		AS REQUIRED
	BX.25 HIGH SPEED (56 KBPS) DATA LINK CONTROLLER MC4C052A1E (TN828)		112		AS REQUIRED
	2 CHANNEL SYNCHRONOUS LINK (TN1839 - NSI APPLICATION ONLY) (SEE NOTES 108, 306.1)		113		AS REQUIRED
	BX.25 HIGH SPEED (64 KBPS) DATA LINK CONTROLLER (TN1420) (SEE NOTES 109,306)		114		AS REQUIRED
1ST T/DC ARRANGED FOR 1ST TAPE UNIT & 4 DRIVES & PWR DIST UNIT T/DC (0) 3B20D MODEL 3	CABINET ASSY				
	PWR DIST UNIT				
	340 MB DISK DRIVES (KS-22875, L4Q, L18 E/W L51)	NO: 00	200		1 PER CKT
		01			
		02	250		AS REQUIRED
		03	251		AS REQUIRED
	TAPE UNIT KS-22762, L3 (1600 BPI, 25/100 IPS STREAMER) (MFR DISC)		220		AS REQUIRED
	TAPE UNIT KS-23113, L10 (6250/1600 BPI 25/75 IPS, STREAMER, UNFUFFERED)		222		AS REQUIRED

INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION			APP FIG.	APP OR WRG	QTY
1ST GROWTH T/DC ARRANGED FOR 2ND TAPE UNIT & 4 DRIVES (04-07) & PWR DIST UNIT OR ARRANGED FOR 8 DISK DRIVES (04-11) & PWR DIST UNIT T/DC (1) 3B20D MODEL 3	CABINET ASSY		201		1 PER CKT
	340 MB DISK DRIVES (KS-22875, L4Q, L18 E/W L51)	NO: 04	252		1 PER CKT
		05	253		AS REQUIRED
		06	254		AS REQUIRED
		07	255		AS REQUIRED
		08	256		AS REQUIRED
		09	257		AS REQUIRED
		10	260		AS REQUIRED
		11	262		AS REQUIRED
	PWR DIST UNIT (4 DRIVES)		211		AS REQUIRED
	PWR DIST UNIT (8 DRIVES)		210		AS REQUIRED
	(MFR DISC) TAPE UNIT KS-22762, L3 (1600 BPI, 25/100 IPS, STREAMER)		221		AS REQUIRED
	TAPE UNIT KS-23113, L10 (6250/1600 BPI 25/75 IPS, STREAMER, UNBUFFERED)		223		AS REQUIRED
2ND GROWTH T/DC ARRANGED FOR 4 DRIVES (12-15) & PWR DIST UNIT OR ARRANGED FOR 8 DRIVES (08-15) & PWR DIST UNIT T/DC (2) 3B20D MODEL 3	CABINET ASSY		202		1 PER CKT
	340 MB DISK DRIVES (KS-22875, L4Q, L18 E/W L51)	NO: 12	264		AS REQUIRED
		13	266		AS REQUIRED
		14	268		AS REQUIRED
		15	270		AS REQUIRED
		08	257		AS REQUIRED
		09	259		AS REQUIRED
		10	261		AS REQUIRED
		11	263		AS REQUIRED
		12	265		AS REQUIRED
		13	267		AS REQUIRED
		14	269		AS REQUIRED
		15	271		AS REQUIRED
	PWR DIST UNIT (4 DRIVES)		211		AS REQUIRED
	PWR DIST UNIT (8 DRIVES)		210		AS REQUIRED
CABLE & DIODES FOR MAIN STORE MEMORY			A		1 PER CKT
WIRING FOR UN33B			B		1 PER CKT
POWER STRAP			C		1 PER CKT
625F JACK			D		1 PER CKT
MEMORY EXPANSION			E		1 PER CKT
PROVIDE TN19 MC4C127A1B			H		1 PER CKT

INFORMATION NOTES: (CONT)

302. (CONT)

FEATURE OR OPTION		APP FIG.	APP OR WRG	QTY
PERIPHERAL INTERFACE CABINET PICO ARRANGED FOR 2 IOP BASIC UNITS, 2 IOP GROWTH UNITS, A COOLING UNIT AND PWR DIST UNIT	CABINET PWR DIST UNIT, COOLING UNIT AND 1ST IOP BASIC UNIT	401		1 PER CKT
	1ST IOP GROWTH UNIT	402		AS REQUIRED
	2ND IOP BASIC UNIT	403		AS REQUIRED
	2ND IOP GROWTH UNIT	404		AS REQUIRED
VERY LARGE MAINSTORE MEMORY	(UN133C & LN48D)	501		1 PER CKT
	(UN608 MC3T001) (UN609 MC3T002)	502		1 PER CKT
	(UN248)	503		1 PER CKT
	NON-INTERFERING WIRING	504		1 PER CKT
	NON-INTERFERING WIRING	505		1 PER CKT
	UN616, UN617	506		1 PER CKT
	UN618	507		1 PER CKT
	(1ST TN2012)	508		1 PER CKT
	(GROWTH TN2012)	509		MAX 15 PER CKT
	(UN611, UN512 & UN268/MC3T003A1)	510		1 PER CKT
	UN615	511		1 PER CKT
	(UN3C)	512		1 PER CKT
	UNASSIGNED	513 & 514		
	NON-INTERFERING R1 WIRING	515		1 PER CKT
	NON-INTERFERING R1 WIRING	516		1 PER CKT
	UNASSIGNED	517		1 PER CKT
	INTERFERING R8 WIRING	518		1 PER CKT
	SSR010 BIT (SEE NOTE 310,311)	519		1 PER CKT

INFORMATION NOTES: (CONT)

303.

RECORD OF FIGURES, WIRING AND APPARATUS CHANGES						
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	ABM	MD
2B MODEL 2	FIG. 103					103
3B MODEL 3	FIG. 220,221					220,221

RECORD OF FIGURES, WIRING AND APPARATUS CHANGES					
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT	
				AVAIL	DA
9B SEE NOTE X	FIG.224 225,226 227,228 229,451 452,453 454,501 502,503 504,505 506,507 508,509 510,512 515,516 518,519			FIG.224 225,226 227,228 229,451 452,453 454,501 502,503 504,505 506,507 508,509 510,512 515,516 518,519	

NOTE X - PRIOR TO ISSUE 9B, COLUMNS HEADED "STD", "MD", ETC., CONVEYED APPLICATION INFORMATION. AT ISSUE 9B, COLUMNS HEADED "AVAIL" AND "DA" NOW INDICATED THE AVAILABILITY OF THE PRODUCT.

304. DISK DRIVES ARE ALLOCATED ON A REGIONAL BASIS AS FOLLOWS:

A) EASTERN, NORTHEASTERN OR CENTRAL REGIONAL ENGINEERING LOCATIONS ARE RESTRICTED TO ORDERING KS-22702, L1 FROM CONTROL DATA CORP.

B) SOUTHERN, SOUTHWESTERN, MOUNTAIN-NORTHWEST OR PACIFIC REGIONAL ENGINEERING LOCATIONS ARE RESTRICTED TO ORDERING KS-22702, L2 FROM CENTURY DATA SYSTEMS CORP.

PROCESSOR SYSTEM CABINET

DWG SIZE C2 | ISSUE 10B

AT&T | SD-4C122-01 | SHEET D10

PRINTED IN U.S.A

INFORMATION NOTES: (CONT)

305. ONE INCREMENT OF ADDITIONAL 1MB MAIN STORE MEMORY (MSM) (TN28) OR 2MB MSM (TN56) IS SUMMARIZED BELOW:

QTY	CKT PACK	MSM SIZE	CKT PACK	MSM SIZE	CKT PACK	MSM SIZE	CKT PACK LOCATION
1ST	TN28	1MB	TN56	2MB	TN2012	4MB	051-162, 151-162
2ND		2MB		4MB		8MB	051-156, 151-156
3RD		3MB		6MB		12MB	051-150, 151-150
4TH		4MB		8MB		16MB	051-144, 151-144
5TH		5MB		10MB		20MB	051-138, 151-138
6TH		6MB		12MB		24MB	051-132, 151-132
7TH		7MB		14MB		28MB	051-126, 151-126
8TH		8MB		16MB		32MB	051-120, 151-120
9TH		9MB		18MB		36MB	042-120, 142-120*
10TH		10MB		20MB		40MB	042-126, 142-126*
11TH		11MB		22MB		44MB	042-132, 142-132*
12TH		12MB		24MB		48MB	042-138, 142-138*
13TH		13MB		26MB		52MB	042-144, 142-144*
14TH		14MB		28MB		56MB	042-150, 142-150*
15TH	↓	15MB	↓	30MB	↓	60MB	042-156, 142-156*
16TH	TN28	16MB	TN56	32MB	TN2012	64MB	042-162, 142-162*

* MEMORY EXPANSION

306. LISTED BELOW IS A SUMMARY OF PERIPHERAL CONTROLLER (PC) CIRCUIT PACKS SLOTS AVAILABLE IN IØP BASIC UNIT & MAIN STORE AND IØP GROWTH UNIT. PC SLOTS 00 & 02 ARE ALWAYS FURNISHED, PC SLOTS 03 ARE RESERVED FOR A TAPE UNIT CP OPTION (UN52) THE REMAINDER IS LINE ENGINEERED ON AN AS REQUIRED (A/R) BASIS.

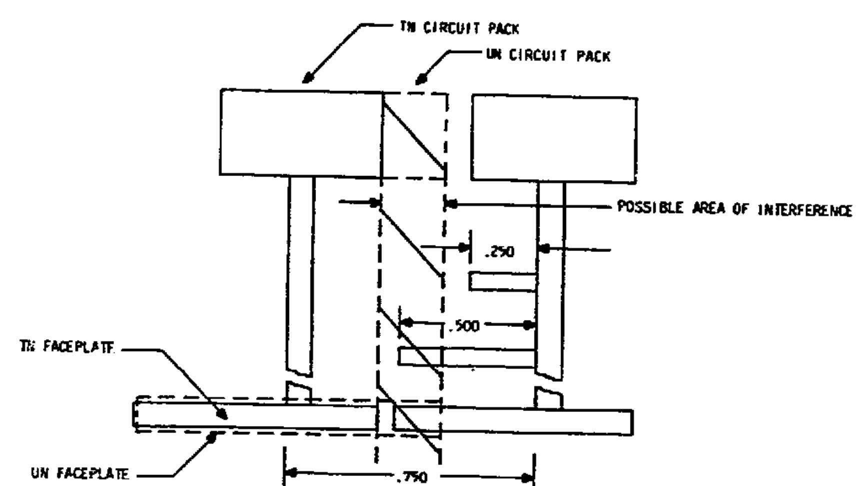
IØP LOCATION	PC COMM	PC SLOT	PACK LOCATION	PC CODE	PACK K LOCATION	PC CODE	REFERENCE NOTES
IØP BASIC UNIT J1C147BD	0	00	033-102	MC4C041A1B (TN83B) MTTY	133-102	MC4C041A1B (TN83B) MTTY	RESERVED
		01	033-094	A/R	133-094	A/R	306.1
		02	033-086	UN33B SCAN/SD	133-086	UN33B SCAN/SD	RESERVED
		03	033-078	UN52 * MC4C160A1C **	133-078	UN52 * MC4C160A1C **	RESERVED
	1	10	033-062	A/R	133-062	A/R	306.1
		11	033-054	A/R	133-054	A/R	306.1
		12	033-046	A/R	133-046	A/R	306.1
		13	033-038	A/R	133-038	A/R	306.1
MAIN STORE AND IØP GROWTH UNIT J1C147BC	2	20	042-080	A/R	142-080	A/R	306.1
		21	042-074	A/R	142-074	A/R	306.1
		22	042-068	A/R	142-068	A/R	306.1
		23	042-062	A/R	142-062	A/R	306.1
	3	30	042-046	A/R	142-046	A/R	306.1
		31	042-040	A/R	142-040	A/R	306.1
		32	042-034	A/R	142-034	A/R	306.1
		33	042-028	A/R	142-028	A/R	306.1

* UN52 CP IS REQUIRED WITH J1C174A-1 TAPE CABINET AND KS-22762 (CDC/CPI 92181) TAPE UNIT. SIGNAL TYPE IS TTL, WITH A MAXIMUM CABLE LENGTH OF 20 FEET.

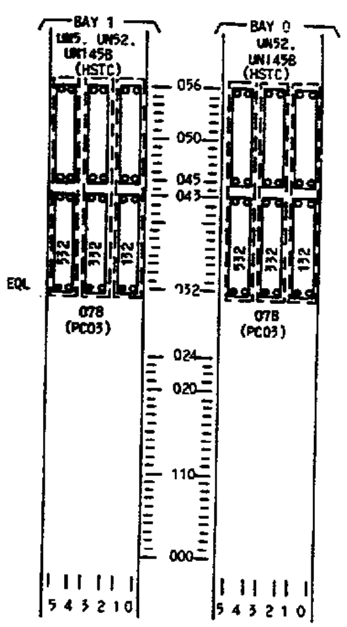
** MC4C160A1C (UN145B) 3B20D MODEL 3 ONLY IS REQUIRED WITH J1C192A-1 TAPE/DISK CABINET AND KS-23115 TAPE UNIT. THE MAXIMUM CABLE LENGTH IS 20 FEET.

306.1 LIST B (J1C176A-1 & J1C176B-1) PROVIDES ADDITIONAL WIRING TO FIVE (5) PC SLOTS (01,10-13) WHEN ADDITIONAL UN33B (SCAN/SD) CP'S ARE REQUIRED.

306.2 P.C. COMMUNITY 1 MAY BE USED WITH ANY PC CIRCUIT PACK. P.C. COMMUNITY 2 & 3 MAY BE USED BY ANY PC CIRCUIT PACK HAVING A MAXIMUM COMPONENT HEIGHT OF .250 INCH EXECPT FOR THE 602 TYPE CAPACITOR MOUNTED NEXT TO THE CONNECTOR, WHEN A (UN) TYPE CIRCUIT PACK IS MOUNTED TO THE LEFT. IF THE COMPONENT HEIGHT IS BETWEEN .250 AND .500 INCH AND THE APPLICATION ENGINEER DESIRES TO USE THE PACK IN COMMUNITY 2 & 3, HE/SHE MUST HAVE A (TN) CIRCUIT PACK TO THE LEFT OR LEAVE THE SLOT EMPTY. (SEE FIGURE BELOW) PC SLOT 33 CAN NOT SUPPORT ANY OF THE PERIPHERAL CONTROLLER CIRCUIT PACKS PROVIDED IN THE 100 SERIES LISTS.



306.3 SHOWN BELOW ARE THE TAPE CABLE CONNECTIONS FROM THE PROC CONT CABINET (UN52 OR UN145B) TO THE TAPE UNIT CABINET VIA A FLAT RIBBON CABLE WITH 3 BACKPLANE CONNECTORS.



PROCESSOR SYSTEM CABINET	DWG SIZE	ISSUE 9B
AT&T	SD-4C122-01	SHEET D11

INFORMATION NOTES: (CONT)

307. LISTED BELOW ARE THE RECOMMENDED I@ ASSIGNMENTS (MAX. 6) FOR 3B20D MODEL 2 PROCESSOR (OMERT):

DMA CHANNELS	FRAME EQL	UNIT SD & J CODE
* CH10 (DMAC0)	51-088	SD-4C099-01 (J1C147BB, MAIN STORE, I@ & DFC UNIT)
* CH11 (DI@CH11)	51-080	SD-4C099-01 (J1C147BB, MAIN STORE, I@ & DFC UNIT)
CH12 (DI@CH12)	51-104	SD-4C099-01 (J1C147BB, MAIN STORE, I@ & DFC UNIT)
CH15 (DMAC1)	51-096	SD-4C099-01 (J1C147BB, MAIN STORE, I@ & DFC UNIT)
CH16 (DI@CH16)	42-112	SD-4C097-01 (J1C147BC, GROWTH UNIT)
CH17 (DI@CH17)	42-104	SD-4C097-01 (J1C147BC, GROWTH UNIT)

PROGRAMMED I@ CHANNELS	FRAME EQL	UNIT SD & J CODE
CH00 (CCI@CH00)	42-088	SD-4C097-01 (J1C147BC, GROWTH UNIT)
CH01 (CCI@CH01)	42-096	SD-4C097-01 (J1C147BC, GROWTH UNIT)
CH02 (CCI@CH02)	42-104	SD-4C097-01 (J1C147BC, GROWTH UNIT)
CH03 (CCI@CH03)	42-112	SD-4C097-01 (J1C147BC, GROWTH UNIT)

* ALWAYS FURNISHED AS PART OF LIST 1

307.1 ONLY POWER AND GROUND ARE FURNISHED WITH THE MLPWB BACKPLANE FOR POSITIONS 51-104, 42-088, 42-096, 42-104 AND 42-112.

307.2 THE APPLICATION (USING SYSTEM) SHALL REQUEST ADDITIONAL CHANNEL WIRING, ETC. REQUIRED FROM THE GENERIC PLANNING & APPLICATION GROUP.

308. LISTED BELOW IS A SUMMARY OF 3B20D MODEL 2 PROCESSOR SYSTEM CABINET INTERFRAME CABLING.

		FROM					TO				
TITLE	FUNCTION	CABLE IDENT	CABINET OR UNIT	EQL-TERM	SD NUMBER	CAD	CABINET OR UNIT	CONN POS OR EQL-TERM	SD NUMBER	CAD	CABLING DRAWING
SEE FS 1 PCC AND MHD 0-2: J1C176A LIST 1, 2 OR 3	DATA	ZA161	PCC(0)	051-064-513	SD-4C119-01	111	MHD0 (KS-22707,L1)	P2	SD-4C056-02	12	ED-4C410-35,G61
		ZA161A	PCC(0)	051-064-532	SD-4C119-01	111	MHD2 (KS-22707,L1)	P2	SD-4C056-02	12	ED-4C410-35,G61A
		ZA161B	PCC(1)	151-064-513	SD-4C119-01	111	MHD1 (KS-22707,L1)	P2	SD-4C056-02	12	ED-4C410-35,G61B
	CONTROL	ZA172	PCC(0)	051-044-500	SD-4C119-01	110	MHD0 (KS-22707,L1)	P3	SD-4C056-02	10	ED-4C410-35,G72
		ZA172A	MHD0 (KS-22707,L1)	P4	SD-4C056-02	11	MHD2 (KS-22707,L1)	P3	SD-4C056-02	10	ED-4C410-35,G72A
		ZA172B	PCC(1)	151-044-500	SD-4C119-01	110	MHD1 (KS-22707,L1)	P3	SD-4C056-02	10	ED-4C410-35,G72B
	DATA	ZA161C	PCC(0)	051-064-513	SD-4C119-01	111	MHD0 (KS-22707,L2)	P2	SD-4C056-02	12	ED-4C410-35,G61C
		ZA161D	PCC(0)	051-064-532	SD-4C119-01	111	MHD2 (KS-22707,L2)	P2	SD-4C056-02	12	ED-4C410-35,G61D
		ZA161E	PCC(1)	151-064-513	SD-4C119-01	111	MHD1 (KS-22707,L2)	P2	SD-4C056-02	12	ED-4C410-35,G61E
	CONTROL	ZA172C	PCC(0)	051-044-500	SD-4C119-01	110	MHD0 (KS-22707,L2)	P3	SD-4C056-02	10	ED-4C410-35,G72C
		ZA172D	MHD0 (KS-22707,L2)	P4	SD-4C056-02	11	MHD2 (KS-22707,L2)	P3	SD-4C056-02	10	ED-4C410-35,G72D
		ZA172E	PCC(1)	151-044-500	SD-4C119-01	110	MHD1 (KS-22707,L2)	P3	SD-4C056-02	10	ED-4C410-35,G72E
	SCSD	ZA63	PCC(0)	033-086-537	SD-4C119-01	205	DISK PWR CAB	050-075-408	SD-4C121-01	08	ED-4C410-35,G17U
		ZA64	PCC(0)	033-086-550	SD-4C119-01	206	DISK PWR CAB	031-075-408	SD-4C121-01	08	ED-4C410-35,G17V
		ZA65	PCC(1)	133-095-037	SD-4C119-01	208	DISK PWR CAB	012-072-405	SD-4C121-01	08	ED-4C410-35,G17W
	POWER	J4	DISK POWER CAB	048-123-A	SD-4C121-01	07	MHD0(KS-22707,L1&L2)	J1	SD-4C056-02	-	ED-4C405-10,G102
		J4	DISK POWER CAB	029-123-A	SD-4C121-01	07	MHD2(KS-22707,L1&L2)	J1	SD-4C056-02	-	ED-4C405-10,G103
		J4	DISK POWER CAB	010-123-A	SD-4C121-01	07	MHD1(KS-22707,L1&L2)	J1	SD-4C056-02	-	ED-4C409-10,G104
SEE FS 1 PCC AND TAPE UNIT J1C176A,L4	CONTROL	ZA165	PCC(0)	033-078-532 033-078-332 033-078-132	(SD-4C119-01)	SEE NOTE 306.3	TAPE UNIT CABINET	036-090-1	SD-4C120-01	1	ED-4C410-35,G65
MTTY AND R@P	MTTY	ZA20 OR 20A	PCC(0)	009-040-313	SD-4C119-01	202	TO MTTY	--	--	-	ED-4C410-35,G7 OR 7B
CABLES FOR INTFC	R@P	ZA21 OR 21A	PCC(0)	009-056-313	SD-4C119-01	203	TO R@P	--	--	-	ED-4C410-35,G7A OR 7C

NOTE: ALARM CABLES PROVIDING INTERCONNECTS BETWEEN PROCESSOR 0 AND PROCESSOR 1 AND BETWEEN DISKS HAVE BEEN DELETED AND APPLICATION ENGINEERED CADS FROM EACH PROCESSOR BAY HAS BEEN PROVIDED.

PROCESSOR SYSTEM CABINET

DWG SIZE: E5

ISSUE: 9B

AT&T | SD-4C122-01 | SHEET: D12

INFORMATION NOTES: (CONT)

309. LISTED BELOW IS A SUMMARY OF 3B200 MODEL 2 SYSTEM CABINET AC TO DC POWER DISTRIBUTION CABLING:

FROM				TO				
CABINET	EQ# -TERM	LEAD DESIG	CABINET STAMPING	AC DC POWER DIST CKT	LEAD DESIG	PWR BUS A OR B	EQPT INFO	COMMENTS
PCCA (BAY 0) (J1C173A-1) SD-4C119-01 FS 9/SYM 6	067-020-N48VA	N48V1	-48VA	SD-4C053-01 FS15	-48VA	A	ED-4C184-12	
	067-020-RTNA	48R1	RTNA		RTNA	A	ED-4C184-12	
	067-036-N48VB	N48V2	-48VB		-48VB	A	ED-4C184-12	
	067-036-RTNB	48R2	RTNB		RTNB	A	ED-4C184-12	
	067-052-N48VC	N48VSP	-48VC		-48VC	A	ED-4C184-12	SPARE
	067-052-RTNC	48RSP	RTNC		RTNC	A	ED-4C184-12	SPARE
	067-077-N48VD	N48V3	-48VD		-48VD	A	ED-4C184-12	
	067-077-RTND	48R3	RTND		RTND	A	ED-4C184-12	
	067-093-N48VE	N48V4	-48VE		-48VE	A	ED-4C184-12	
	067-093-RTNE	48R4	RTNE		RTNE	A	ED-4C184-12	
	067-109-N48VF	N48V5	-48VF		-48VF	A	ED-4C184-12	
	067-109-RTNF	48R5	RTNF		RTNF	A	ED-4C184-12	
	067-134-N48VG	N48V6	-48VG		-48VG	A	ED-4C184-12	
	067-134-RTNG	48R6	RTNG		RTNG	A	ED-4C184-12	
	067-150-N48VH	N48V7	-48VH		-48VH	A	ED-4C184-12	
	067-150-RTNH	48R7	RTNH		RTNH	A	ED-4C184-12	
PCCA (BAY 0) FS 9/SYM 11	067-166-N48VJ	N48V8	-48VJ		-48VJ	B	ED-4C184-12	PORT SWITCH
	067-166-RTNJ	48R8	RTNJ		RTNJ	B	ED-4C184-12	PORT SWITCH
PCCB (BAY 1) FS 9/SYM 8	167-020-N48VA	N48V1	-48VA		-48VA	B	ED-4C184-12	
	167-020-RTNA	48R1	RTNA		RTNA	B	ED-4C184-12	
	167-036-N48VB	N48V2	-48VB		-48VB	B	ED-4C184-12	
	167-036-RTNB	48R2	RTNB		RTNB	B	ED-4C184-12	
	167-052-N48VC	N48VSP	-48VC		-48VC	B	ED-4C184-12	SPARE
	167-052-RTNC	48RSP	RTNC		RTNC	B	ED-4C184-12	SPARE
	167-077-N48VD	N48V3	-48VD		-48VD	B	ED-4C184-12	
	167-077-RTND	48R3	RTND		RTND	B	ED-4C184-12	
	167-093-N48VE	N48V4	-48VE		-48VE	B	ED-4C184-12	
	167-093-RTNE	48R4	RTNE		RTNE	B	ED-4C184-12	
	167-109-N48VF	N48V5	-48VF		-48VF	B	ED-4C184-12	
	167-109-RTNF	48R5	RTNF		RTNF	B	ED-4C184-12	
	167-134-N48VG	N48V6	-48VG		-48VG	B	ED-4C184-12	
	167-134-RTNG	48R6	RTNG		RTNG	B	ED-4C184-12	
	167-150-N48VH	N48V7	-48VH		-48VH	B	ED-4C184-12	
	167-150-RTNH	48R7	RTNH		RTNH	B	ED-4C184-12	
DISK POWER CAB (J1C175A-1) SD-4C121-01	070-036-N48A	N48VA	INV0 -48V0	SD-4C053-01 FS16	-48A	A	ED-4C184-12	
	070-036-RTNA	N48RETA	INV0 RTN		RTNA	A	ED-4C184-12	
	070-056-N48B	N48VB	CP0 -48V0		-48B	A	ED-4C184-12	
	070-056-RTNB	N48RETB	CP0 RTN		RTNB	A	ED-4C184-12	
	070-076-N48C	N48VC	INV2 -48V2		-48C	A	ED-4C184-12	
	070-076-RTNC	N48RETC	INV2 RTN		RTNC	A	ED-4C184-12	
	070-096-N48D	N48VD	CP2 -48V2		-48D	A	ED-4C184-12	
	070-096-RTND	N48RETD	CP2 RTN		RTND	A	ED-4C184-12	
	070-116-N48E	N48VE	INV1 -48V1		-48E	B	ED-4C184-12	
	070-116-RTNE	N48RETE	INV1 RTN		RTNE	B	ED-4C184-12	
	070-136-N48F	N48VF	CP1 -48V1		-48F	B	ED-4C184-12	
	070-136-RTNF	N48RETF	CP1 RTN		RTNF	B	ED-4C184-12	

PROCESSOR SYSTEM CABINET

DWG SIZE: 8S

ISSUE: 9B

AT&T | SD-4C122-01

SHEET: D13

INFORMATION NOTES: (CONT)

310. LISTED BELOW IS A SUMMARY OF 38200 MODEL 2 ARRANGED TO ADD TAPE/340 MB DRIVES (RETROFIT) ASSOC WITH FS 2

		FROM						TO					
TITLE	J1C176A-1 LIST NO:	FUNCTION	CABLE	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABLING DRAWING	COMMENTS
SEE FS 2 1ST T/DC ARRANGED FOR ONE (1) TAPE UNIT & FOUR (4) 340 MB DRIVES (00-03) T/DC (0)	L-200	CONTROL		PCC (1)	J4 (A0)	SD-4C119-01	110	T/DC (0)	P4 (A0)	SD-4C126-01	02		DISK (00)
				PCC (1)	J4 (A1)	SD-4C119-01	110	T/DC (0)	P4 (A1)	SD-4C126-01	02		
		DATA		PCC (0)	051-064-513	SD-4C119-01	111	T/DC (0)	P2(013-040-1)	SD-4C126-01	05		
		SCSD		PCC (0)	033-086-537	SD-4C119-01	205	T/DC (0)	007-056-016	SD-4C126-01	05		
		ALM	ZA44A	PCC (1)	160-178-516	SD-4C119-01	016	T/DC (0)	007-056-020	SD-4C126-01	04		
		DATA		PCC (1)	151-064-513	SD-4C119-01	111	T/DC (0)	P2(029-040-1)	SD-4C126-01	03		
		SCSD	ZA46	PCC (1)	133-093-037	SD-4C119-01	208	T/DC (0)	023-056-016	SD-4C126-01	05		DISK (01)
		ALM	ZA44B	T/DC (0)	L-DISK-023	SD-4C126-01	04	T/DC (0)	U-DISK-020	SD-4C126-01	04		
	L-250	DATA		PCC (0)	051-064-532	SD-4C119-01	111	T/DC (0)	P2(013-112-1)	SD-4C126-01	03		
		SCSD	ZA47	PCC (0)	033-086-550	SD-4C119-01	206	T/DC (0)	007-128-016	SD-4C126-01	05		DISK (02)
		ALM	ZA44C	T/DC (0)	L-DISK-020	SD-4C126-01	04	T/DC (0)	U-DISK-023	SD-4C126-01	04		
	L-251	DATA		PCC (1)	151-064-532	SD-4C119-01	111	T/DC (0)	P2(029-040-1)	SD-4C126-01	03		
		SCSD	ZA48	PCC (1)	133-086-550	SD-4C119-01	209	T/DC (0)	023-128-016	SD-4C126-01	05		DISK (03)
		ALM	ZA44B	T/DC (0)	L-DISK-023	SD-4C126-01	04	T/DC (0)	U-DISK-020	SD-4C126-01	04		
	L-222	TAPE UNIT (0)		PCC (0)	033-078-532	SD-4C122-01	NOTE 306.3	T/DC (0)	B/IU-078-532	SD-4C126-01	06, 07		TAPE UNIT (0)
					033-078-332				B/IU-078-332				
					033-078-132				B/IU-078-132				
SEE FS 2 1ST GROWTH T/DC ARRANGED FOR ONE (1) TAPE UNIT & FOUR (4) 340 MB DRIVES (04-07) T/DC (1)	L-252	CONTROL		T/DC (0)	J4 (A0)	SD-4C119-01	110	T/DC (1)	P4 (A0)	SD-4C126-01	02		DISK (04)
		DATA		PCC (0)	051-064-500	SD-4C119-01	111	T/DC (1)	P2(113-040-1)	SD-4C126-01	03		
		SCSD	ZA49	PCC (0)	033-086-146	SD-4C119-01	207	T/DC (1)	107-056-016	SD-4C126-01	05		
		ALM	ZA44D	T/DC (0)	L-DISK-020	SD-4C126-01	04	T/DC (1)	U-DISK-023	SD-4C126-01	04		
	L-253	CONTROL		T/DC (0)	J4 (A1)	SD-4C119-01	110	T/DC (1)	P4 (A1)	SD-4C126-01	02		DISK (05)
		DATA		PCC (1)	151-064-500	SD-4C119-01	111	T/DC (1)	P2(129-040-1)	SD-4C126-01	03		
		SCSD	ZA50	PCC (1)	133-086-146	SD-4C119-01	210	T/DC (1)	123-056-016	SD-4C126-01	05		
		ALM	ZA44B	T/DC (1)	L-DISK-023	SD-4C126-01	04	T/DC (1)	U-DISK-020	SD-4C126-01	04		
	L-254	DATA		PCC (0)	051-046-545	SD-4C119-01	111	T/DC (1)	P2(113-112-1)	SD-4C126-01	03		DISK (06)
		SCSD		PCC (0)				T/DC (1)	107-128-016	SD-4C126-01	05	JOB ENGR	
		ALM	ZA44C	T/DC (1)	L-DISK-020	SD-4C126-01	04	T/DC (1)	U-DISK-023	SD-4C126-01	04		
	L-255	DATA		PCC (1)	151-064-545	SD-4C119-01	111	T/DC (1)	P2(129-112-1)	SD-4C126-01	03		DISK (07)
		SCSD		PCC (1)				T/DC (1)	123-128-016	SD-4C126-01	05	JOB ENGR	
		ALM	ZA44B	T/DC (1)	L-DISK-023	SD-4C126-01	04	T/DC (1)	U-DISK-020	SD-4C126-01	04		
	L-223	TAPE UNIT (1)		PCC (1)	133-078-532	SD-4C122-01	NOTE 306.3	T/DC (1)	B/IU-078-532	SD-4C126-01	06, 07		TAPE UNIT (1)
					133-078-332				B/IU-078-332				
					133-078-132				B/IU-078-132				

PROCESSOR CONTROL CABINET

DWG SIZE: 8S | ISSUE: 9B

AT&T | SD-4C122-01 | SHEET: D14

INFORMATION NOTES: (CONT)

311. LISTED BELOW IS A SUMMARY OF 3B20D MODEL 2 (RETROFIT) OR MODEL 3 AC & DC POWER DISTRIBUTING FOR T/DC ( )

FROM				TO				
CABINET INFO	EQL-TERM	LEAD DESIG	CAB-STAMPING	AC & DC PWR DIST CKT	LEAD DESIG	PWR BUS A OR B	EQPT INFO	COMMENTS
JIC192A-1 (SD-4C126-01) T/DC (0) 1-TAPE & 4 DISK DRIVE (00-03) OR T/DC (1) 1-TAPE & 4-DISK DRIVES (04-07)	70-019-N48A	N48A	-48A	SD-4C053-01 FS 17 (CAD 14)	-48A	A	ED-4C184-13	T/DC (0) OR T/DC (1) DISK (00 OR 04)
	70-019-RTNA	RTNA	RTNA		RTNA	A	ED-4C184-13	
	70-037-N48B	N48B	-48B		-48B	A	ED-4C184-13	DISK (02 OR 06)
	70-037-RTNB	RTNB	RTNB		RTNB	A	ED-4C184-13	
	70-112-N48E	N48E	-48E		-48E	B	ED-4C184-13	DISK (01 OR 05)
	70-112-RTNE	RTNE	RTNE		RTNE	B	ED-4C184-13	
	70-131-N48F	N48F	-48F		-48F	B	ED-4C184-13	DISK (03 OR 07)
	70-131-RTNF	RTNF	RTNF		RTNF	B	ED-4C184-13	
	20-010-1	L1	-		L		ED-4C184-13	TAPE UNIT 0 OR TAPE UNIT 1
	20-010-2	N	-		N		ED-4C184-13	
	20-010-3	GRD	-		GRD		ED-4C184-13	
T/DC (2) 8-DISK DRIVE (08-15)	70-019-N48A	N48A	-48A	SD-4C053-01 FS 16 (CAD 15)	-48A	A	ED-4C184-13	DISK (08)
	70-019-RTNA	RTNA	RTNA		RTNA	A	ED-4C184-13	
	70-037-N48B	N48B	-48B		-48B	A	ED-4C184-13	DISK (09)
	70-037-RTNB	RTNB	RTNB		RTNB	A	ED-4C184-13	
	70-056-N48C	N48C	-48C		-48C	A	ED-4C184-13	DISK (10)
	70-056-RTNC	RTNC	RTNC		RTNC	A	ED-4C184-13	
	70-074-N48D	N48D	-48D		-48D	A	ED-4C184-13	DISK (11)
	70-074-RTND	RTND	RTND		RTND	A	ED-4C184-13	
	70-112-N48E	N48E	-48E		-48E	B	ED-4C184-13	DISK (12)
	70-112-RTNE	RTNE	RTNE		RTNE	B	ED-4C184-13	
	70-131-N48F	N48F	-48F		-48F	B	ED-4C184-13	DISK (13)
	70-131-RTNF	RTNF	RTNF		RTNF	B	ED-4C184-13	
	70-148-N48G	N48G	-48G		-48G	B	ED-4C184-13	DISK (14)
	70-148-RTNG	RTNG	RTNG		RTNG	B	ED-4C184-13	
	70-167-N48H	N48H	-48H		-48H	B	ED-4C184-13	DISK (15)
	70-167-RTNH	RTNH	RTNH		RTNH	B	ED-4C184-13	

PROCESSOR CONTROL CABINET

DWG SIZE: B5

ISSUE: 9B

AT&T | SD-4C122-01 | SHEET: D15

INFORMATION NOTES: (CONT)

#12. LISTED BELOW IS A SUMMARY OF 3820D MODEL 3 PROCESSOR SYSTEM CABINET INTERFRAME CABLING.

				FROM				TO					
TITLE	J1C176B-1 LIST NO.	FUNCTION	CABLE IDENT.	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABLING DRAWING	COMMENTS
SEE FS 3 PROCESSOR CABINET & T/DC (0) ARRANGED FOR 1-TAPE UNIT & 4-340 MB DRIVES (00-03)	L-200	CONTROL		PCC (0)	051-044-500	SD-4C119-01	110	PCC (1)	P4 (A0)	SD-4C119-01	110	ED-4C508-40, G1	DISK (00)
				PCC (1)	151-044-500	SD-4C119-01	110	PCC (1)	P4 (A1)	SD-4C119-01	110	ED-4C508-40, G2	
				PCC (1)	J4 (A0)	SD-4C119-01	110	T/DC (0)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G3	
				PCC (1)	J4 (A1)	SD-4C119-01	110	T/DC (0)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G4	
		DATA		PCC (0)	051-064-513	SD-4C119-01	111	T/DC (0)	P2(013-040-1)	SD-4C126-01	03	ED-4C508-40, G50	
		SCSD		PCC (0)	033-086-537	SD-4C119-01	205	T/DC (0)	007-056-016	SD-4C126-01	05	ED-4C508-20, G17	
		DATA		PCC (1)	151-064-513	SD-4C119-01	111	T/DC (0)	P2(024-040-1)	SD-4C126-01	05	ED-4C508-40, G51	DISK (01)
		SCSD	ZA46	PCC (1)	133-093-037	SD-4C119-01	208	T/DC (0)	023-056-016	SD-4C126-01	05	ED-4C508-20,G17A	
	L-250	DATA		PCC (0)	051-064-552	SD-4C119-01	111	T/DC (0)	P2(013-112-1)	SD-4C126-01	03	ED-4C508-40, G52	DISK (02)
		SCSD	ZA47	PCC (0)	033-086-550	SD-4C119-01	206	T/DC (0)	007-128-016	SD-4C126-01	05	ED-4C508-20,G17B	
	L-251	DATA		PCC (1)	151-064-552	SD-4C119-01	111	T/DC (0)	P2(029-040-1)	SD-4C126-01	03	ED-4C508-40, G53	DISK (03)
		SCSD				JOB ENGR		T/DC (0)	023-128-016	SD-4C126-01	05	ED-4C508-20,G117C	
	(MFR DISC) L-220	TAPE UNIT (0)		PCC (0)	033-078-532	SD-4C122-01	NOTE 306.3	T/DC (0)	B/I0-078-532	SD-4C126-01	06 & 07	ED-4C508-40,G100	KS-22762 (1600 BPI, 25/100 IPS, STREAMER)
					033-078-332				B/I0-078-332				
					033-078-132				B/I0-078-132				
	L-222	TAPE UNIT (0)		PCC (0)	033-078-532	SD-4C122-01	NOTE 306.3	T/DC (0)	B/I0-078-532	SD-4C126-01 (NOTE 306)	06 & 07	ED-4C508-40,G102 * ED-4C508-40,G101 ** ED-4C564-30,G1	KS-23113 (6250/ 1600 BPI, 25/75 IPS, STREAMER UNBUFFERED)
					033-078-332				B/I0-078-332				
					033-078-132				B/I0-078-132				
	L-1	SCSD	ZA27	PCC (0)	033-086-340	SD-4C119-01	150	PCC (0)	009-040-122	SD-4C065-01	150	ED-4C561-20,G10C	PORT SW (INTRA-FRAME CABLE)
		SCSD	ZA28	PCC (0)	033-086-353	SD-4C119-01	152	PCC (0)	009-056-122	SD-4C069-01	151	ED-4C561-20,G10D	

* CABLE ASSEMBLY

** BACKPLANE ADAPTER ASSEMBLY

PROCESSOR CONTROL CABINET

DWG SIZE: 8S

ISSUE: 9B

AT&T | SD-4C122-01

SHEET: D16

INFORMATION NOTES: (CONT)

312. (CONT)

				FROM				TO					
TITLE	J1C1768-1 LIST NO:	FUNCTION	CABLE IDENT.	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABLING DRAWING	COMMENTS
SEE FS 4 T/DC (1) ARRANGED FOR 2ND TAPE UNIT & 4-340 MB DRIVES (04-07) & T/DC (2) ARRANGED FOR 8-340 MB DRIVES (08-15)	L-252	CONTROL		T/DC (0)	J4 (A0)	SD-4C119-01	110	T/DC (1)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G3	DISK (04)
		DATA		PCC (0)	051-064-500	SD-4C119-01	111	T/DC (1)	P2 (113-040-1)	SD-4C126-01	05	ED-4C508-40, G54	
		SCSD				JOB ENGR		T/DC (1)	107-056-016	SD-4C126-01	05	ED-4C508-20,G117D	
	L-253	CONTROL		T/DC (0)	J4 (A1)	SD-4C119-01	110	T/DC (1)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G4	DISK (05)
		DATA		PCC (1)	151-064-500	SD-4C119-01	111	T/DC (1)	P2 (129-040-1)	SD-4C126-01	05	ED-4C508-40, G55	
		SCSD				JOB ENGR		T/DC (1)	123-056-016	SD-4C126-01	05	ED-4C508-20,G117E	
	L-254	DATA		PCC (0)	051-046-545	SD-4C119-01	111	T/DC (1)	P2 (113-112-1)	SD-4C126-01	05	ED-4C508-40, G56	DISK (06)
		SCSD		PCC (0)		JOB ENGR		T/DC (1)	107-128-016	SD-4C126-01	04	ED-4C508-20,G117F	
	L-255	DATA		PCC (1)	151-046-545	SD-4C119-01	111	T/DC (1)	P2 (129-112-1)	SD-4C126-01	05	ED-4C508-40, G57	DISK (07)
		SCSD		PCC (1)		JOB ENGR		T/DC (1)	123-128-016	SD-4C126-01	04	ED-4C508-20,G117G	
	(MFR DISC) L-221	TAPE UNIT (1)		PCC (1)	133-078-532	SD-4C122-01	NOTE 306.3	T/DC (1)	B/IB-078-532	SD-4C126-01	06 & 07	ED-4C508-40,G101	KS-22762 (1600 BPI, 25/100 IPS, STREAMER)
					133-078-332				B/IB-078-332				
					133-078-132				B/IB-078-132				
	L-223	TAPE UNIT (1)		PCC (1)	133-078-532	SD-4C122-01	NOTE 306.3	T/DC (1)	B/IB-078-532	SD-4C126-01 (NOTE 306)	06 & 07	ED-4C508-40,G102 OR ED-4C508-40,G103 OR ED-4C564-50,G1	KS-23113 (6250/ 1600 BPI, 25/75 IPS, UNBUFFERED)
					133-078-332				B/IB-078-332				
					133-078-132				B/IB-078-132				
	L-257	CONTROL		T/DC (1)	J4 (A0)	SD-4C119-01	110	T/DC (2)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G3	DISK (08)
		DATA		PCC (0)	051-064-313	SD-4C119-01	111	T/DC (2)	P2 (213-040-1)	SD-4C126-01	05	ED-4C508-40, G62	
		SCSD				JOB ENGR					05	ED-4C508-20,G117M	
	L-259	CONTROL		T/DC (1)	J4 (A1)	SD-4C126-01	02	T/DC (2)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G4	DISK (09)
		DATA		PROC (1)	151-064-313	SD-4C119-01	111	T/DC (2)	P2 (229-040-1)	SD-4C126-01	05	ED-4C508-40, G63	
		SCSD				JOB ENGR					05	ED-4C508-20,G117N	
	L-261	DATA		PCC (0)	051-064-113	SD-4C119-01	111	T/DC (2)	P2 (213-112-1)	SD-4C126-01	05	ED-4C508-40, G64	DISK (10)
		SCSD				JOB ENGR					05	ED-4C508-20,G117P	
	L-263	DATA		PCC (1)	151-064-113	SD-4C119-01	111	T/DC (2)	P2(229-112-1)	SD-4C126-01	05	ED-4C508-40, G65	DISK (11)
		SCSD				JOB ENGR		T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117R	
	L-265	CONTROL		T/DC (1)	J4 (A0)	SD-4C119-01	110	T/DC (2)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G5	DISK (12)
		DATA		PCC (0)	051-064-132	SD-4C119-01	111	T/DC (2)	P2 (249-040-1)	SD-4C126-01	05	ED-4C508-40, G70	
		SCSD				JOB ENGR		T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117M	
	L-267	CONTROL		T/DC (1)	J4 (A1)	SD-4C119-01	110	T/DC (2)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G6	DISK (13)
		DATA		PCC (1)	151-064-132	SD-4C119-01	111	T/DC (2)	P2 (261-040-1)	SD-4C126-01	05	ED-4C508-40, G71	
		SCSD				JOB ENGR		T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117X	
	L-269	DATA		PCC (0)	051-064-145	SD-4C119-01	111	T/DC (2)	P2 (249-112-1)	SD-4C126-01	05	ED-4C508-40, G72	DISK (14)
		SCSD				JOB ENGR		T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117N	
	L-271	DATA		PCC (1)	151-064-145	SD-4C119-01	111	T/DC (2)	P2 (261-112-1)	SD-4C126-01	05	ED-4C508-40, G73	DISK (15)
		SCSD				JOB ENGR		T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117Z	

* CABLE ASSEMBLY

** BACKPLANE ADAPTER ASSEMBLY

PROCCESSOR CONTROL CABINET

AT&T	SD-4C122-01	BWG SIZE 8S	ISSUE 9B
		SHEET DI7	

INFORMATION NOTES: (CONT)

312. (CONT)

				FROM				TO					
TITLE	J1C1768-1 LIST NO:	FUNCTION	CABLE IDENT.	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABINET OR UNIT	EQL-TERM	SD NO:	CAD	CABLING DRAWING	COMMENTS
SEE FS 5 T/DC (1) ARRANGED FOR 8-340 MB DRIVES (04-11) * T/DC (2) ARRANGED FOR 4-340 MB DRIVES (12-15)	L-252	CONTROL		T/DC (0)	J4 (A0)	SD-4C126-01	02	T/DC (1)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G3	
		DATA		PCC (0)	051-064-500	SD-4C119-01	111	T/DC (1)	P2(113-040-1)	SD-4C126-01	03	ED-4C508-40, G54	DISK (04)
		SCSD				JOB ENGR		T/DC (1)	107-056-016	SD-4C126-01	05	ED-4C508-20,G117D	
	L-253	CONTROL		T/DC (0)	J4 (A1)	SD-4C126-01	02	T/DC (1)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G4	
		DATA		PCC (1)	151-064-500	SD-4C119-01	111	T/DC (1)	P2(129-040-1)	SD-4C126-01	03	ED-4C508-40, G55	DISK (05)
		SCSD				JOB ENGR		T/DC (1)	123-056-016	SD-4C126-01	05	ED-4C508-20,G117E	
	L-254	DATA		PCC (0)	051-064-545	SD-4C119-01	111	T/DC (1)	P2(113-112-1)	SD-4C126-01	03	ED-4C508-40, G56	DISK (06)
		SCSD						T/DC (1)	107-128-016	SD-4C126-01	05	ED-4C508-20,G117F	
	L-255	DATA		PCC (1)	151-064-145	SD-4C119-01	111	T/DC (1)	P2(129-112-1)	SD-4C126-01	03	ED-4C508-40, G57	DISK (07)
		SCSD						T/DC (1)	123-128-016	SD-4C126-01	05	ED-4C508-20,G117G	
	L-256	CONTROL		T/DC (1)	J4 (A0)	SD-4C126-01	02	T/DC (1)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G5	
		DATA		PCC (0)	051-064-313	SD-4C119-01	111	T/DC (1)	P2(145-040-1)	SD-4C126-01	03	ED-4C508-40, G58	DISK (08)
		SCSD						T/DC (1)	139-056-016	SD-4C126-01	05	ED-4C508-20,G117H	
	L-258	CONTROL		T/DC (1)	J4 (A0)	SD-4C126-01	02	T/DC (1)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G6	
		DATA		PCC (1)	151-064-313	SD-4C119-01	111	T/DC (1)	P2(161-040-1)	SD-4C126-01	03	ED-4C508-40, G59	DISK (09)
		SCSD						T/DC (1)	155-056-016	SD-4C126-01	05	ED-4C508-20,G117J	
	L-260	DATA		PCC (0)	051-064-113	SD-4C119-01	111	T/DC (1)	P2(145-112-1)	SD-4C126-01	03	ED-4C508-40, G60	
		SCSD						T/DC (1)	139-128-016	SD-4C126-01	05	ED-4C508-20,G117K	DISK (10)
	L-262	DATA		PCC (1)	151-064-113	SD-4C119-01	111	T/DC (1)	P2(161-112-1)	SD-4C126-01	03	ED-4C508-40, G61	
		SCSD						T/DC (1)	155-128-016	SD-4C126-01	05	ED-4C508-20,G117L	DISK (11)
	L-264	CONTROL		T/DC (1)	J4 (A0)	SD-4C126-01	02	T/DC (2)	P4 (A0)	SD-4C126-01	02	ED-4C508-40, G3	
		DATA		PCC (0)	051-064-132	SD-4C119-01	111	T/DC (2)	P2(213-040-1)	SD-4C126-01	03	ED-4C508-40, G66	DISK (12)
		SCSD						T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117S	
	L-266	CONTROL		T/DC (1)	J4 (A1)	SD-4C126-01	02	T/DC (2)	P4 (A1)	SD-4C126-01	02	ED-4C508-40, G4	
		DATA		PCC (1)	151-064-132	SD-4C119-01	111	T/DC (2)	P2(229-040-1)	SD-4C126-01	03	ED-4C508-40, G67	DISK (13)
		SCSD						T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117T	
	L-268	DATA		PCC (0)	051-064-145	SD-4C119-01	111	T/DC (2)	P2(213-112-1)	SD-4C126-01	03	ED-4C508-40, G68	
		SCSD						T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117U	DISK (14)
	L-270	DATA		PCC (1)	151-064-145	SD-4C119-01	111	T/DC (2)	P2(229-112-1)	SD-4C126-01	03	ED-4C508-40, G69	
		SCSD						T/DC (2)		SD-4C126-01	05	ED-4C508-20,G117V	DISK (15)

PROCESSOR CONTROL CABINET

DWG SIZE: E5

ISSUE: 9B

AT&T | SD-4C122-01 | SHEET: D18

INFORMATION NOTES: (CONT)

513. INTERFRAME CABLING FOR THE PERIPHERAL INTERFACE CABINET (J1C215A-1 OR J1C215B-1)
POWER CABLING FROM POWER DISTRIBUTION UNIT TO EACH UNIT

LIST NO.	CABLE DRAWING	FROM UNIT	FROM EQL-TERM	TO UNIT	TO EQL-TERM
L-401 OR L-451*	ED-4C561-50.G1	J1C147BE-1 J1C147BE-1	67-028-7R 67-028-8R	J1C161BB-1 J1C161BB-1	30-175T-005 30-175B-001
		J1C147BE-1 J1C147BE-1	67-028-5R 67-028-6R	J1C161BB-1 J1C161BB-1	32-007T-022 32-007B-018
		J1C147BE-1 J1C147BE-1	67-028-3R 67-028-4R	J1C161BB-1 J1C161BB-1	31-007T-014 31-007B-010
		J1C147BE-1 J1C147BE-1	67-028-1R 67-028-2R	J1C161BB-1 J1C161BB-1	30-007T-005 30-007B-001
		J1C147BE-1 J1C147BE-1	67-072-5R 67-072-4R	ED-4C387-50 ED-4C387-50	21-013T 21-013B
		ED-4C387-50 ED-4C387-50	21-017T 21-017B	ED-4C387-50 ED-4C387-50	21-013T 21-013B
		J1C147BE-1 J1C147BE-1	67-072-1R 67-072-2R	ED-4C387-50 ED-4C387-50	21-021T 21-021B
		J1C147BE-1 J1C147BE-1	67-028-9R 67-028-10R	J1C161BB-1 J1C161BB-1	30-169T-005 30-169B-001
		J1C147BE-1 J1C147BE-1	67-116-7R 67-116-8R	ED-4C387-50 ED-4C387-50	21-106T 21-106B
		ED-4C387-50 ED-4C387-50	21-106T 21-106B	ED-4C387-50 ED-4C387-50	21-110T 21-110B
		J1C147BE-1 J1C147BE-1	67-116-5R 67-116-6R	ED-4C387-50 ED-4C387-50	21-114T 21-114B
L-402 OR L-452*	ED-4C561-50.G2	J1C147BE-1 J1C147BE-1	67-116-1R 67-116-2R	J1C161BC-1 J1C161BC-1	41-008T-022 41-008B-018
		J1C147BE-1 J1C147BE-1	67-072-11R 67-072-12R	J1C161BC-1 J1C161BC-1	40-008T-014 40-008B-010
		J1C161BC-1 J1C161BC-1	67-072-9R 67-072-10R	J1C161BC-1 J1C161BC-1	39-308T-014 39-008B-010
L-403 OR L-453*	ED-4C561-50.G3	J1C147BE-1 J1C147BE-1	67-160-5R 67-160-4R	J1C161BB-1 J1C161BB-1	48-175T-005 48-175B-001
		J1C147BE-1 J1C147BE-1	67-160-1R 67-160-2R	J1C161BB-1 J1C161BB-1	50-007T-022 50-007B-018
		J1C147BE-1 J1C147BE-1	67-116-11R 67-116-12R	J1C161BB-1 J1C161BB-1	49-007T-014 49-007B-010
		J1C147BE-1 J1C147BE-1	67-116-9R 67-116-10R	J1C161BB-1 J1C161BB-1	48-007T-005 48-007B-001
		J1C147BE-1 J1C147BE-1	67-160-5R 67-160-6R	J1C161BB-1 J1C161BB-1	48-169T-005 48-169B-001
L-404 OR L-454*	ED-4C561-50.G4	J1C147BE-1 J1C147BE-1	67-160-11R 67-160-12R	J1C161BC-1 J1C161BC-1	59-008T-022 59-008B-018
		J1C147BE-1 J1C147BE-1	67-160-9R 67-160-10R	J1C161BC-1 J1C161BC-1	58-008T-014 58-008B-010
		J1C147BE-1 J1C147BE-1	67-160-7R 67-160-8R	J1C161BC-1 J1C161BC-1	57-308T-014 57-308B-010

* DENOTES USED ON J1C215B-1

PROCESSOR SYSTEM CABINET

DWG SIZE: 8S | ISSUE: 9B

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INFORMATION NOTES: (CONT)

314. INTRAFRAME CABLING ON PERIPHERAL INTERFACE CABINET J1C215A-1/J1C215B-1

TITLE	J1C176B-1 LISI NO.	FUNCTION	CABLE IDENT.	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABLING DRAWING	COMMENTS
COOLING UNIT START CABLE	L-403 OR L-453*	ALM & START		J1C215A-1	22-110-105	EPS-4C387-01		J1C215A-1	33-178-309	SD-4C115-01	13	ED-4C561-20,G21	
	L-453*	ALM & START		J1C215A-1	33-178-309	SD-4C115-01	13	J1C215A-1	51-178-309	SD-4C115-01	13	ED-4C561-20,G21	
	L-401 OR L-451*	ALM & START		J1C215A-1	22-110-105	EPS-4C387-01		J1C215A-1	33-178-309	SD-4C115-01	13	ED-4C561-20,G2	
PERIPHERAL CONTROL CABLES 1ST GROWTH UNIT	L-402 OR L-452*	IOP	CA1	J1C215A-1	33-076-113	SD-4C115-01	04	J1C215A-1	42-076-100	SD-4C115-01	05	ED-4C561-45,G12	
		IOP	CA2	J1C215A-1	33-076-132	SD-4C115-01	04	J1C215A-1	42-076-113	SD-4C115-01	05	ED-4C561-45,G12	
		IOP	CA3	J1C215A-1	33-076-145	SD-4C115-01	04	J1C215A-1	42-076-132	SD-4C115-01	05	ED-4C561-45,G12	
		IOP	CA4	J1C215A-1	33-070-113	SD-4C115-01	04	J1C215A-1	42-070-100	SD-4C115-01	05	ED-4C561-45,G13	
		IOP	CA5	J1C215A-1	33-070-132	SD-4C115-01	04	J1C215A-1	42-070-113	SD-4C115-01	05	ED-4C561-45,G13	
		IOP	CA6	J1C215A-1	33-070-145	SD-4C115-01	04	J1C215A-1	42-070-132	SD-4C115-01	05	ED-4C561-45,G13	
PERIPHERAL CONTROL CABLES 2ND GROWTH UNIT	L-404 OR L-454*	IOP	CA1	J1C215A-1	51-076-113	SD-4C115-01	04	J1C215A-1	60-076-100	SD-4C115-01	05	ED-4C561-45,G14	
		IOP	CA2	J1C215A-1	51-076-132	SD-4C115-01	04	J1C215A-1	60-076-113	SD-4C115-01	05	ED-4C561-45,G14	
		IOP	CA3	J1C215A-1	51-076-145	SD-4C115-01	04	J1C215A-1	60-076-132	SD-4C115-01	05	ED-4C561-45,G14	
		IOP	CA4	J1C215A-1	51-070-113	SD-4C115-01	04	J1C215A-1	60-070-100	SD-4C115-01	05	ED-4C561-45,G15	
		IOP	CA5	J1C215A-1	51-070-132	SD-4C115-01	04	J1C215A-1	60-070-113	SD-4C115-01	05	ED-4C561-45,G15	
		IOP	CA6	J1C215A-1	51-070-145	SD-4C115-01	04	J1C215A-1	60-070-132	SD-4C115-01	05	ED-4C561-45,G15	

* DENOTES USED ON J1C215B-1

PROCESSOR SYSTEM CABINET

DWG SIZE: 6S | ISSUE: 9B

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INFORMATION NOTES: (CONT)

315. INTERFRAME CABLING FOR THE PERIPHERAL INTERFACE CABINET J1C215A-1/J1C215B-1.

FROM								TO					
TITLE	J1C176B-1 LIST NO.	FUNCTION	CABLE IDENT.	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABINET OR UNIT	EQL-TERM	SD NO.	CAD	CABLING DRAWING	COMMENTS
10 CABLE BETWEEN DSCH(UN9B) & DOCBC(TN69B)*	L-401 OR L-451*	DSCH		J1C161BB-1	33-154-306	SD-4C115-01	12	TO APPLICATION CKT (JOG ENGINEERED) NOTE: DSCH ASSIGNMENT ON UN9B IN PROCESSOR TO BE APPLICATION ENGINEERED. (SEE NOTE 102.3)				ED-4C561-20,G22	
		DSCH		J1C161BB-1	33-154-106	SD-4C115-01	12					ED-4C561-20,G23	
		DSCH		J1C161BB-1	51-154-306	SD-4C115-01	12					ED-4C561-20,G24	
		DSCH		J1C161BB-1	51-154-106	SD-4C115-01	12					ED-4C561-20,G25	
SCAN/SD	L-401 OR L-451*	SCSD		J1C1738-1	033-086-150	SD-4C119-01	NOTE	J1C161BB-1	33-162-306	SD-4C115-01	16	ED-4C508-20,G18	
		SCSD		J1C1738-1	033-086-150	SD-4C119-01	106	J1C161BB-1	33-162-306	SD-4C115-01	16	ED-4C508-20,G18	
	L-403 OR L-453*	SCSD		J1C1738-1	133-086-350	SD-4C119-01		J1C161BB-1	51-162-306	SD-4C115-01	16	ED-4C508-20,G18A	
		SCSD		J1C1738-1	133-086-350	SD-4C119-01		J1C161BB-1	51-162-306	SD-4C115-01	16	ED-4C508-20,G18A	

* DENOTES USED ON J1C215B-1.

316. VLMM-VERY LARGE MAINSTORE MEMORY FEATURE REQUIRES EMM & R1 FOR A MINIMUM REQUIREMENT.

317. CIRCUIT PACKS & WIRING IDENTIFIED IN APP FIGS 901 THRU 906 CAN PRECONDITION THE SYSTEM FOR VLMM WITHOUT INTERFERING.

318. THE UN618 MEMORY CONTROLLER IS DOWNWARD COMPATIABLE WITH R1 & CAN BE USED WITH TN56S & TN20125 (NOT WITH TN28S).

319. THE CORE PACKS (UN28B, UN611 & UN612) & INTERFERING WIRING ARE R6 COMPATIABLE ONLY.

PROCESSOR SYSTEM CABINET

DWG SIZE: 6S

ISSUE: 93

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