

0 1 2 3 4 5 6 7 8 9

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OPTION INDEX

| APP OR WRG | RATED ON ISSUE | REF NOTES | LOCATION |
|------------|----------------|-----------|------------|
| 3 | 1 | | APP FIG. 3 |
| Z | 1 | | FS 1, FS 2 |
| Y | 1 | | FS 1, FS 2 |
| X | 1 | | FS 1, FS 2 |
| W | 1 | 306 | FS 1, FS 2 |
| V | 1 | | FS 1, FS 2 |
| U | 1 | | FS 1, FS 2 |
| T | 1 | | FS 1, FS 2 |
| S | 1 | | UNASSIGNED |
| R | 1 | | UNASSIGNED |
| Q | 1 | | UNASSIGNED |
| P | 3AC | | FS 1, FS 2 |
| N | 3AC | | FS 1, FS 2 |
| M | 3AC | | FS 1, FS 2 |
| K | 3AC | 305 | FS 1, FS 2 |
| J | 3AC | 305 | FS 1, FS 2 |
| G | 5B | 306 | FS 1, FS 2 |
| F | 5B | | FS 1, FS 2 |
| E | 5B | | FS 1, FS 2 |
| D | 5B | | FS 1, FS 2 |
| B | 5B | | FS 1, FS 2 |
| A | 5B | | FS 1, FS 2 |
| ZA | 5B | | FS 1, FS 2 |
| ZB | 6A | | FS 1, FS 2 |
| ZC | 6A | | FS 1, FS 2 |
| ZD | 7B | | FS 1, FS 2 |
| ZE | 7B | | FS 1, FS 2 |
| ZF | 8B | | FS 1, FS 2 |
| ZG | 8B | | FS 1, FS 2 |
| ZH | 8B | | FS 1, FS 2 |
| ZJ | 8B | | FS 1, FS 2 |
| ZK | 8B | | FS 1, FS 2 |
| ZL | 8B | | FS 1, FS 2 |
| ZM | 8B | | FS 1, FS 2 |
| ZN | 8B | | FS 1, FS 2 |
| ZP | 8B | | FS 1, FS 2 |
| ZQ | 8B | | FS 1, FS 2 |
| ZT | 9B | | FS 1, FS 2 |
| ZU | 9B | | FS 1, FS 2 |

| DWG ISS | CD ISS | DWG ISS | CD ISS | DWG ISS | CD ISS |
|---------|--------|-----------|--------|---------|--------|
| 1 | 1 | 2A | 2A | 3AC | 2A |
| 4B | 2A | 5B | 2A | 6A | 2A |
| 7B | 2A | 5B | APPX | | APPX |
| DWG ISS | CD ISS | DATE ISSD | DRN | APP | |
| 8B | 2 | 6-2-95 | | | |
| 9B | 2 | 5-28-96 | | | |

SUPPORTING INFORMATION

| SYSTEM USED ON | DESIGN CONTROL | CATEGORY | NO |
|----------------|----------------|-------------------|--------------------------------------|
| 5ESS 5ESS | IH IH | EQUIPMENT DRAWING | J5D003BH SD5D118-01 SD5D118-03 |

SHEET INDEX NOTES

- ONLY THE LATEST ISSUE, OR ISSUES IF CONCURRENT, ARE SHOWN IN THE INDEX.
- FOR REISSUES, A CHANGED OR NEW SHEET IS ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.
- THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

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BT13

**5ESS[®] SWITCHING EQUIPMENT
MEMORY EXPANSION UNIT
CIRCUIT**

| | |
|--------------------------|--------------------|
| DWG SIZE C2 | ISSUE 9B |
| AT&T | SD-5D048-01 |
| SHEET A1 OF 37 | |

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DESIGNATION MNEMONICS INDEX

| <u>MNEMONIC</u> | <u>DEFINITION</u> | <u>MNEMONIC</u> | <u>DEFINITION</u> | <u>MNEMONIC</u> | <u>DEFINITION</u> |
|-----------------|---|-----------------|---|-----------------|---|
| +5V04C(0,1) | PLUS 5 VOLTS, CONTROLLER | 0SID560 | SIDE 0, SEPARATE INSTRUCTION AND DATA, TN56 | 1SID560 | SIDE 1, SEPARATE INSTRUCTION AND DATA, TN56 |
| -4804LC(0,1) | MINUS 48 VOLTS, CONTROLLER | 0TN281 | SIDE 0, TN28 EQUIPPED, ACTIVE HIGH | 1TN281 | SIDE 1, TN28 EQUIPPED, ACTIVE HIGH |
| 08MB0 | SIDE 0, 8MBYTE MEMORY ARRAY INDICATOR | 0WB00 | SIDE 0, BYTE WRITE ENABLE | 1WB00 | SIDE 1, BYTE WRITE ENABLE |
| GRD | GROUND | 0WH0 | SIDE 0, HAMMING WRITE ENABLE | 1WH0 | SIDE 1, HAMMING WRITE ENABLE |
| SPARE(01-27) | SPARE NET | 18MB0 | SIDE 1, 8MBYTE MEMORY ARRAY INDICATOR | | |
| 0ADPC00 | SIDE 0, COLUMN ADDRESS PARITY | 1ADPC00 | SIDE 1, COLUMN ADDRESS PARITY | | |
| 0ADPRO0 | SIDE 0, ROW ADDRESS PARITY | 1ADPRO0 | SIDE 1, ROW ADDRESS PARITY | | |
| 0A02IB | SIDE 0, MEMORY ADDRESS BIT 02B | 1A02IB | SIDE 1, MEMORY ADDRESS BIT 02B | | |
| 0A02IB0 | SIDE 0, MEMORY ADDRESS BIT 02B, ACTIVE LOW | 1A02IB0 | SIDE 1, MEMORY ADDRESS BIT 02B, ACTIVE LOW | | |
| 0A02IB1 | SIDE 0, MEMORY ADDRESS BIT 02B, ACTIVE HIGH | 1A02IB1 | SIDE 1, MEMORY ADDRESS BIT 02B, ACTIVE HIGH | | |
| 0A02I0 | SIDE 0, MEMORY ADDRESS BIT 02, ACTIVE LOW | 1A02I0 | SIDE 1, MEMORY ADDRESS BIT 02, ACTIVE LOW | | |
| 0A(03-21)11 | SIDE 0, MEMORY ADDRESS BIT 03 THROUGH 21, ACTIVE HIGH | 1A(03-21)11 | SIDE 1, MEMORY ADDRESS BIT 03 THROUGH 21, ACTIVE HIGH | | |
| 0A19I0 | SIDE 0, MEMORY ADDRESS BIT 19, ACTIVE LOW | 1A19I0 | SIDE 1, MEMORY ADDRESS BIT 19, ACTIVE LOW | | |
| 0A22I1 | SIDE 0, MEMORY ADDRESS BIT 22, ACTIVE HIGH | 1A22I1 | SIDE 1, MEMORY ADDRESS BIT 22, ACTIVE HIGH | | |
| 0BDI(0-3)0 | SIDE 0, BUFFERED PARITY BIT, BYTE (0-3) | 1BDI(0-3)0 | SIDE 1, BUFFERED PARITY BIT, BYTE (0-3) | | |
| 0BD(A,X,Y,Z)0 | SIDE 0, BUFFERED HAMMING BITS A, X, Y, AND Z | 1BD(A,X,Y,Z)0 | SIDE 1, BUFFERED HAMMING BITS A, X, Y, AND Z | | |
| 0BD(00-31)B1 | SIDE 0, BUFFERED MEMORY DATA BIT (00-31), ACTIVE HIGH | 1BD(00-31)B1 | SIDE 1, BUFFERED MEMORY DATA BIT (00-31), ACTIVE HIGH | | |
| 0C(+,-)CP04L | SIDE 0, CURRENT PROGRAM BUS | 1C(+,-)CP04L | SIDE 1, CURRENT PROGRAM BUS | | |
| 0CAL(M,MP) | SIDE 0, CONVERTER ALARM | 1CAL(M,MP) | SIDE 1, CONVERTER ALARM | | |
| 0COOS(04L,48P) | SIDE 0, CONVERTER OUT OF SERVICE | 1COOS(04L,48P) | SIDE 1, CONVERTER OUT OF SERVICE | | |
| 0CSC04L | SIDE 0, CONVERTER SET POINT CONVERTER | 1CSC04L | SIDE 1, CONVERTER SET POINT CONVERTER | | |
| 0CS(1-3) | CONVERTER REMOTE SWITCH | 1CS(1-3) | CONVERTER REMOTE SWITCH | | |
| 0C48R04L | SIDE 0, 48 VOLT RETURN | 1C48R04L | SIDE 1, 48 VOLT RETURN | | |
| 0DI(0-3)0 | SIDE 0, PARITY BIT, BYTE (0-3) | 1DI(0-3)0 | SIDE 1, PARITY BIT, BYTE (0-3) | | |
| 0D(A,X,Y,Z)0 | SIDE 0, HAMMING BIT A, X, Y, AND Z | 1D(A,X,Y,Z)0 | SIDE 1, HAMMING BIT A, X, Y, AND Z | | |
| 0D(00-31)B1 | SIDE 0, MEMORY DATA BIT (00-31), ACTIVE HIGH | 1D(00-31)B1 | SIDE 1, MEMORY DATA BIT (00-31), ACTIVE HIGH | | |
| 0EXTRA4 | SIDE 0, EXTRA NET | 1EXTRA4 | SIDE 1, EXTRA NET | | |
| 0GO(B-D)11 | SIDE 0, MEMORY GO LEAD (B-D), ACTIVE HIGH | 1GO(B-D)11 | SIDE 1, MEMORY GO LEAD (B-D), ACTIVE HIGH | | |
| 0GSMEQ0 | SIDE 0, GLOBAL SERVICE MODULE EQUIPPED | 1GSMEQ0 | SIDE 1, GLOBAL SERVICE MODULE EQUIPPED | | |
| 0INTC | SIDE 0, INTERLOCK CONVERTER | 1INTC | SIDE 1, INTERLOCK CONVERTER | | |
| 0INTR | SIDE 0, INTERLOCK RETURN | 1INTR | SIDE 1, INTERLOCK RETURN | | |
| 0MESEL(A-J)0 | SIDE 0, MEMORY SELECT (A-J), ACTIVE LOW | 1MESEL(A-J)0 | SIDE 1, MEMORY SELECT (A-J), ACTIVE LOW | | |
| 0READI1 | SIDE 0, MEMORY READ ENABLE | 1READI1 | SIDE 1, MEMORY READ ENABLE | | |
| 0REFI0 | SIDE 0, MEMORY REFRESH | 1REFI0 | SIDE 1, MEMORY REFRESH | | |
| 0SID4M(A,B) | SIDE 0, SEPARATE INSTRUCTION AND DATA, 4 MEGABYTE (A,B) | 1SID4M(A,B) | SIDE 1, SEPARATE INSTRUCTION AND DATA, 4 MEGABYTE (A,B) | | |
| 0SID4M0 | SIDE 0, SEPARATE INSTRUCTION AND DATA, 4 MEGABYTE | 1SID4M0 | SIDE 1, SEPARATE INSTRUCTION AND DATA, 4 MEGABYTE | | |
| 0SID56(A,B) | SIDE 0, SEPARATE INSTRUCTION AND DATA, TN56, (A,B) | 1SID56(A,B) | SIDE 1, SEPARATE INSTRUCTION AND DATA, TN56, (A,B) | | |

| | | |
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| MEMORY EXPANSION UNIT | | |
| DWG SIZE C2 | ISSUE 8B | |
| AT&T | SD-5D048-01 | SHEET A2 |

0 1 2 3 4 5 6 7 8 9

A
B
C
D
E
F
G
H

| DESIG | LOCATION | | | |
|--------|----------|---------|------|------------|
| | LOC | APP FIG | EQPT | OPT |
| TN28 | 04-022 | 3 | | (Z) |
| TN28 | 04-028 | 3 | | (Y) |
| TN28 | 04-034 | 3 | | (X) |
| TN28 | 04-046 | 3 | | (W) |
| TN28 | 04-052 | 3 | | (V) |
| TN28 | 05-058 | 3 | | (U) |
| TN28 | 04-064 | 3 | | (T) |
| SPARE | 04-070 | 1 | | |
| SPARE | 04-076 | 1 | | |
| TN1370 | 04-082 | 2 | | (ZC) |
| TN1370 | 04-100 | 2 | | (ZC) |
| SPARE | 04-106 | 1 | | |
| SPARE | 04-112 | 1 | | |
| TN28 | 04-118 | 3 | | (T) |
| TN28 | 04-124 | 3 | | (U) |
| TN28 | 04-130 | 3 | | (V) |
| TN28 | 04-136 | 3 | | (W) |
| TN28 | 04-148 | 3 | | (X) |
| TN28 | 04-154 | 3 | | (Y) |
| TN28 | 04-160 | 3 | | (Z) |
| TN56 | 04-022 | 3 | | (P) |
| TN56 | 04-028 | 3 | | (N) |
| TN56 | 04-034 | 3 | | (M) |
| TN56 | 04-046 | 3 | | (G) |
| TN56 | 04-052 | 3 | | (F) |
| TN56 | 05-058 | 3 | | (E) |
| TN56 | 04-064 | 3 | | (D) |
| TN56 | 04-118 | 3 | | (D) |
| TN56 | 04-124 | 3 | | (E) |
| TN56 | 04-130 | 3 | | (F) |
| TN56 | 04-136 | 3 | | (G) |
| TN56 | 04-148 | 3 | | (M) |
| TN56 | 04-154 | 3 | | (N) |
| TN56 | 04-160 | 3 | | (P) |
| TN2012 | 04-022 | 3 | | (B) |
| TN2012 | 04-028 | 3 | | (A) |
| TN2012 | 04-034 | 3 | | (ZA) |
| TN2012 | 04-148 | 3 | | (ZA) |
| TN2012 | 04-154 | 3 | | (A) |
| TN2012 | 04-160 | 3 | | (B) |
| 495FB | 04-016 | 2 | | |
| 495FB | 04-178 | 2 | | |
| TN1419 | 04-058 | 5 | | (ZH) |
| TN1419 | 04-064 | 5 | | (ZH) |
| TN1419 | 04-124 | 5 | | (ZH) |
| TN1419 | 04-118 | 5 | | (ZH) |
| TN1409 | 04-046 | 6 | | (ZL,ZM,ZT) |
| TN1409 | 04-052 | 6 | | (ZL,ZM,ZT) |
| TN1409 | 04-130 | 6 | | (ZL,ZM,ZT) |
| TN1409 | 04-136 | 6 | | (ZL,ZM,ZT) |

| DESIG | LOCATION | | | |
|----------------|------------|---------|------|------|
| | LOC | APP FIG | EQPT | OPT |
| 6122-111-09320 | 04-082-332 | | | (ZK) |
| 6122-111-09330 | 04-082-313 | | | (ZK) |
| 6122-111-09330 | 04-082-113 | | | (ZK) |
| 6122-111-09320 | 04-100-332 | | | (ZK) |
| 6122-111-09330 | 04-100-313 | | | (ZK) |
| 6122-111-09330 | 04-100-113 | | | (ZK) |

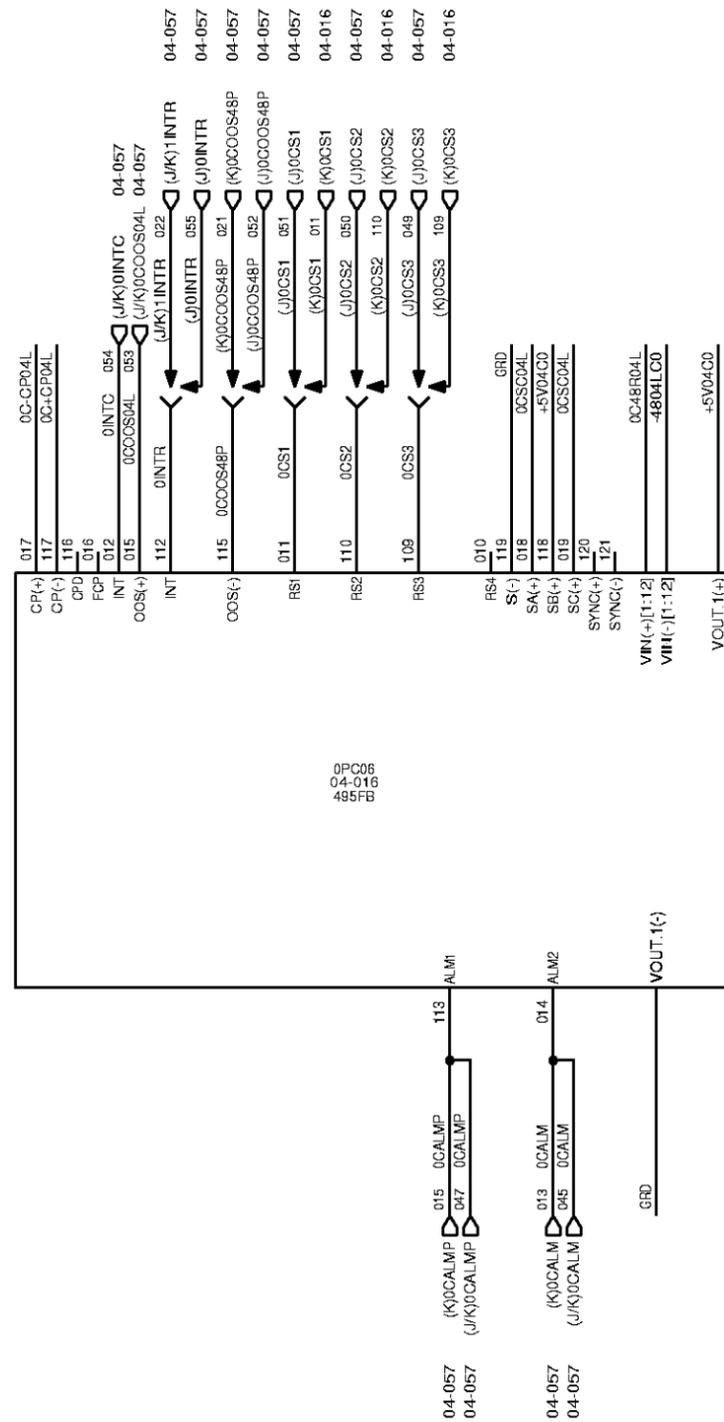
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| | C2 | 9B |
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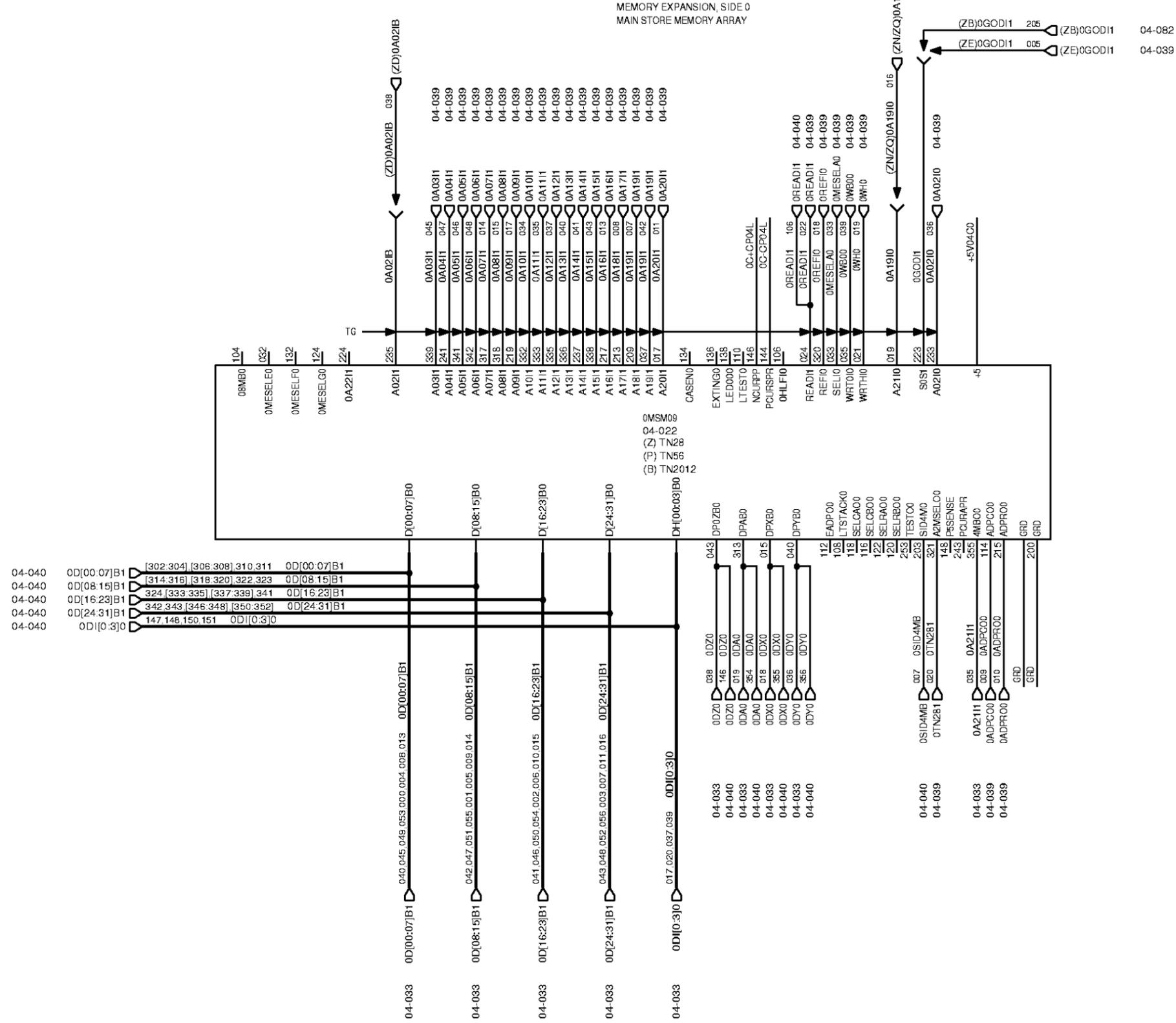
MEMORY EXPANSION, SIDE 0
POWER CONVERTER



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PART OF FS 1

MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY

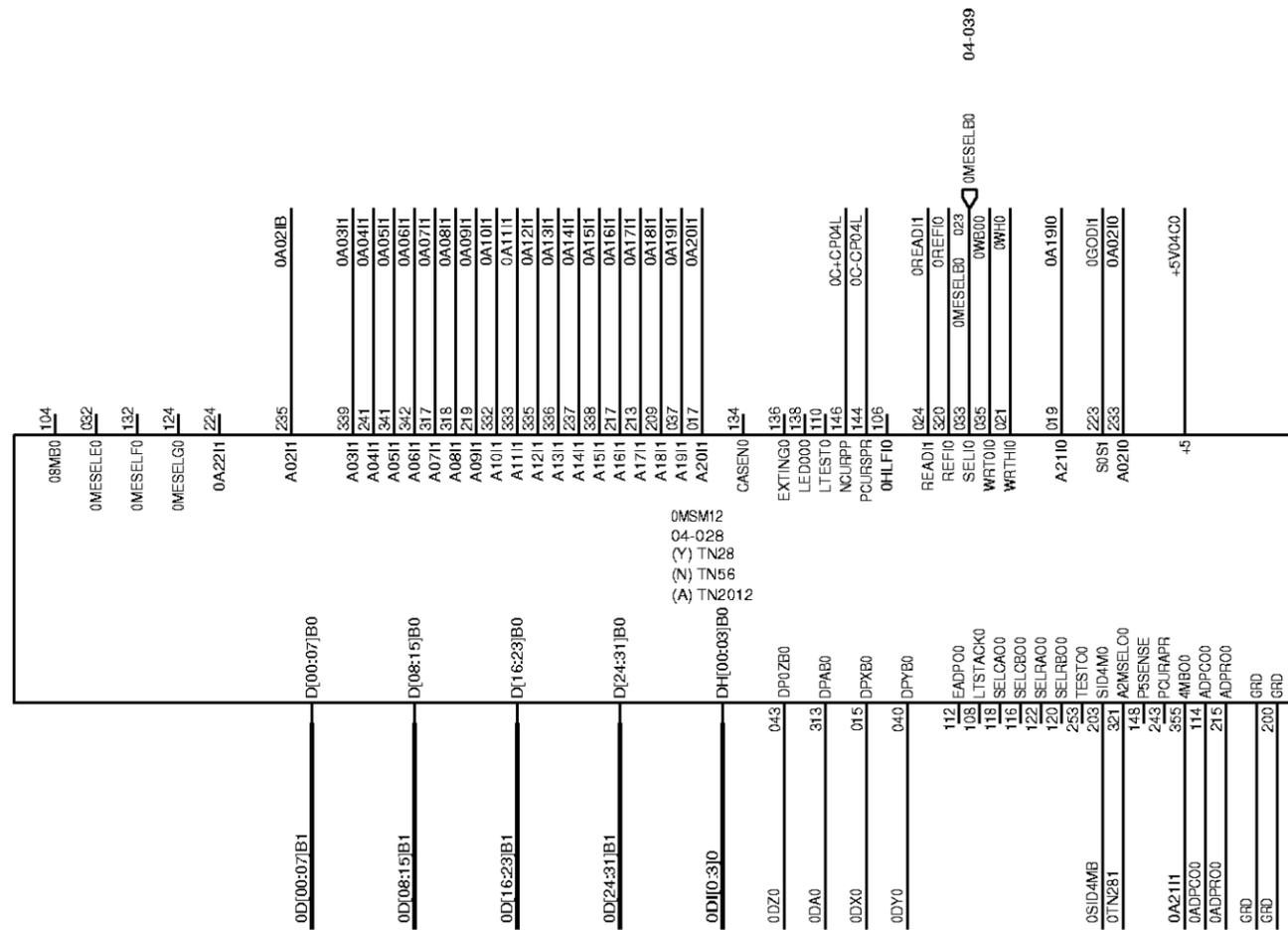


04-040 0D[00:07]B1 [302:304] [306:308] 310,311 0D[00:07]B1
 04-040 0D[08:15]B1 [314:316] [318:320] 322,323 0D[08:15]B1
 04-040 0D[16:23]B1 324 [333:335] [337:339] 341 0D[16:23]B1
 04-040 0D[24:31]B1 342,343 [346:348] [350:352] 0D[24:31]B1
 04-040 0D[0:3]0 147,148,150,151 0D[0:3]0

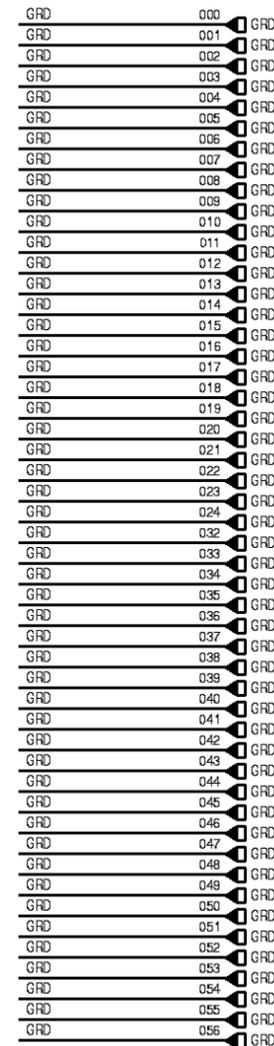
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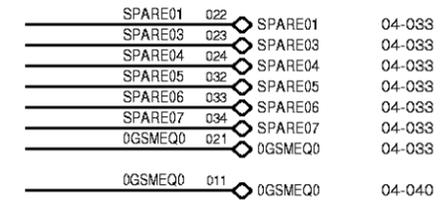
MEMORY EXPANSION, SIDE 0
 MAIN STORE MEMORY ARRAY
 DATA SIGNAL, SIDE 0



TF 04-032



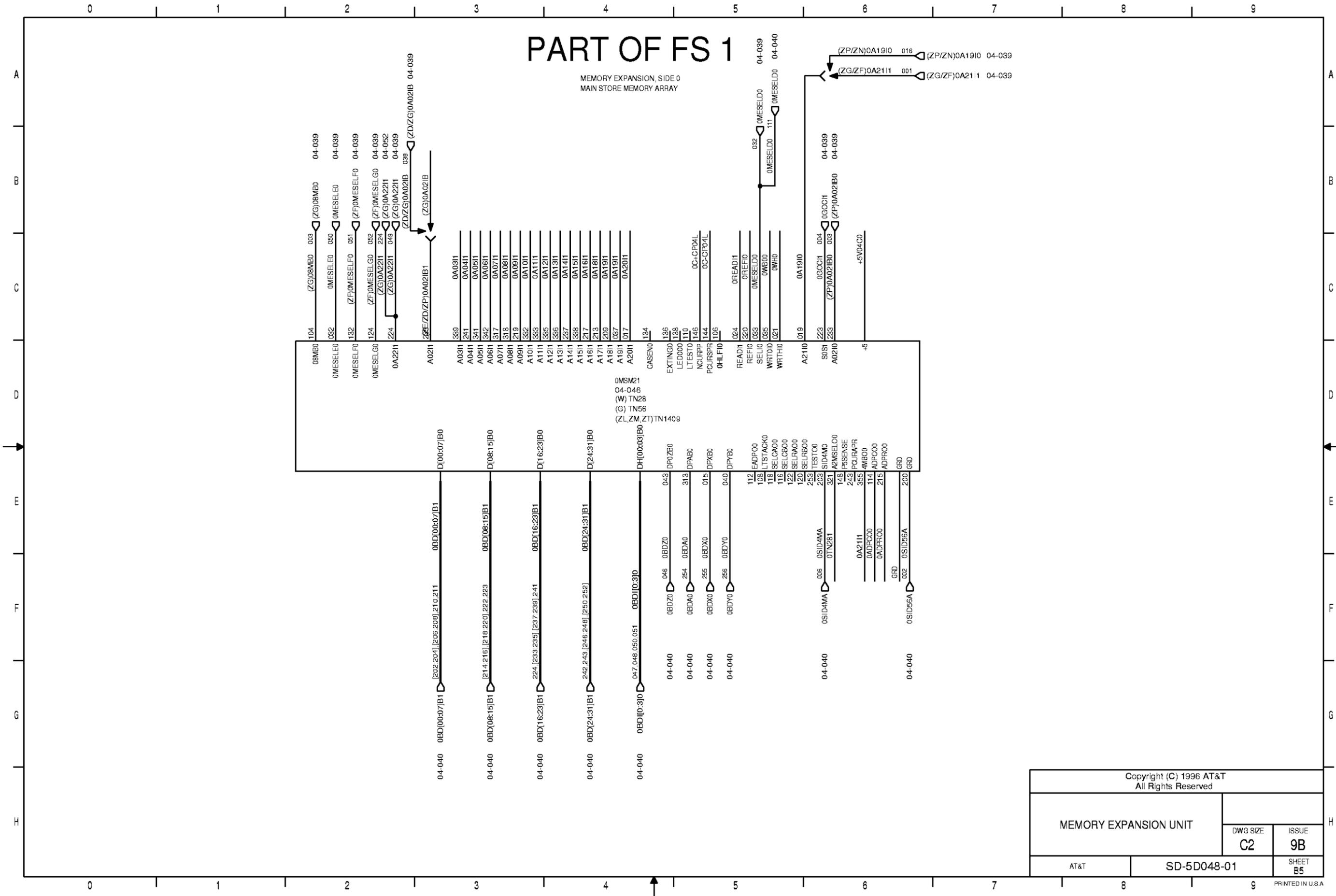
TF 04-033



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| | C2 | 8B |
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PART OF FS 1

MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY



0MSM21
04-046
(W) TN28
(G) TN56
(ZL, ZM, ZT) TN1409

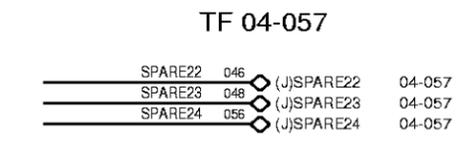
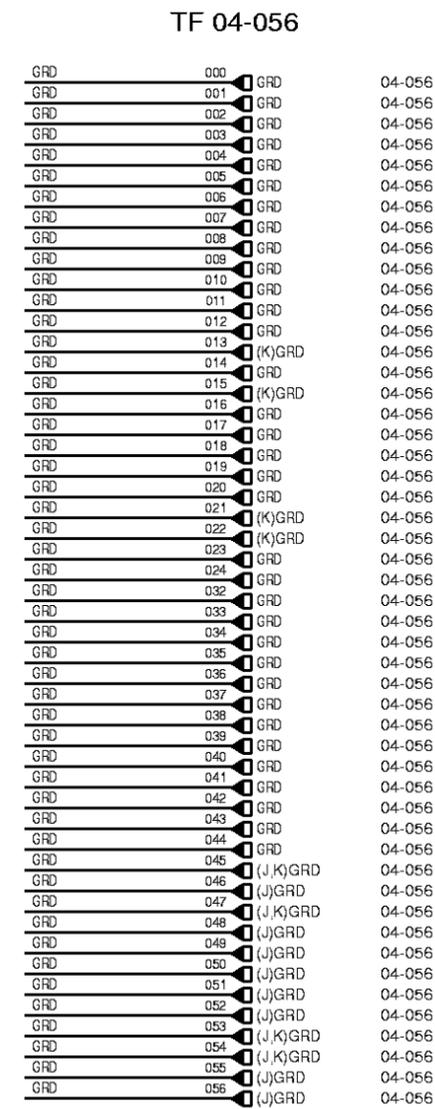
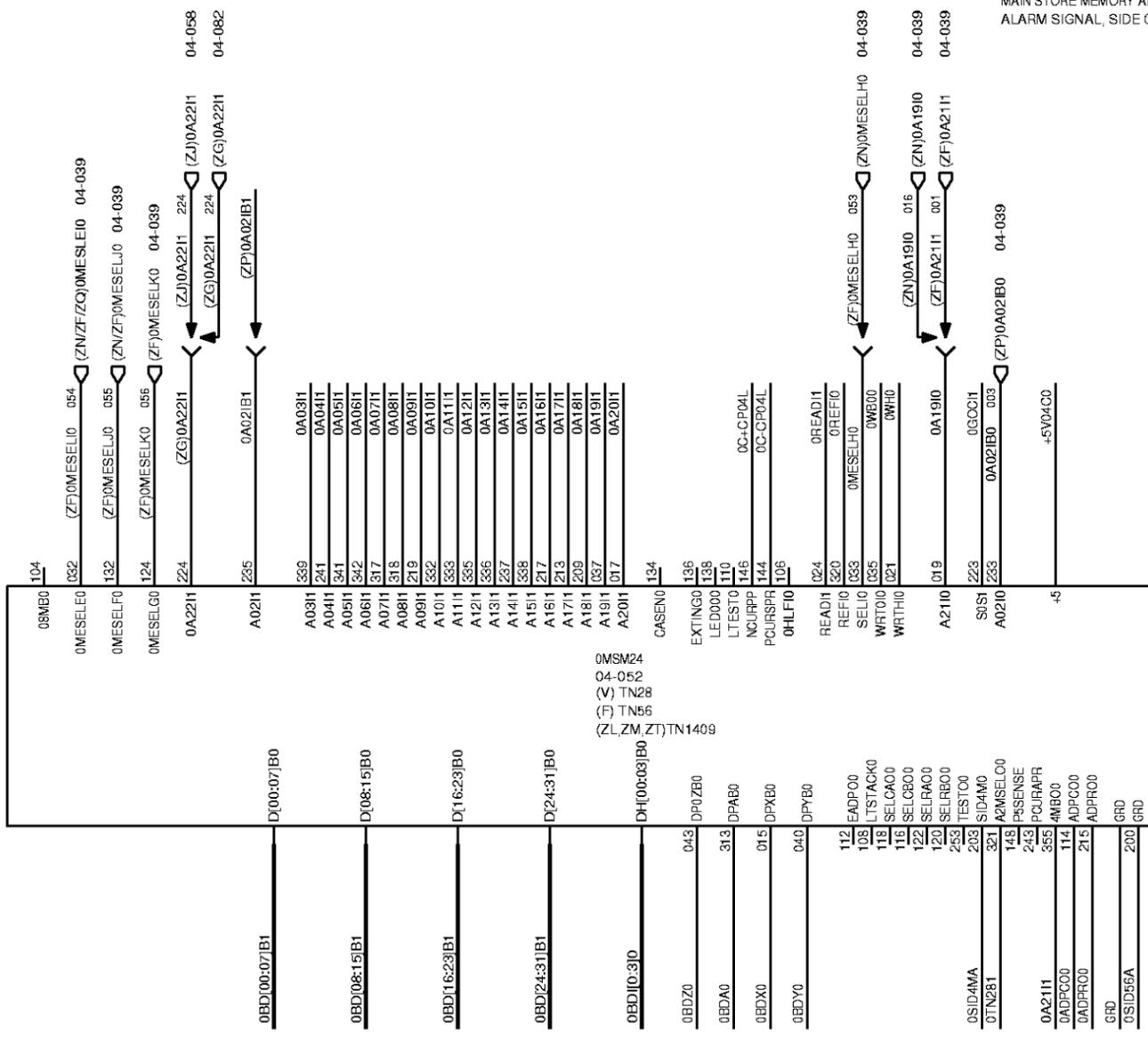
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PART OF FS 1

MEMORY EXPANSION, SIDE 0
 MAIN STORE MEMORY ARRAY
 ALARM SIGNAL, SIDE 0

A
B
C
D
E
F
G
H

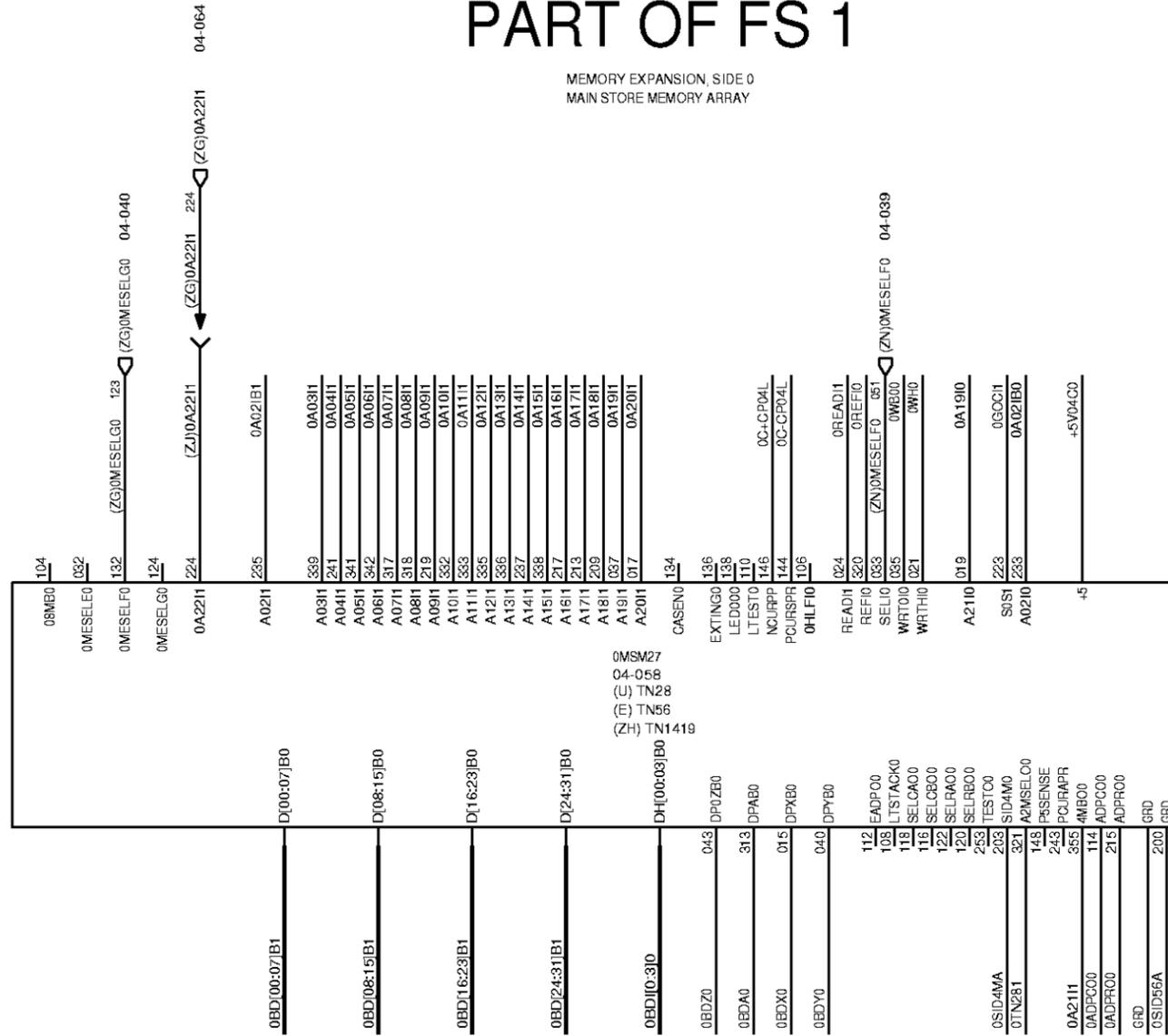
A
B
C
D
E
F
G
H



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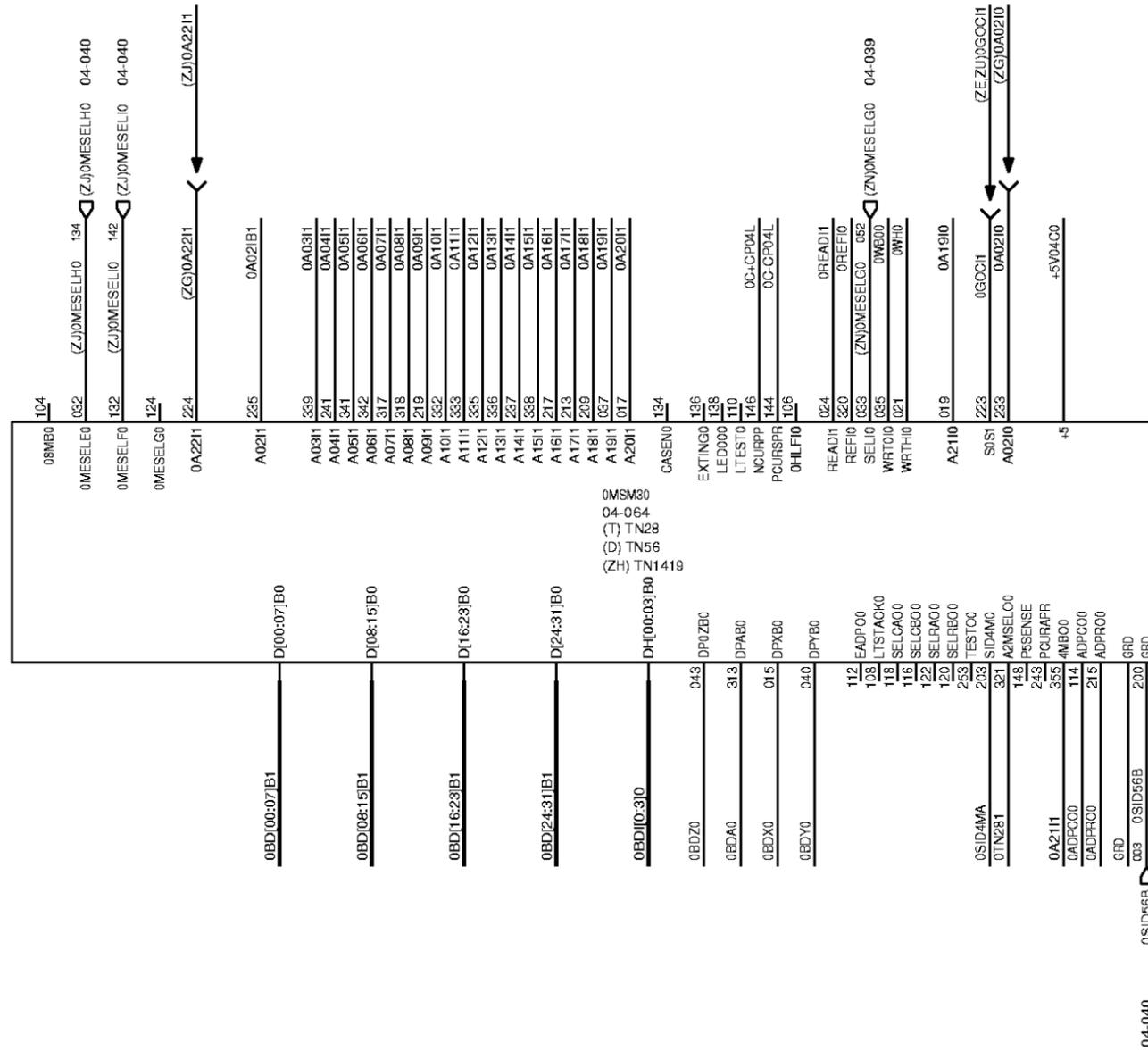
MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY



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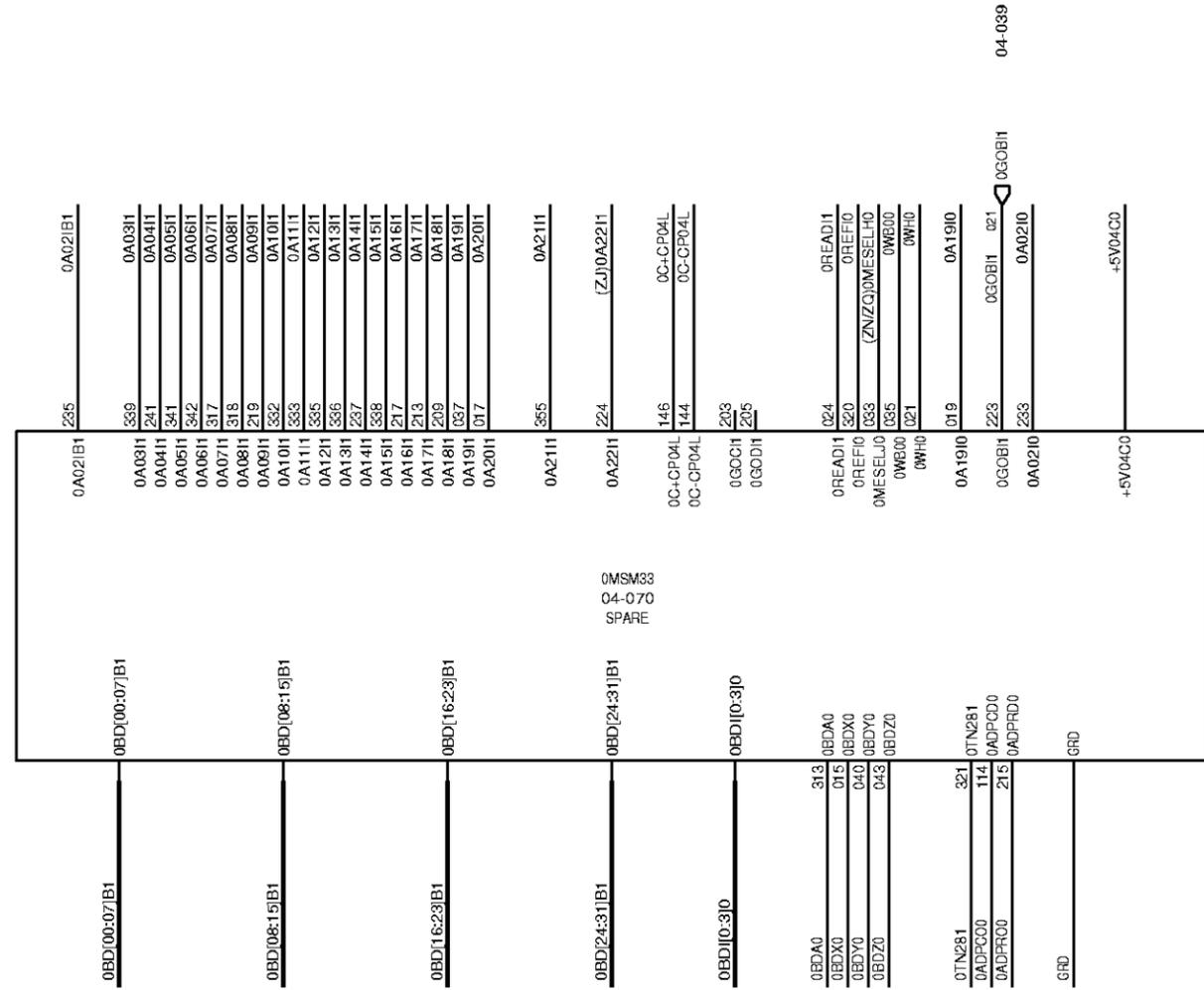
MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY



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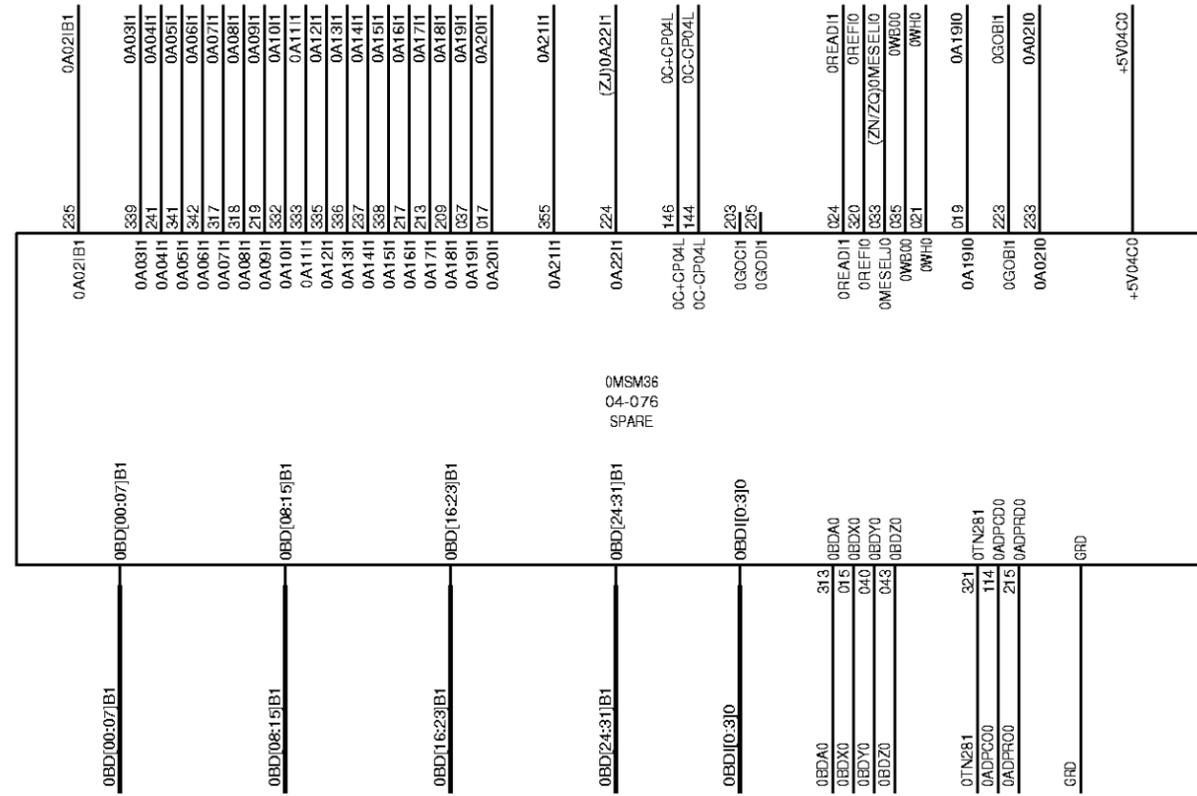
MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY



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| | C2 | 8B |
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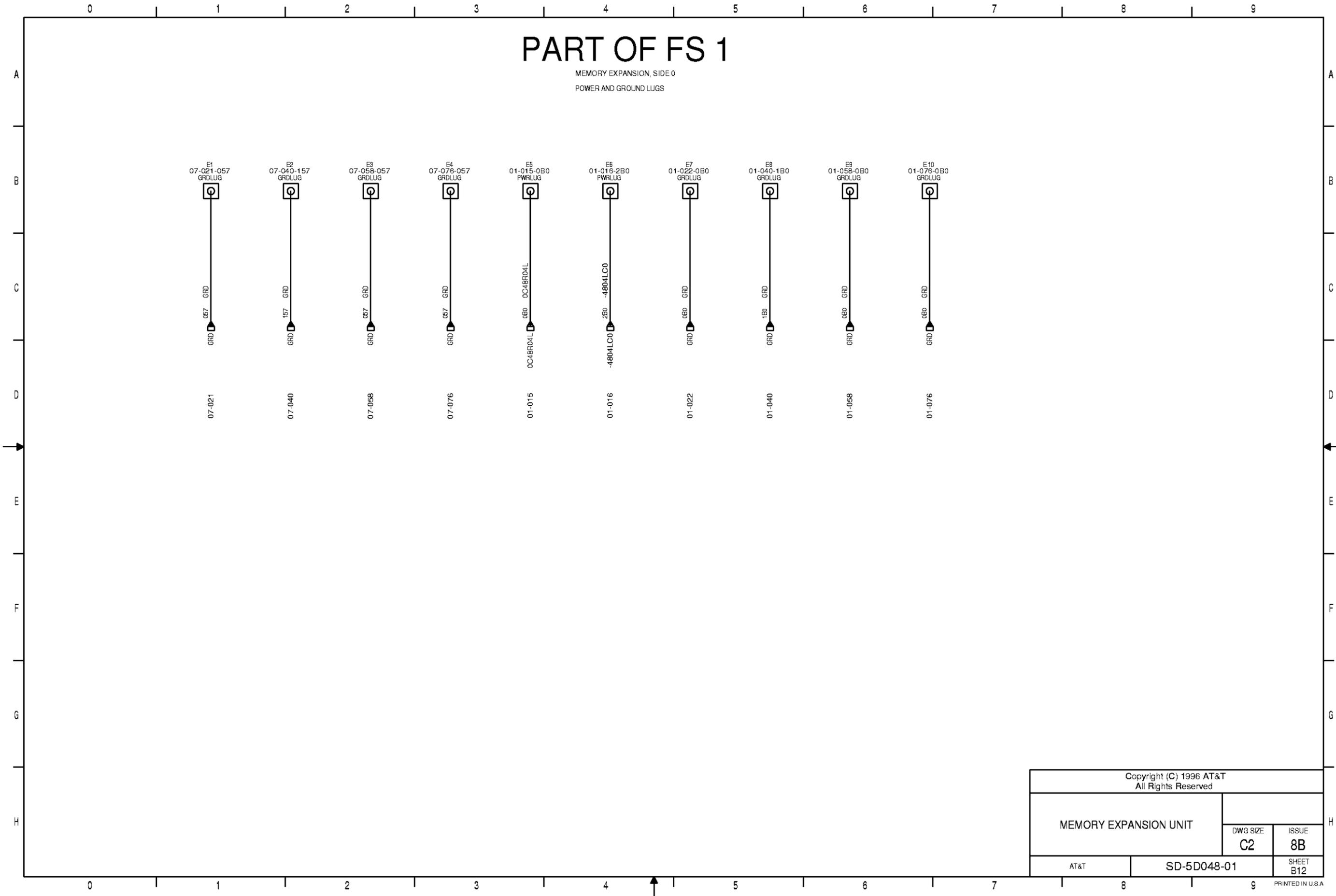
MEMORY EXPANSION, SIDE 0
MAIN STORE MEMORY ARRAY



| | | |
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| | C2 | 8B |
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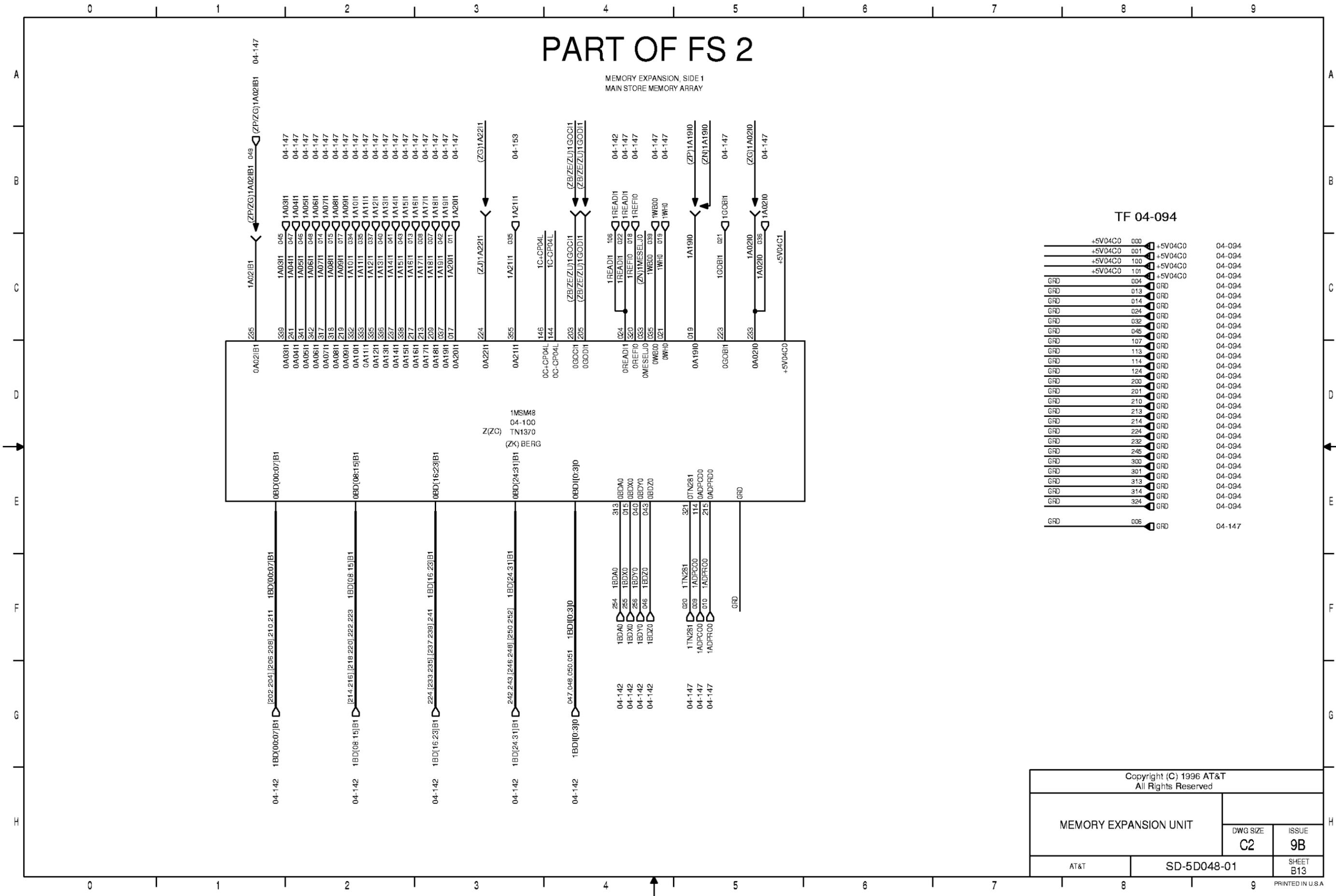
MEMORY EXPANSION, SIDE 0
POWER AND GROUND LUGS



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| | C2 | 8B |
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PART OF FS 2

MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



TF 04-094

| | | | |
|---------|-----|---------|--------|
| +5V04C0 | 000 | +5V04C0 | 04-094 |
| +5V04C0 | 001 | +5V04C0 | 04-094 |
| +5V04C0 | 100 | +5V04C0 | 04-094 |
| +5V04C0 | 101 | +5V04C0 | 04-094 |
| GRD | 004 | GRD | 04-094 |
| GRD | 013 | GRD | 04-094 |
| GRD | 014 | GRD | 04-094 |
| GRD | 024 | GRD | 04-094 |
| GRD | 032 | GRD | 04-094 |
| GRD | 045 | GRD | 04-094 |
| GRD | 107 | GRD | 04-094 |
| GRD | 113 | GRD | 04-094 |
| GRD | 114 | GRD | 04-094 |
| GRD | 124 | GRD | 04-094 |
| GRD | 200 | GRD | 04-094 |
| GRD | 201 | GRD | 04-094 |
| GRD | 210 | GRD | 04-094 |
| GRD | 213 | GRD | 04-094 |
| GRD | 214 | GRD | 04-094 |
| GRD | 224 | GRD | 04-094 |
| GRD | 232 | GRD | 04-094 |
| GRD | 245 | GRD | 04-094 |
| GRD | 300 | GRD | 04-094 |
| GRD | 301 | GRD | 04-094 |
| GRD | 313 | GRD | 04-094 |
| GRD | 314 | GRD | 04-094 |
| GRD | 324 | GRD | 04-094 |
| GRD | 006 | GRD | 04-147 |

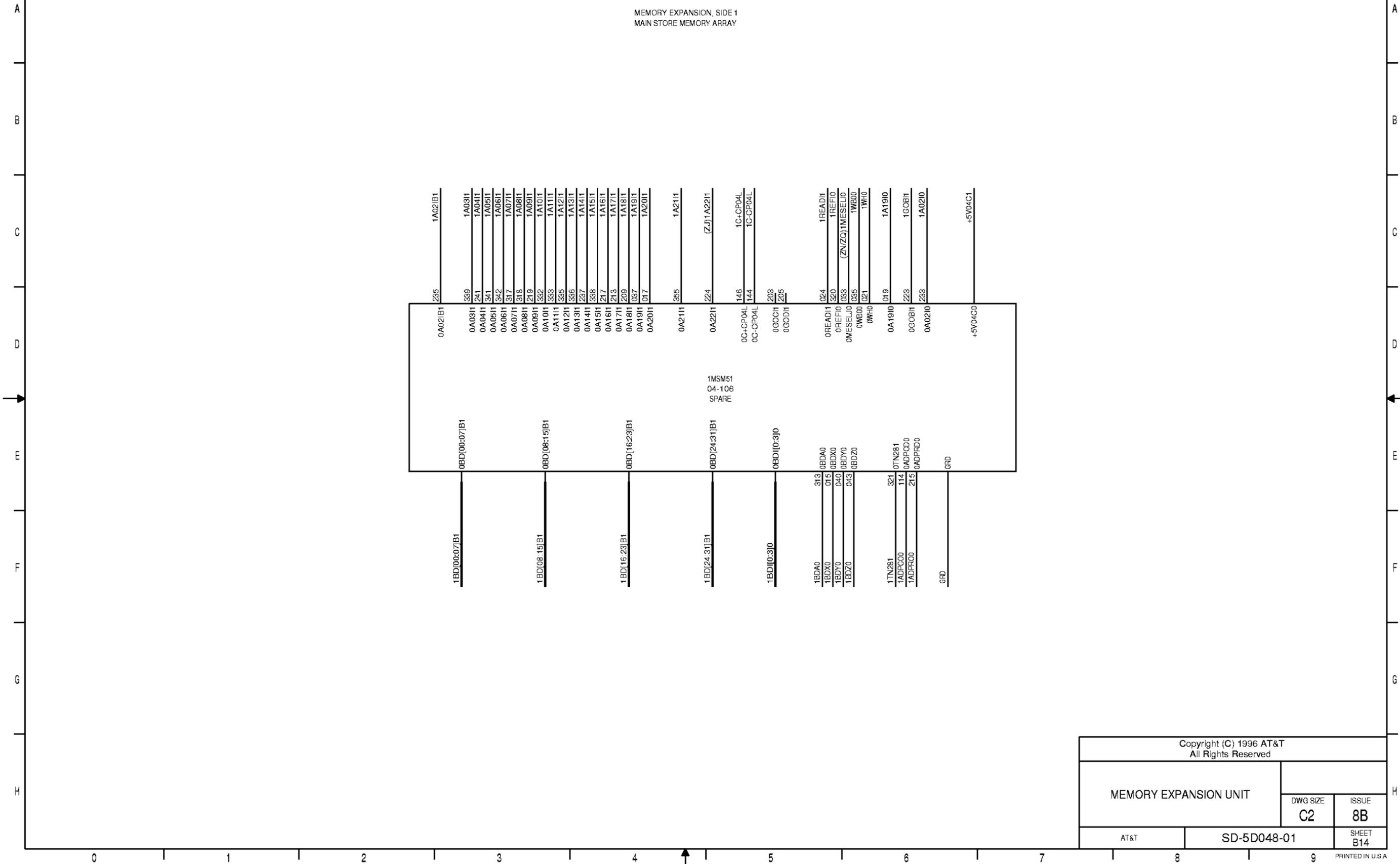
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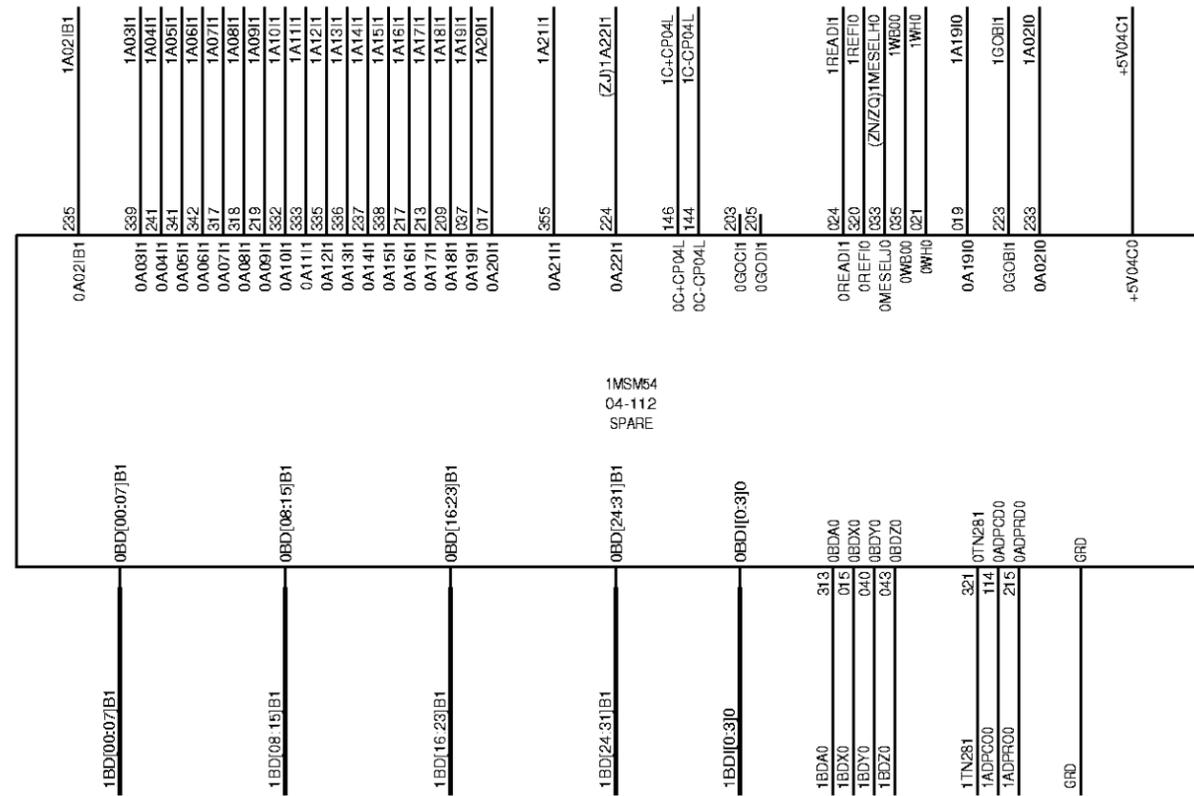
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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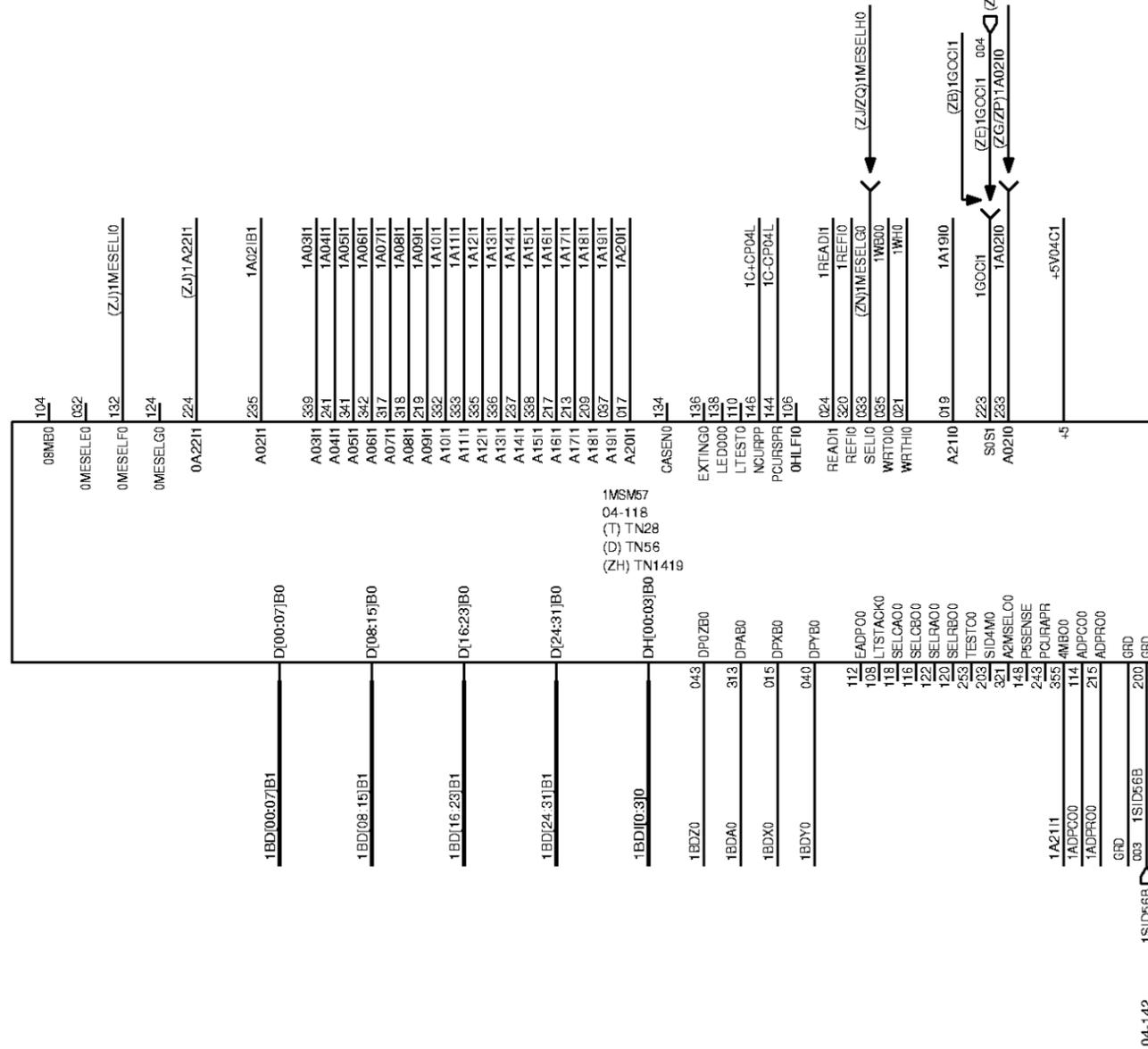
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



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| MEMORY EXPANSION UNIT | | DWG SIZE C2 |
| | | ISSUE 8B |
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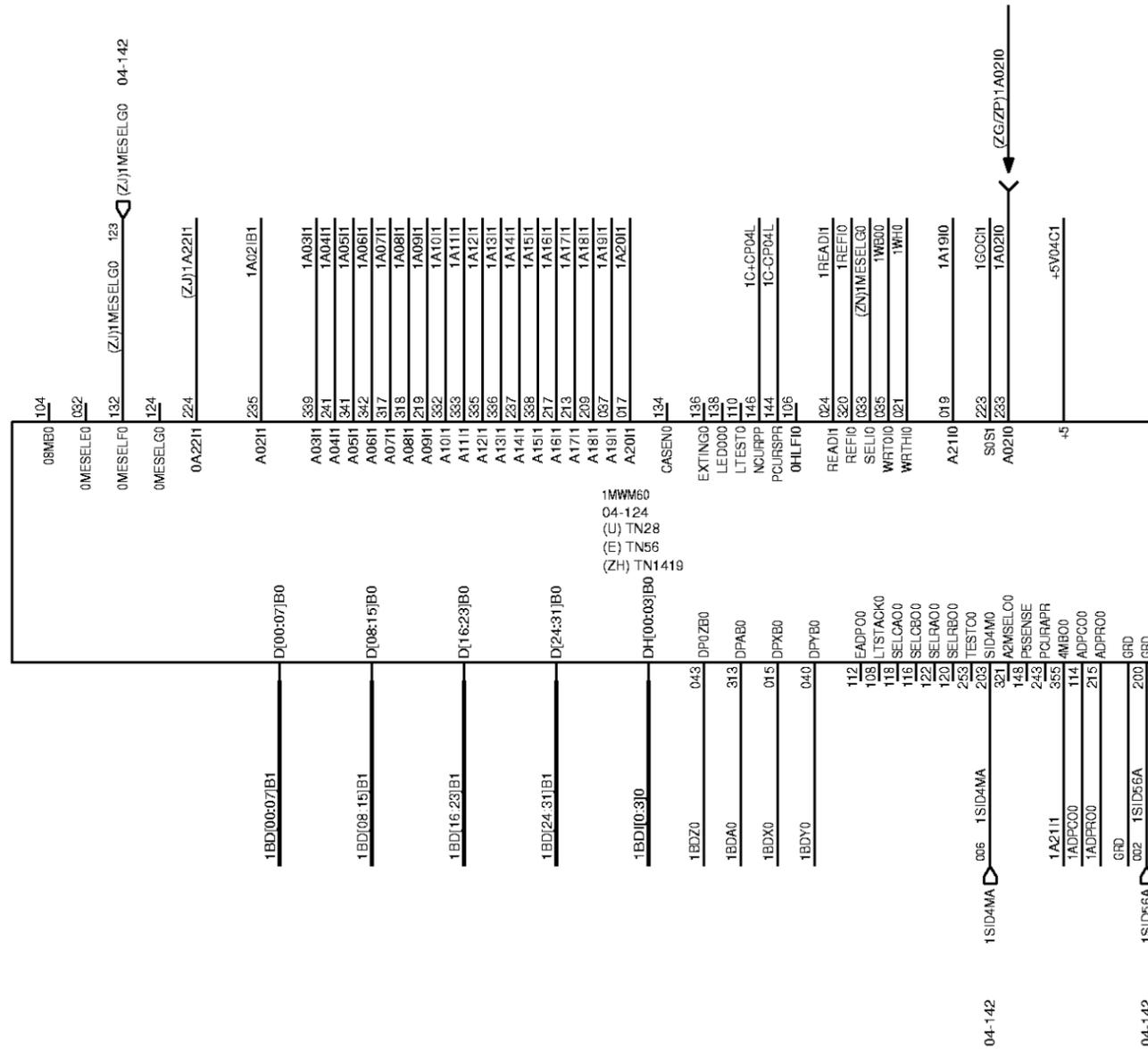
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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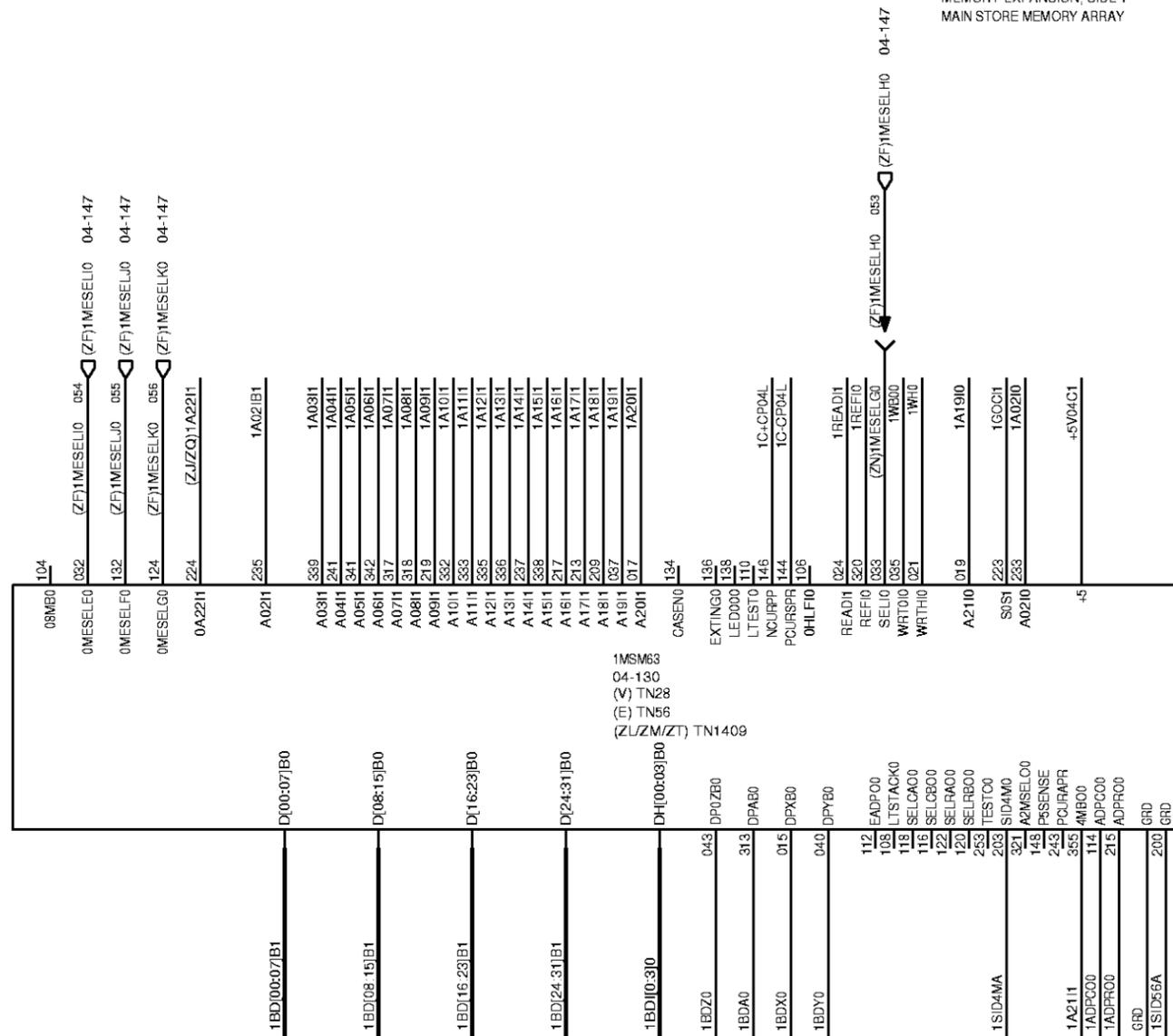
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



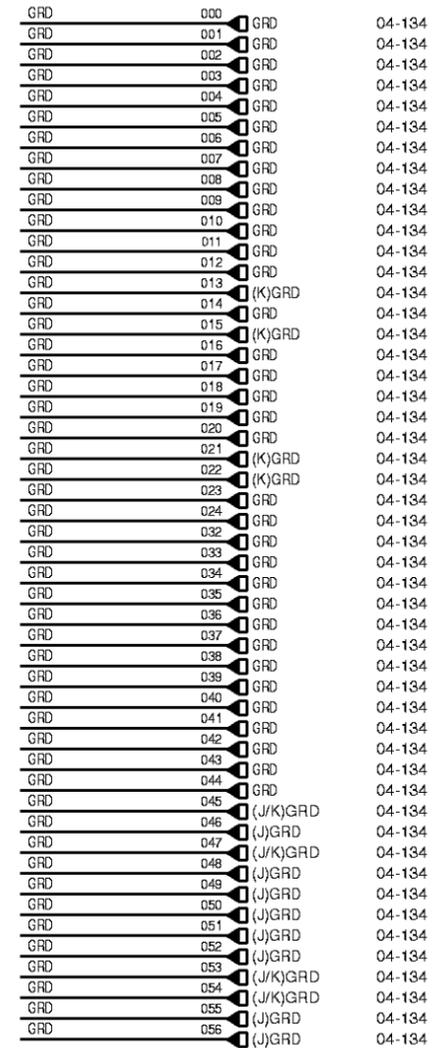
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| | | ISSUE 8B |
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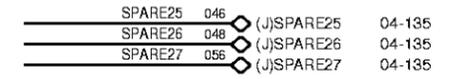
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



TF 04-134



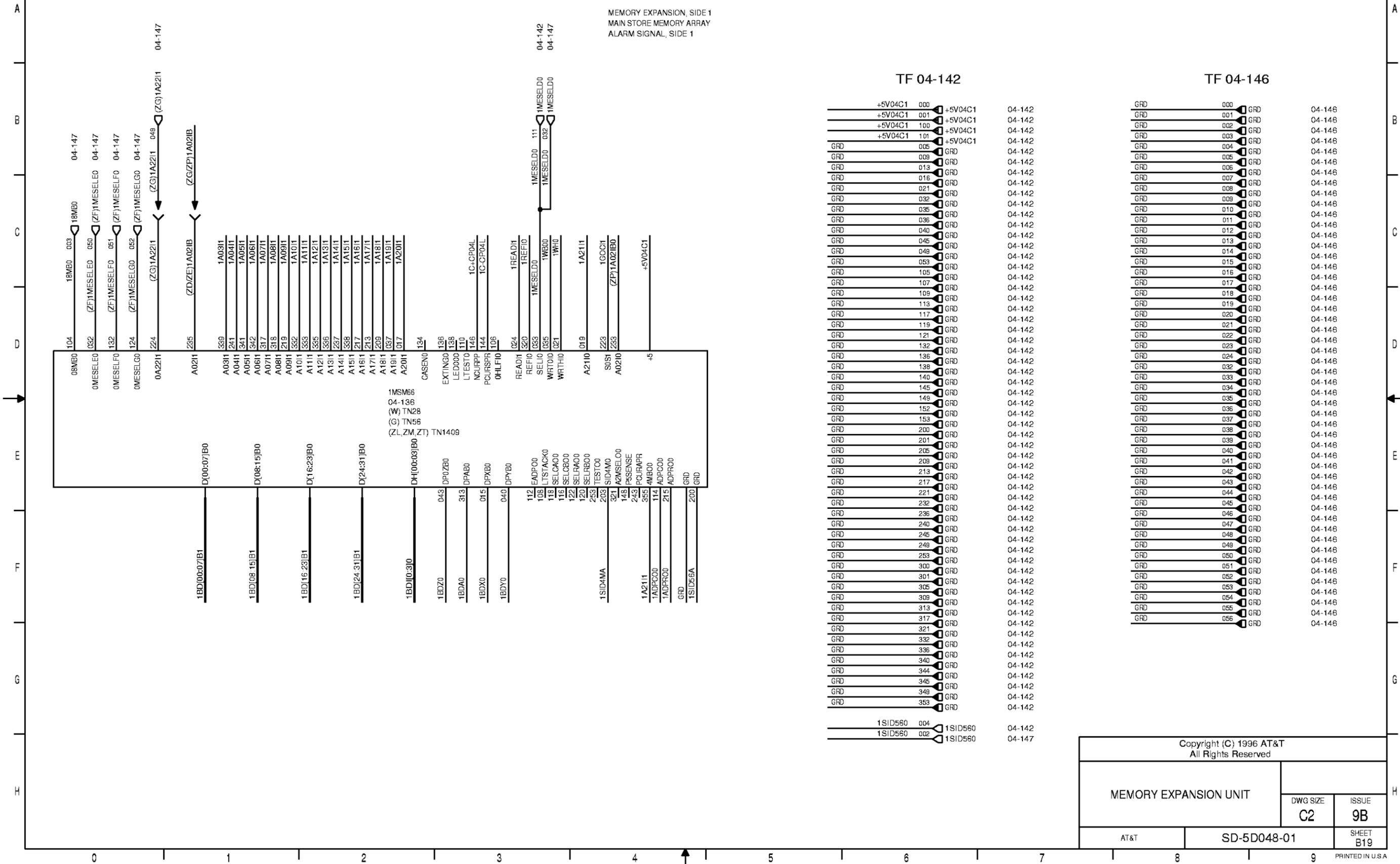
TF 04-135



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PART OF FS 2

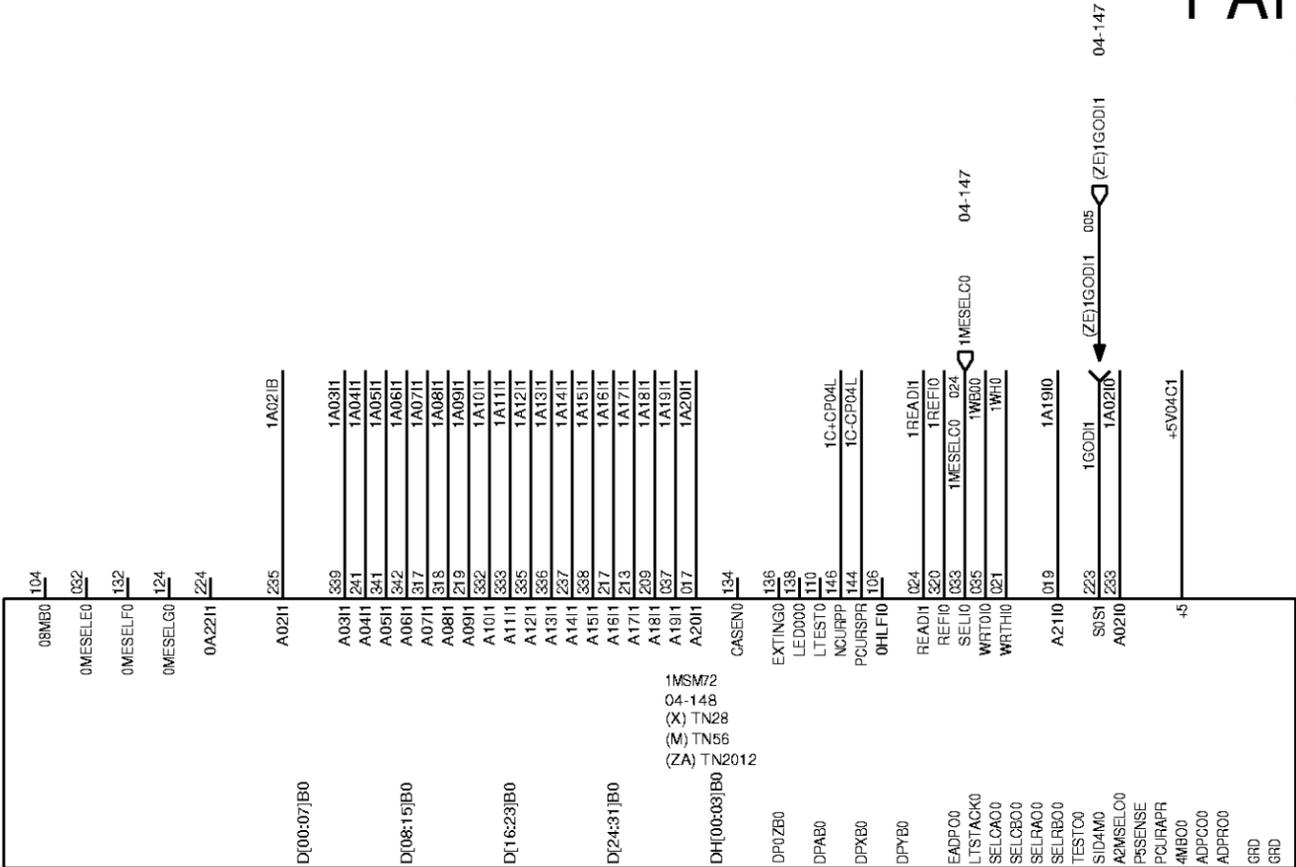
MEMORY EXPANSION, SIDE 1
 MAIN STORE MEMORY ARRAY
 ALARM SIGNAL, SIDE 1



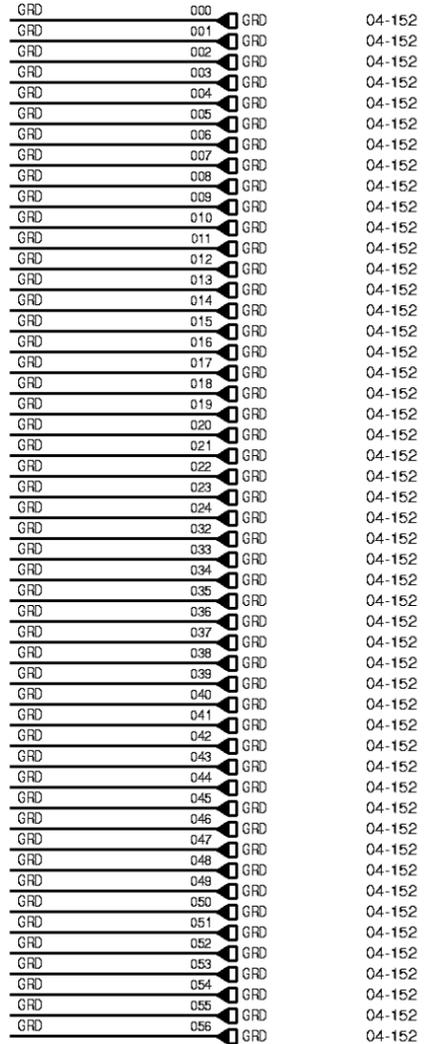
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| | | ISSUE 9B |
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PART OF FS 2

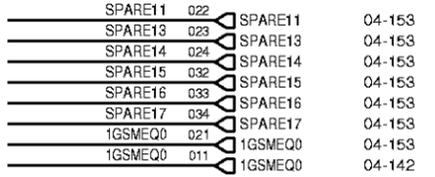
MEMORY EXPANSION, SIDE 1
 MAIN STORE MEMORY ARRAY
 ADDRESS AND CONTROL, SIDE 1



TF 04-152



TF 04-153



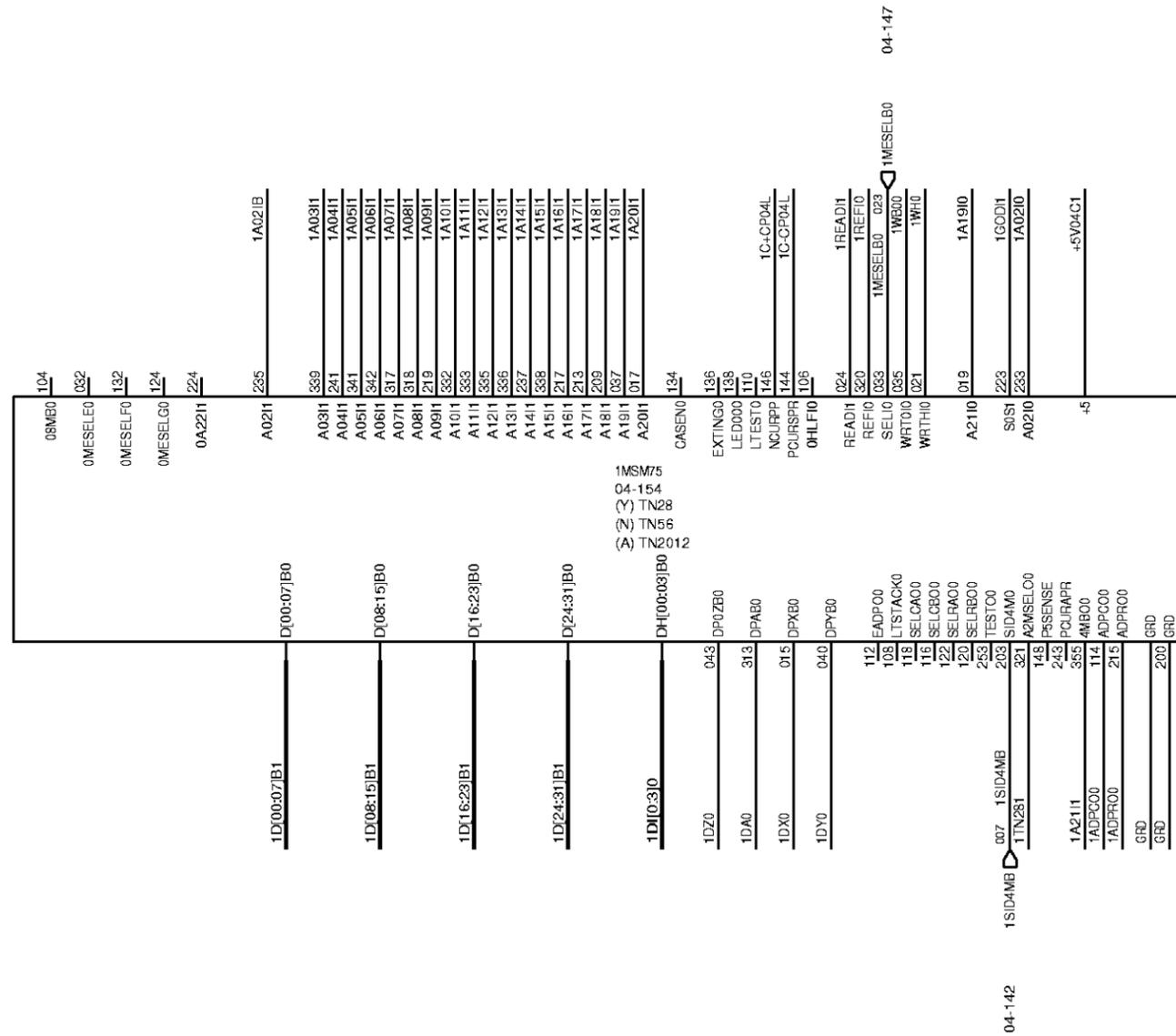
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| MEMORY EXPANSION UNIT | | DWG SIZE C2 |
| | | ISSUE 8B |
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PART OF FS 2

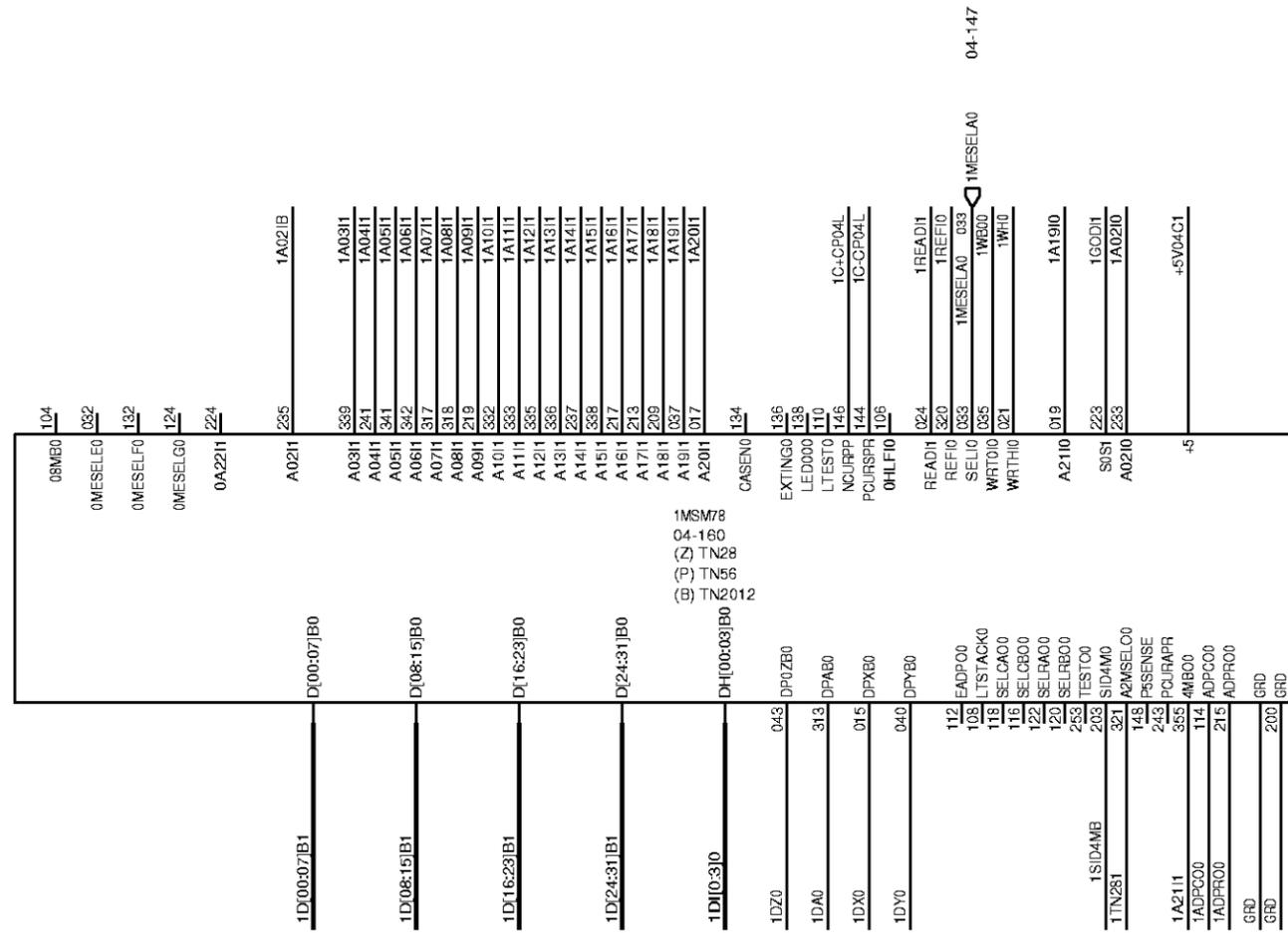
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



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| MEMORY EXPANSION UNIT | | DWG SIZE C2 |
| | | ISSUE 8B |
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PART OF FS 2

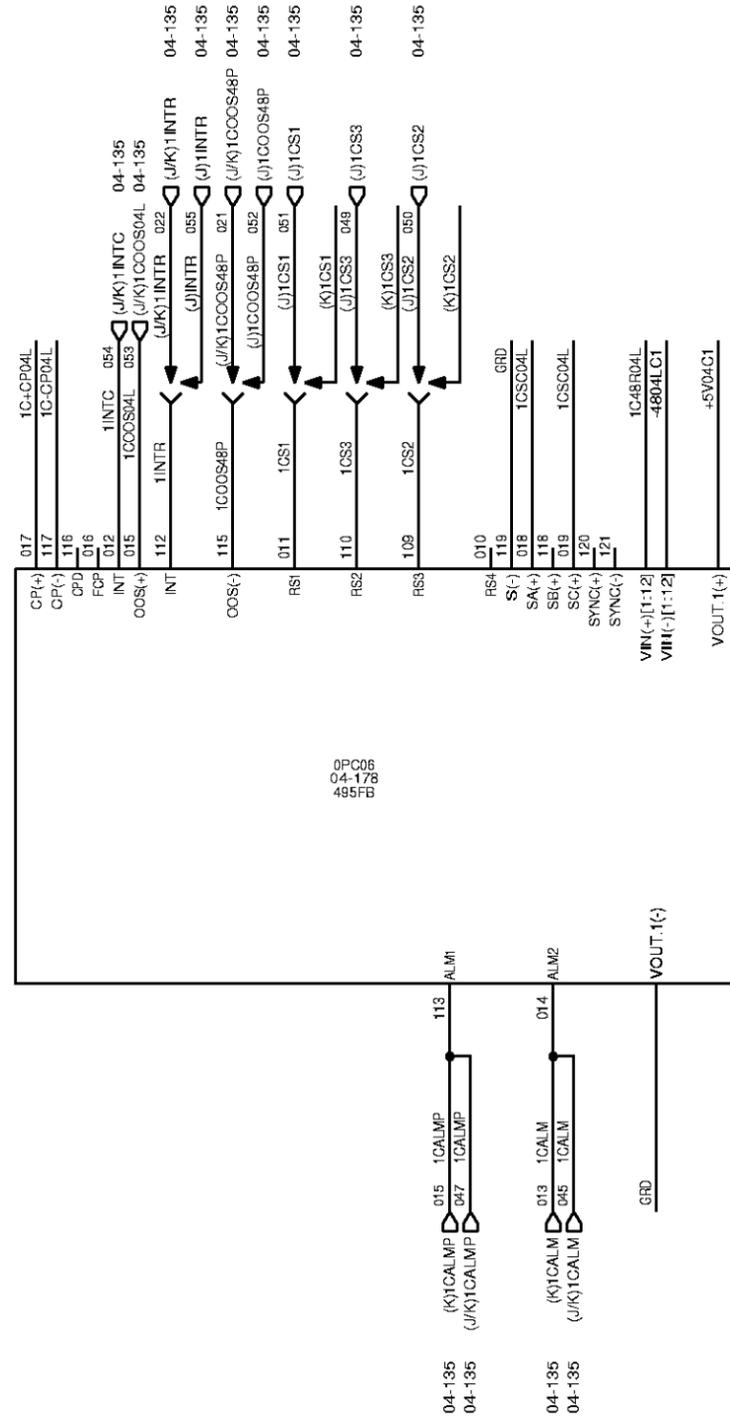
MEMORY EXPANSION, SIDE 1
MAIN STORE MEMORY ARRAY



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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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PART OF FS 2

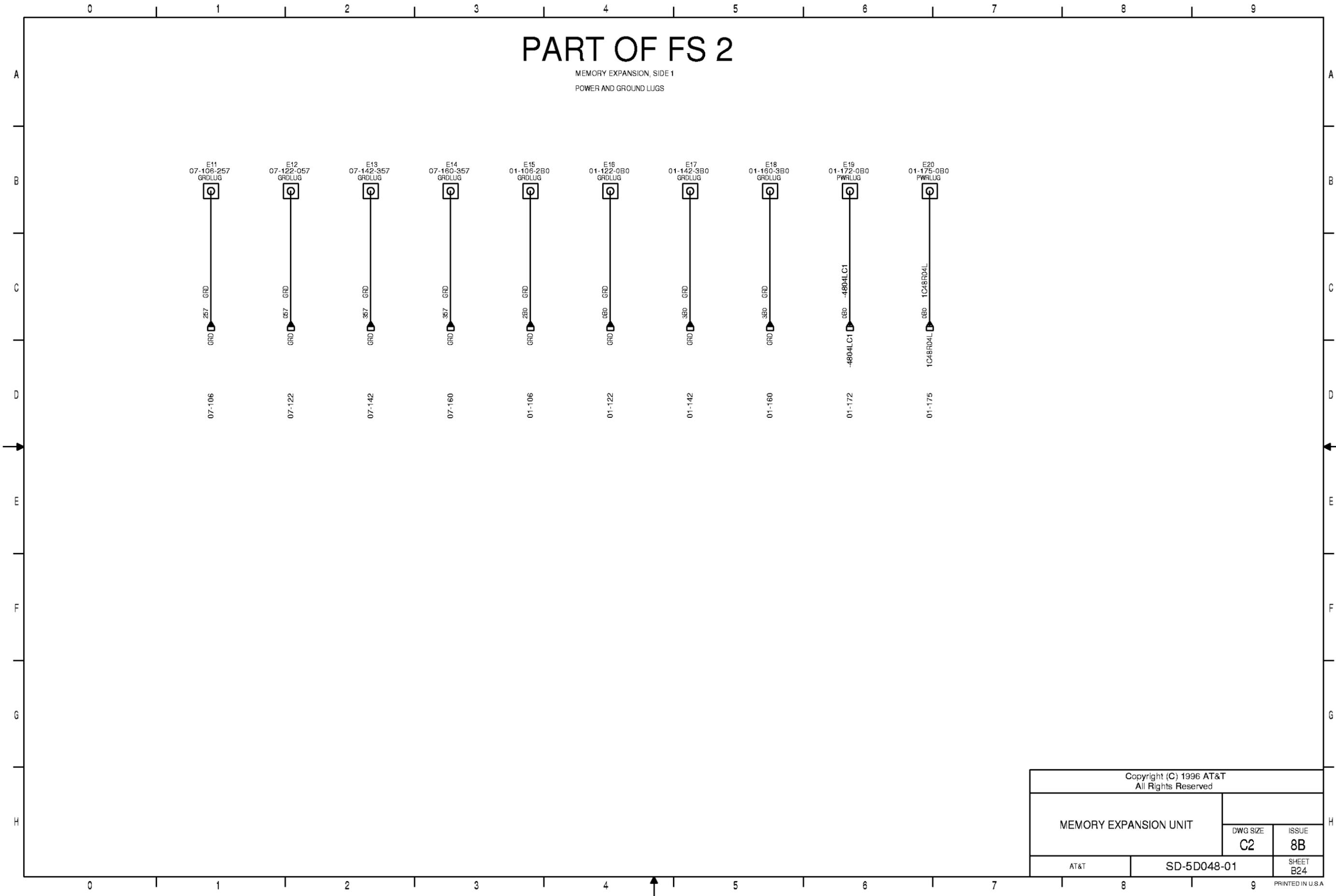
MEMORY EXPANSION, SIDE 1
POWER CONVERTER



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| MEMORY EXPANSION UNIT | | DWG SIZE C2 |
| | | ISSUE 8B |
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PART OF FS 2

MEMORY EXPANSION, SIDE 1
POWER AND GROUND LUGS



| | | |
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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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0 1 2 3 4 5 6 7 8 9

CIRCUIT NOTES:

101.

| DESIG | FUSE AMP | POTENTIAL | ONE PER |
|-----------------------|-------------|----------------------|---------------|
| 70D | 5A | -48V | SERVICE GROUP |
| <u>BATTERY SYMBOL</u> | | <u>VOLTAGE RANGE</u> | |

A
B
C
D
E
F
G
H

A
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C
D
E
F
G
H



0 1 2 3 4 5 6 7 8 9

| | | |
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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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201. UNLESS OTHERWISE SPECIFIED, ALL
BACKPLANE WIRING WILL BE AUTOMATIC
MACHINE WIRING (A-D4) 30 GAUGE
WHICH HAS BEEN PROCESSED BY THE
NESWRAP PROGRAMS.

202. ALL PRINTED WIRING CONNECTIONS ARE SPECIFIED BY ED-5D525

203.

| APPARATUS CODE | CIRCUIT PACK REMOVAL PROCEDURES | | |
|----------------|---------------------------------|--------------------|-----------|
| | PULL HOT | REMOVE UNIT POWER | SEQUENCED |
| 495FB | NO | POWER DOWN UNIT | NO |
| TN28 | NO | POWER DOWN UNIT | NO |
| TN56 | NO | POWER DOWN UNIT | NO |
| TN2012 | NO | POWER DOWN UNIT | NO |
| TN1409 | NO | POWER DOWN UNIT | NO |
| TN1419 | NO | POWER DOWN UNIT | NO |

| | | |
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| MEMORY EXPANSION UNIT | DWG SIZE | ISSUE |
| | C2 | 8B |
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INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.

| FEATURE OR OPTION | PROVIDE | | |
|--|---------|------------|--------------------------|
| | APP FIG | APP OR WBG | QUANTITY |
| PRINTED BACKPLANE & SURFACE WIRING | | | |
| ADDITIONAL WIRING ALWAYS REQUIRED | | ZB | |
| WIRING ADDED FOR SMP23 FEATURE | 1 | ZD | |
| WIRING REMOVED FOR SMP23 FEATURE | | ZE | |
| CABLING REQUIRED FOR ALARM SIGNALING (SEE NOTE 305) | | K | 1 PER UNIT |
| | | J | |
| MINIMUM EQUIPPAGE | | | |
| 495FB POWER CONVERTER | | | |
| TN1370 TERMINATOR BOARD (ALWAYS REQUIRED) | 2 | ZC | |
| MEMORY GROWTH AT 1 MBYTE (TN28) 2 MBYTE (TN56), OR 4 MBYTE (TN2012) INCREMENTS | | | |
| TN28 | | | |
| 04-022, 04-160 | | Z | |
| 04-028, 04-154 | | Y | |
| 04-034, 04-148 | | X | |
| 04-046, 04-136 | | W | |
| 04-052, 04-130 | | V | |
| 04-058, 04-124 | | U | |
| 04-064, 04-118 | | T | |
| TN56 | | | |
| 04-022, 04-160 | | P | |
| 04-028, 04-154 | | N | |
| 04-034, 04-148 | | M | |
| 04-046, 04-136 | | G | MINIMUM 1 PER APP FIG. 1 |
| 04-052, 04-130 | | F | |
| 04-058, 04-124 | | E | |
| 04-064, 04-118 | | D | |
| TN2012 | | | |
| 04-022, 04-160 | | B | |
| 04-028, 04-154 | | A | |
| 04-034, 04-148 | | ZA | |
| 982ET SHORTING BERGS REQUIRED FOR MEMORY GROWTH BEYOND 3 MEMORY CIRCUIT PACKS. (NOTES 306 & 307) | 4 | | 5 PER SIDE |
| SMP12 WITH SPLIT MEMORY | | | |
| TN1409 | | | |
| 04-046, 04-130 | | ZL | 1 PER CKT |
| 04-052, 04-136 | | | |
| WIRES REMOVED | | ZN | |
| WIRES ADDED | | ZF | |

INFORMATION NOTES (CONT):

| FEATURE OR OPTION | PROVIDE | | |
|-------------------------|---------|------------|------------|
| | APP FIG | APP OR WBG | QUANTITY |
| SMP23 WITH SPLIT MEMORY | | | |
| TN1409 | | | |
| 04-046, 04-130 | | ZM | 1 PER CKT |
| 04-052, 04-136 | | | |
| WIRES REMOVED | | ZP | |
| WIRES ADDED | | ZG | |
| TERMINATOR PADDLEBOARDS | | ZK | 3 PER SIDE |
| SMP23X | | | |
| TN1419 | | | |
| 04-058, 04-124 | | ZH | 1 PER CKT |
| 04-064, 04-118 | | | |
| WIRES REMOVED | | ZQ | |
| WIRES ADDED | | ZJ | |
| TERMINATOR PADDLEBOARDS | | ZK | 3 PER SIDE |
| SMP23CDMC | | | |
| CIRCUIT PACKS REMOVED | | ZT | 4 PER CKT |
| WIRES REMOVED | | ZU | |

INFORMATION NOTES (CONT):

| CHANGED ON ISS | IF JOB RECORDS DO NOT SPECIFY | THIS OPTION WAS FURN | SEE NOTE | USE IN CIRCUIT | | |
|----------------|-------------------------------|----------------------------|------------|----------------------------------|-----|----|
| | | | | STD | A&M | MD |
| 3AC | K OR J | K | 305 | K OR J | | |
| | | | 308 | AVAIL | | DA |
| 4B | APP FIG. 4 | 4 | 306 307 | AVAIL | | |
| | APP FIG. 3 | W,V U,T | | AVAIL | | |
| 5B | APP FIG. 3 | ZA, A, B, D, E, F, G | | AVAIL | | |
| 6A | APP FIG. 1 | ZB | | REQ | | |
| 6A | APP FIG. 2 | ZC | | REQ | | |
| 7B | ZD | ZE | | ZD, ZE | | |
| 8B | ZL, ZM, ZH, ZK | NONE | | ZL, ZM, ZH, ZK | | |
| 8B | ZG ZF ZJ | ZP ZN ZQ | | ZN ZP ZQ ZG ZF ZJ | | |
| 9B | | ZU ZT | | ZU ZT | | |

| CIRCUIT PACK CODE OR MICROCODE | COMMON LANGUAGE EQUIPMENT IDENTIFICATION CODE (CLEI) |
|--------------------------------|--|
| TN28 | SP3004AAXX |
| TN56 | SP3013JAXX |
| TN1370 | E5PQBAXX |
| TN2012 | SPPQ03WAXX |
| 495FB | PWPQ54EAXX |
| TN1419 | E5PQAG5AAA |
| TN1409 | E5PQAAWA |

INFORMATION NOTES (CONT):

305. OPTIONS K AND J DESIGNATE THE ALARM SIGNAL CABLES LEAVING THE MEU AND TERMINATING AT THE SMPU2 AND TSIU2 OR MCTY RESPECTIVELY.
306. APP FIG. 4 MUST BE EQUIPPED WITH OPTION W OR G.
307. 982ET SHORTING BERGS ARE LOCATED AT THE FOLLOWING EQL'S
- 04-040-145
 - 04-040-300
 - 04-040-313
 - 04-040-332
 - 04-040-345
 - 04-142-145
 - 04-142-300
 - 04-142-313
 - 04-142-332
 - 04-142-345
308. PRIOR TO ISSUE 5B, COLUMNS HEADED "STD", "MD" ETC., CONVEYED APPLICATIONS INFORMATION. AT ISSUE 5B, COLUMNS HEADED "AVAIL" AND "DA" NOW INDICATE THE AVAILABILITY OF THE PRODUCT.
309. OPTION ZC AND OPTION ZK ARE MUTUALLY EXCLUSIVE.

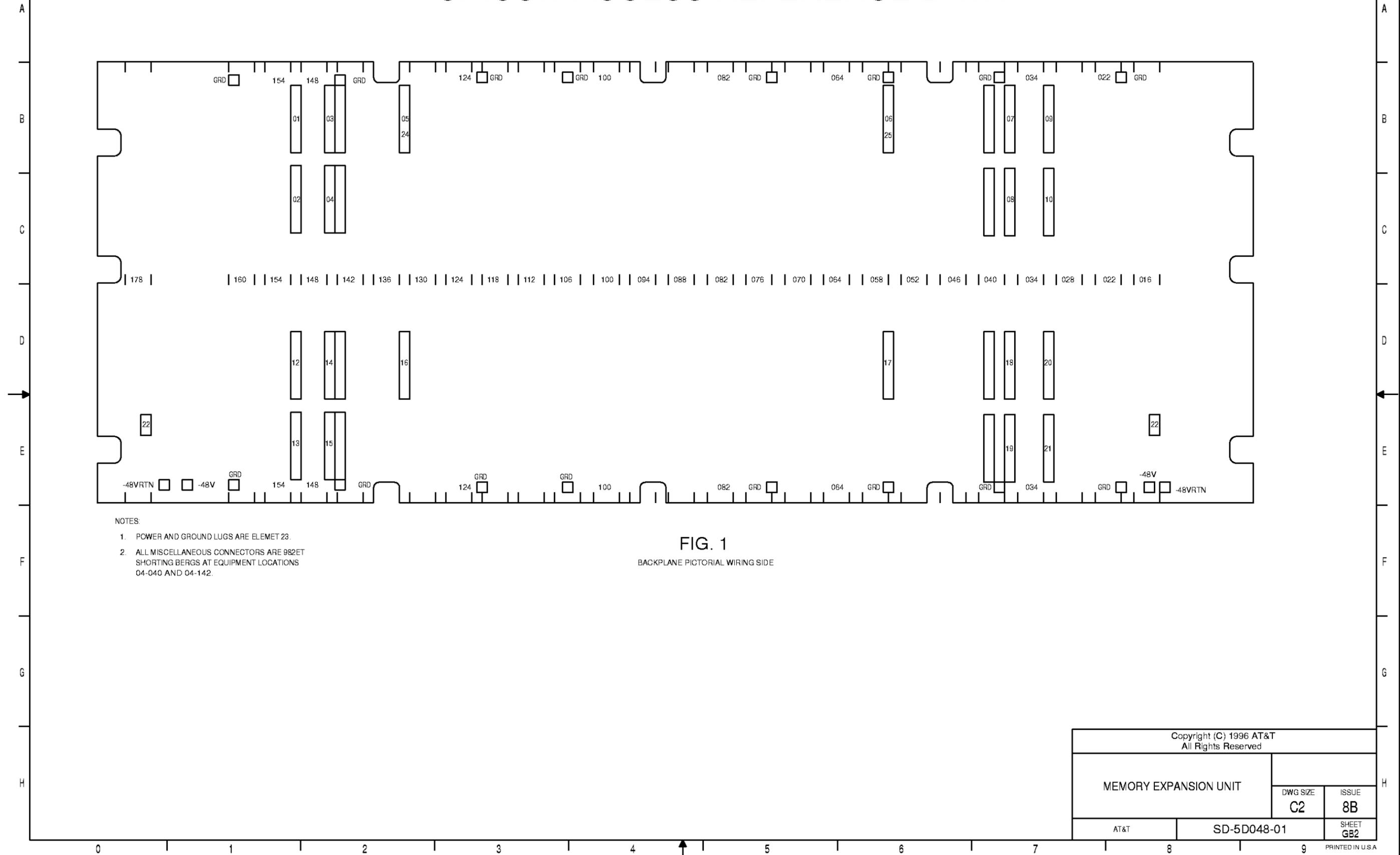
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| MEMORY EXPANSION UNIT | | DWG SIZE C2 |
| | | ISSUE 9B |
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| UNIT SYMBOL TABLE OF CONTENTS | | | | |
|-------------------------------------|--------|--|--------|---------|
| FUNCTION | | DESCRIPTION (LOWER LEFT PIN OF CONNECTOR) | SIZE | ELEMENT |
| ADDRESS AND MEMORY CONTROL | SIDE 0 | (04-039-045) SMPUC0 | 2 X 12 | 07 |
| | | (04-039-032) SMPUC0 | 2 X 12 | 08 |
| | | (04-039-013) SMPUC0 | 2 X 12 | 18 |
| | | (04-039-000) SMPUC0 | 2 X 12 | 19 |
| | SIDE 1 | (04-147-045) SMPUC1 | 2 X 12 | 03 |
| | | (04-147-032) SMPUC1 | 2 X 12 | 04 |
| | | (04-147-013) SMPUC1 | 2 X 12 | 14 |
| | | (04-147-000) SMPUC1 | 2 X 12 | 15 |
| DATA AND BUFFERED DATA | SIDE 0 | (04-033-045) SMPUC0 | 2 X 12 | 09 |
| | | (04-033-032) SMPUC0 | 2 X 12 | 10 |
| | | (04-033-013) SMPUC0 | 2 X 12 | 20 |
| | | (04-033-000) SMPUC0 | 2 X 12 | 21 |
| | SIDE 1 | (04-153-045) SMPUC1 | 2 X 12 | 01 |
| | | (04-153-032) SMPUC1 | 2 X 12 | 02 |
| | | (04-153-013) SMPUC1 | 2 X 12 | 12 |
| | | (04-153-000) SMPUC1 | 2 X 12 | 13 |
| CONTROL SIGNAL | SIDE 0 | (04-016-109) (K) SMPU2 | 2 X 3 | 22 |
| | SIDE 1 | (04-178-109) (K) SMPU2 | 2 X 3 | 11 |
| POWER | SIDE 0 | (01-015-0B0) POWER LUG | LUG | 23 |
| | | (01-016-016) POWER LUG | LUG | 23 |
| | | (01-022-0B0) RETURN LUG | LUG | 23 |
| | | (01-040-1B0) RETURN LUG | LUG | 23 |
| | | (01-058-0B0) RETURN LUG | LUG | 23 |
| | | (01-076-0B0) RETURN LUG | LUG | 23 |
| | | (07-071-057) RETURN LUG | LUG | 23 |
| | | (07-040-157) RETURN LUG | LUG | 23 |
| | | (07-058-057) RETURN LUG | LUG | 23 |
| | SIDE 1 | (07-076-057) RETURN LUG | LUG | 23 |
| | | (01-172-0B0) POWER LUG | LUG | 23 |
| | | (01-175-0B0) POWER LUG | LUG | 23 |
| | | (01-106-2B0) RETURN LUG | LUG | 23 |
| | | (01-122-2B0) RETURN LUG | LUG | 23 |
| | | (01-142-3B0) RETURN LUG | LUG | 23 |
| | | (01-160-3B0) RETURN LUG | LUG | 23 |
| | | (07-106-257) RETURN LUG | LUG | 23 |
| | | (07-122-057) RETURN LUG | LUG | 23 |
| (07-142-357) RETURN LUG | LUG | 23 | | |
| (07-160-357) RETURN LUG | LUG | 23 | | |

| UNIT SYMBOL TABLE OF CONTENTS | | | | |
|-------------------------------|--------|--|--------|---------|
| FUNCTION | | DESCRIPTION (LOWER LEFT PIN OF CONNECTOR) | SIZE | ELEMENT |
| ALARM SIGNAL | SIDE 0 | (04-057-045) (K) SMPU2 | 2 X 12 | 25 |
| | | (04-057-045) (J) MCTU | 2 X 12 | 06 |
| | | (04-057-013) TSIU2 PWR0 | 2 X 12 | 17 |
| | SIDE 1 | (04-135-045) (K) SMPU2 | 2 X 12 | 24 |
| | | (04-135-045) (J) MCTU | 2 X 12 | 05 |
| | | (04-135-013) TSIU2 PWR1 | 2 X 12 | 16 |

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CIRCUIT ACCESS REFERENCE DATA



NOTES:

1. POWER AND GROUND LUGS ARE ELEMET 23.
2. ALL MISCELLANEOUS CONNECTORS ARE 982ET SHORTING BERGS AT EQUIPMENT LOCATIONS 04-040 AND 04-142.

FIG. 1

BACKPLANE PICTORIAL WIRING SIDE

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CAD 1

UNIT SYMBOL

0 1 2 3 4 5 6 7 8 9

A

B

C

D

E

F

G

H

A

B

C

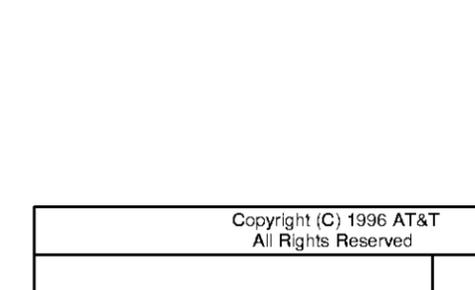
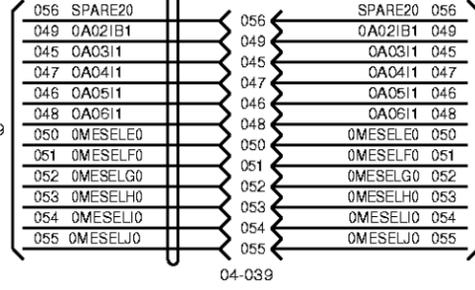
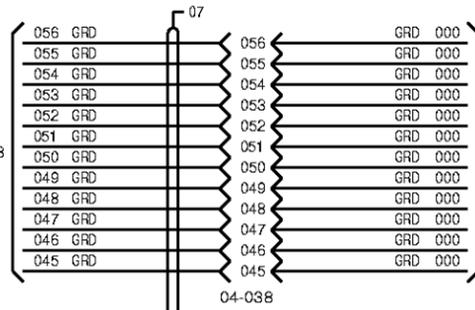
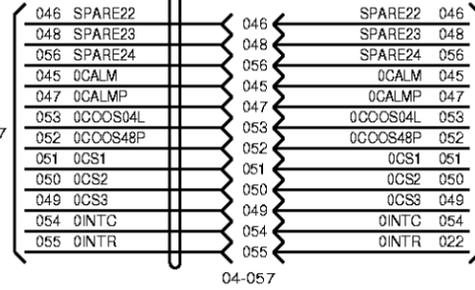
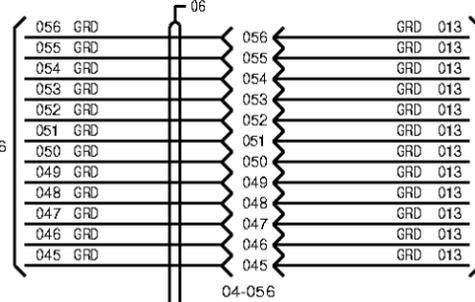
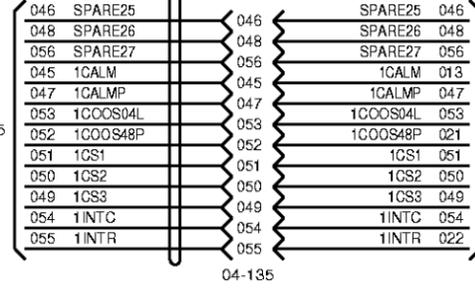
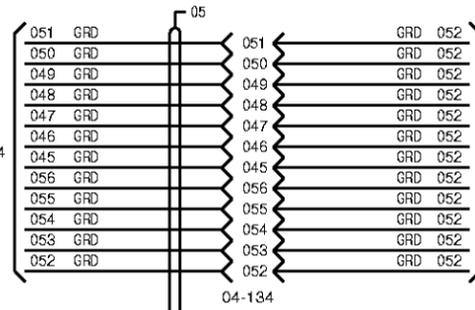
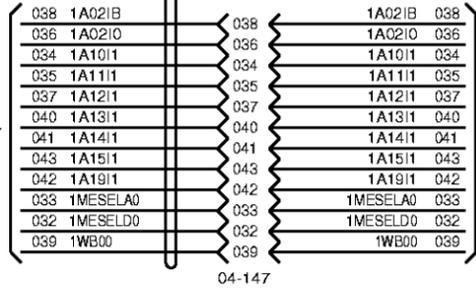
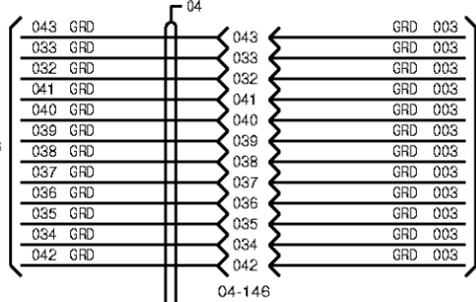
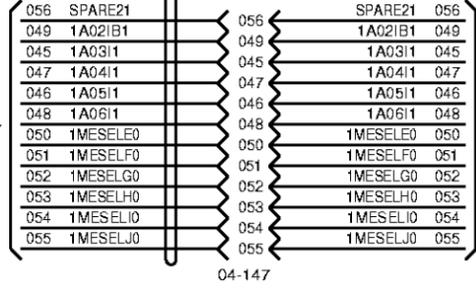
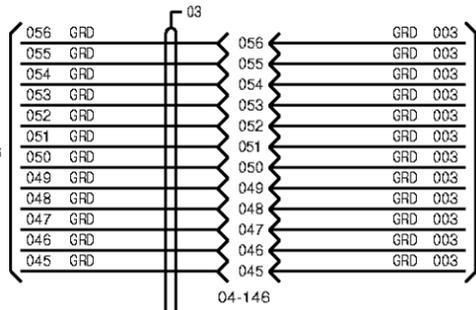
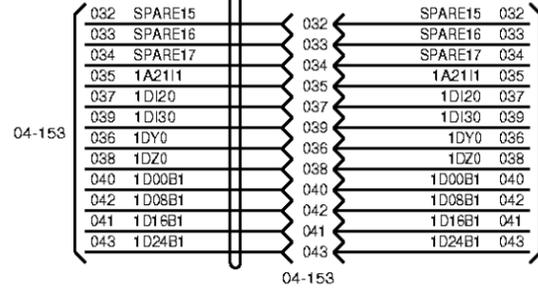
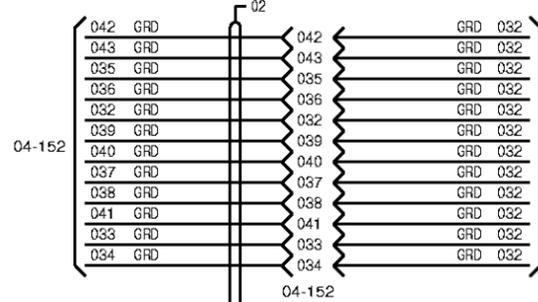
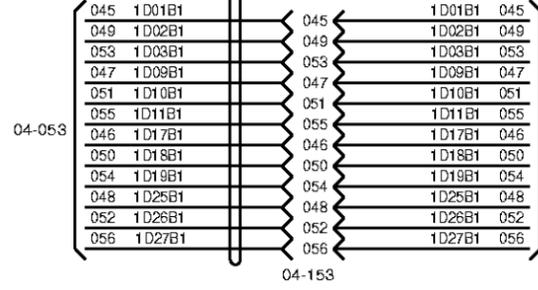
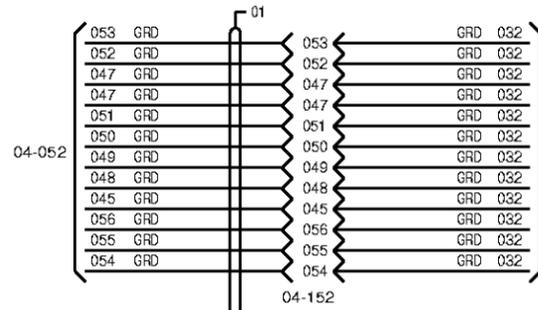
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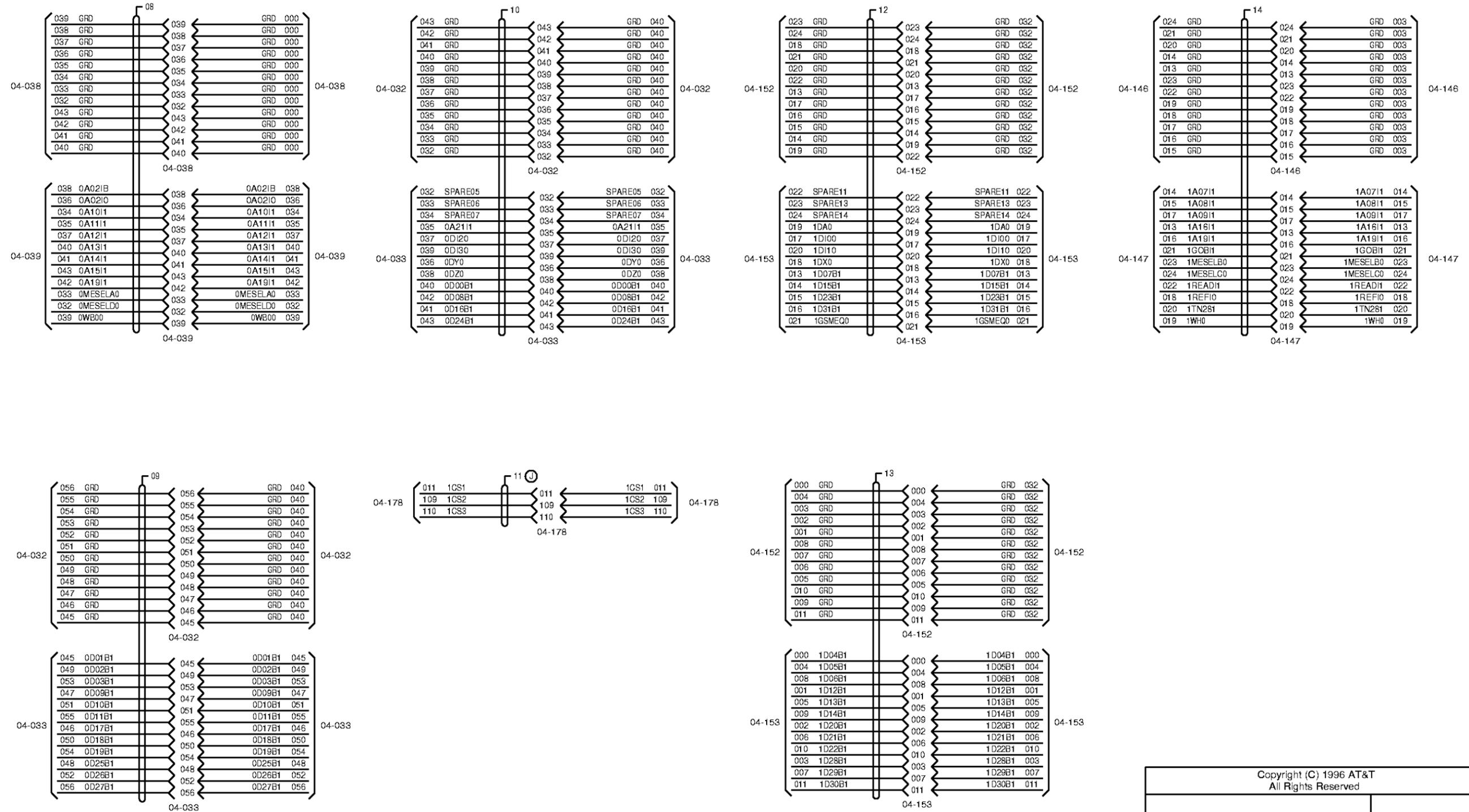


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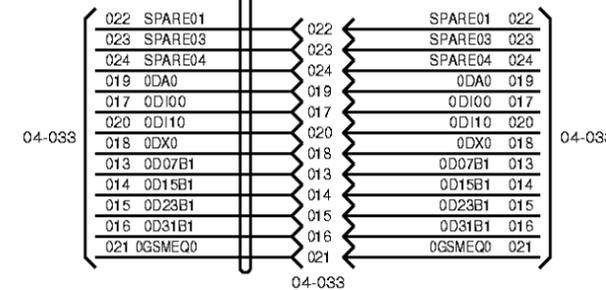
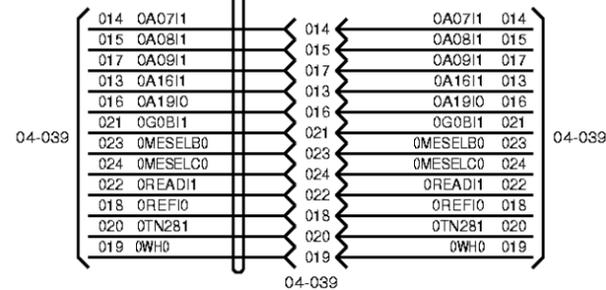
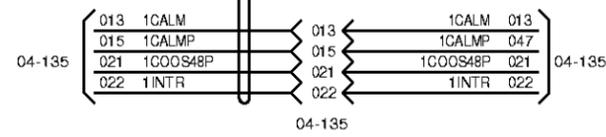
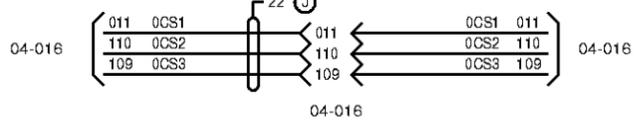
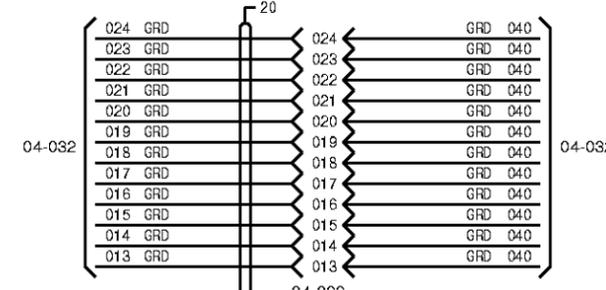
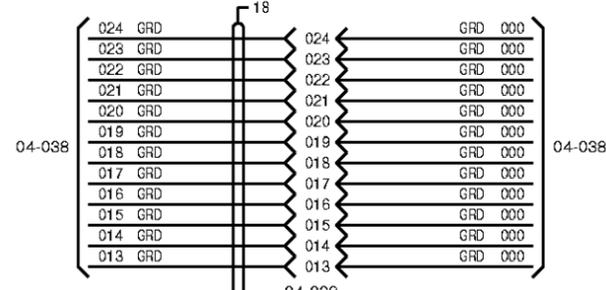
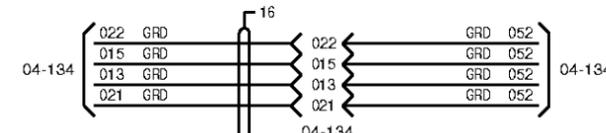
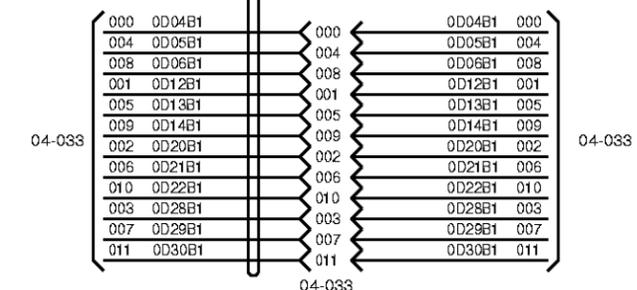
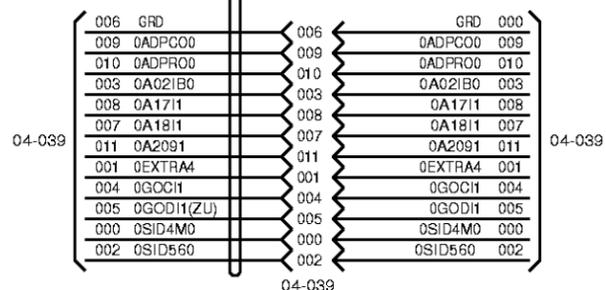
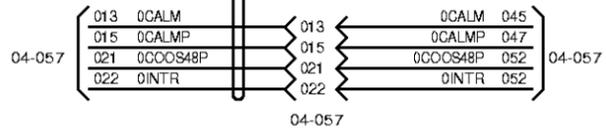
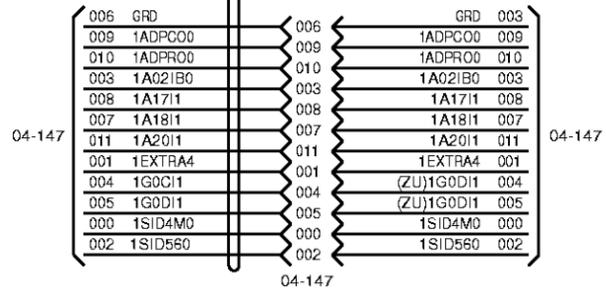
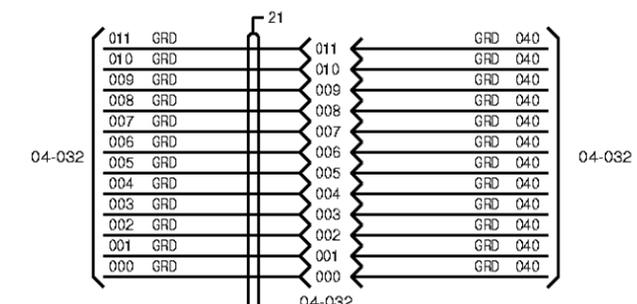
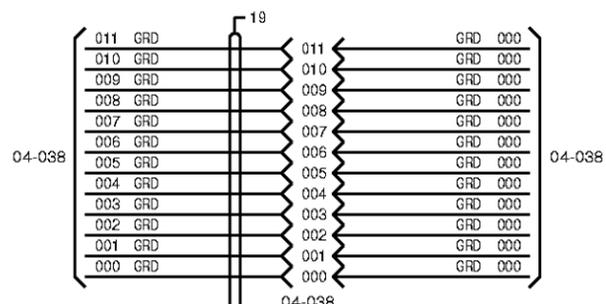
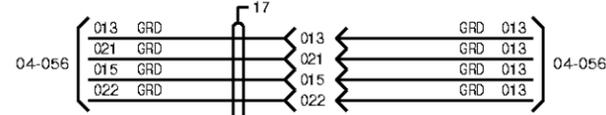
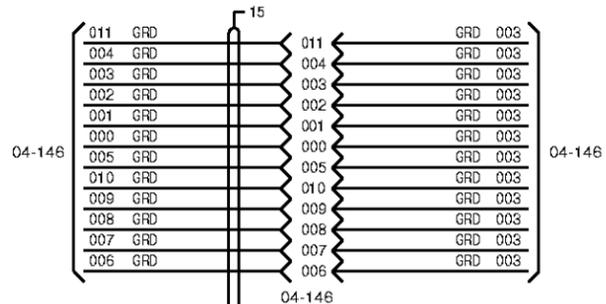
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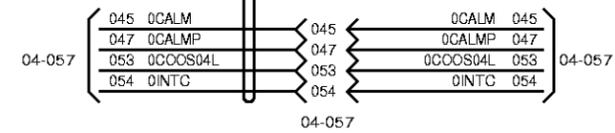
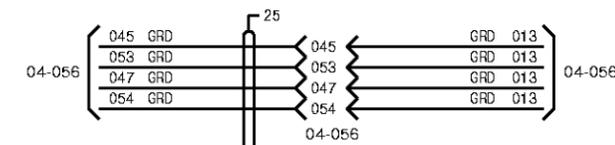
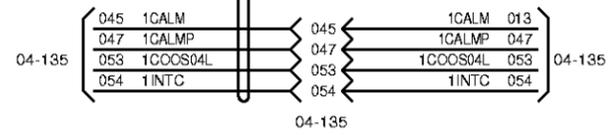
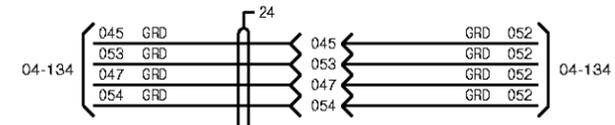
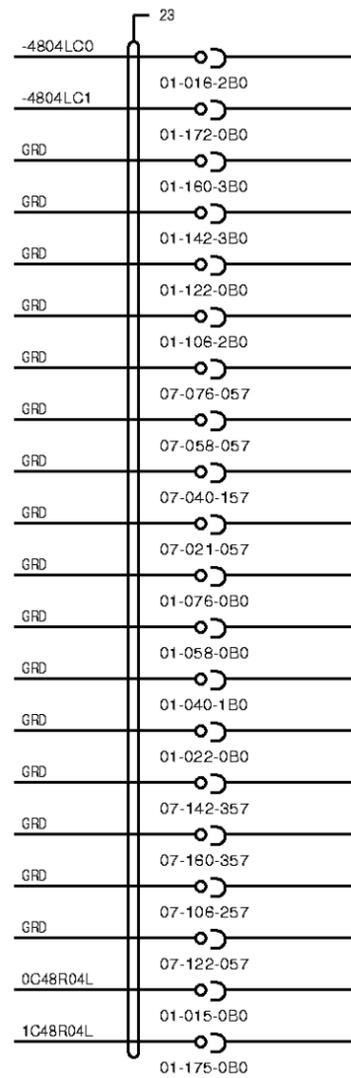
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