

APPARATUS INDEX

DESIG	LOCATION		
	FS	APP FIG	EQPT
RELAYS			
ALM	2F1	4	
COD	3M	3	
DS	1G7	2	
H	3C0	3	
IMP	2C1	4	
MR	2B7	3	
MS	1F9	2	
NV	2B4	3	
P	1E2	1	
SX	1B2	1	
CAPACITORS			
ALM	2E0	4	
CONNECTORS			
J1	2D4	4	

DESIG	LOCATION		
	FS	APP FIG	EQPT
DIODES			
ALM	2G1	4	
DS	1F6	2	
HG	3D9	3	
LP	1D1	1	
PP	1F6	2	
SI	1D6	2	
SX	1C2	1	
TR	1D1	1	
FUSE			
MR	2A6	3	
LAMP			
MR	1C2	1	
MR	2B7	3	
IV	2B4	3	
RA	2G1	4	
NETWORKS			
DS	1E6	2	
MS	1E9	2	
P	1E2	1	
SC	1L8	2	
SX	1B2	1	
IMP	2D1	4	
POWER SUPPLY			
+48V	2B2	3	
RELAY TIME DELAY			
ALM	2F2	4	

DESIG	LOCATION		
	FS	APP FIG	EQPT
RESISTORS			
ALM	2F0	4	
MS	1F8	2	
TR	1D0	1	
SELECTOR			
SC	1A7 1A8	2	
THERMISTOR			
LP	1D2	1	
PP	1D3	1	

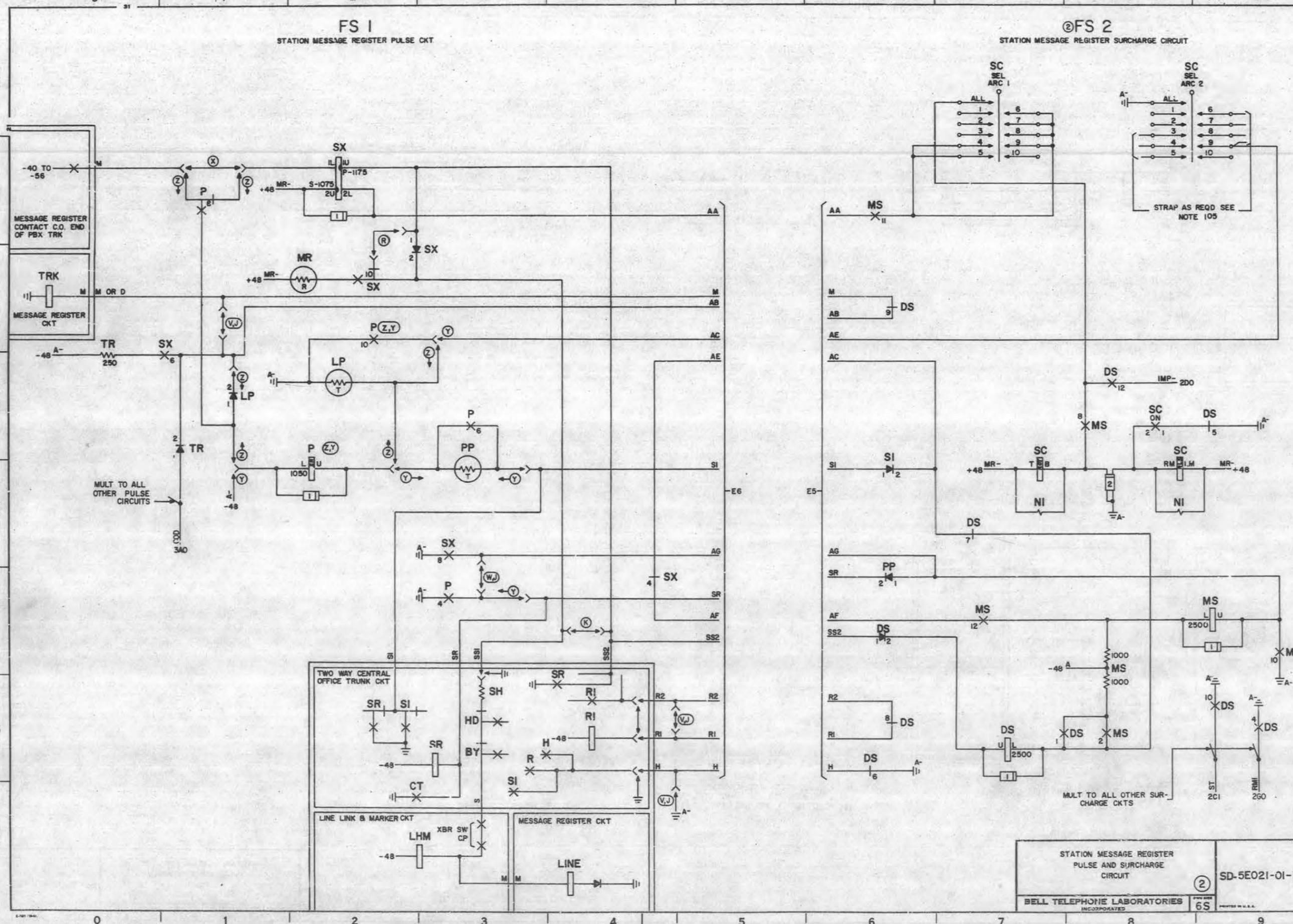
LEAD INDEX

DESIG	FS LOC	CAD LOC
ALARM, TRANSFER & TEST CKT		
EXT	2B6	
BUSY VERIFICATION AUX TRK CIRCUIT		
BVS	3G8	
INTERRUPTER CKT 756A PBX		
FF1	2C0	
LINE, LINK AND MARKER		
COD	3B1	
MR1	3C8	
MR2	3D8	
MR3	3E8	
MR4	3F8	
MR5	3B8	
MR6	3C8	
MR7	3E1	
MR8	3E1	
MR9	3D1	
MR10	3C1	
MR11	3G1	
MR12	3F1	
MR13	3B1	
MR14	3C1	
RSA	3E8	
RSB	3D8	

OPTION INDEX

APP OR BRG	LOCATION
Z	1B1, 1C3, 1C1, APP FIG. 1
Y	1D3, 1E1, 1F3, APP FIG. 1
X	1B1
W	1F3
V	1G5, 1H4, 1C1
T	3B9, 3C9, 3E9, 3F9
S	3B9, 3C9, 3E9, 3F8
R	1B2, 1C2
Q	CAD1
N	CAD 3
M	CAD 3
K	1F4
J	1C1, 1F3, 1G5, 1H4

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DRAWING ISSUE	
1	EMG HW
30	ROZ P75 RMP
40	ROZ P75 RMP
5B	PWC PUS LDJ

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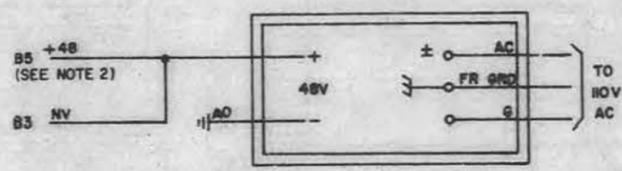
STATION MESSAGE REGISTER PULSE AND SURCHARGE CIRCUIT
 2 SD-5E021-01-B1
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 6S

0 1 2 3 4 5 6 7 8 9

DRAWING ISSUE	
1	HW
2D	A
3D	PJS
6A	LDJ

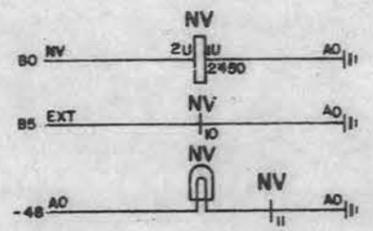
FS 3

+48V POWER SUPPLY
KS-15620 RECTIFIER



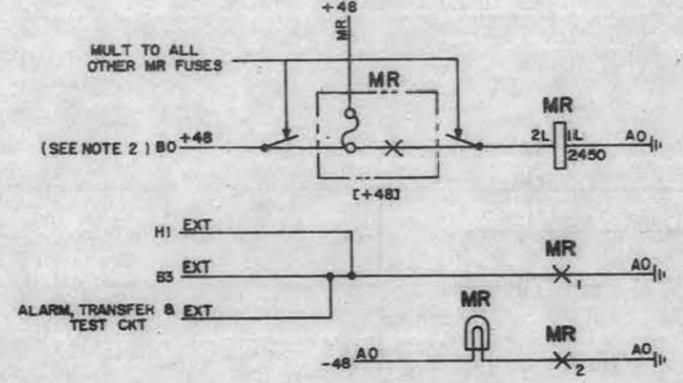
FS 4

NO VOLTAGE ALARM



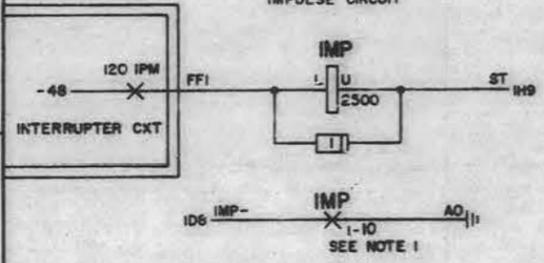
FS 5

FUSE ALARM



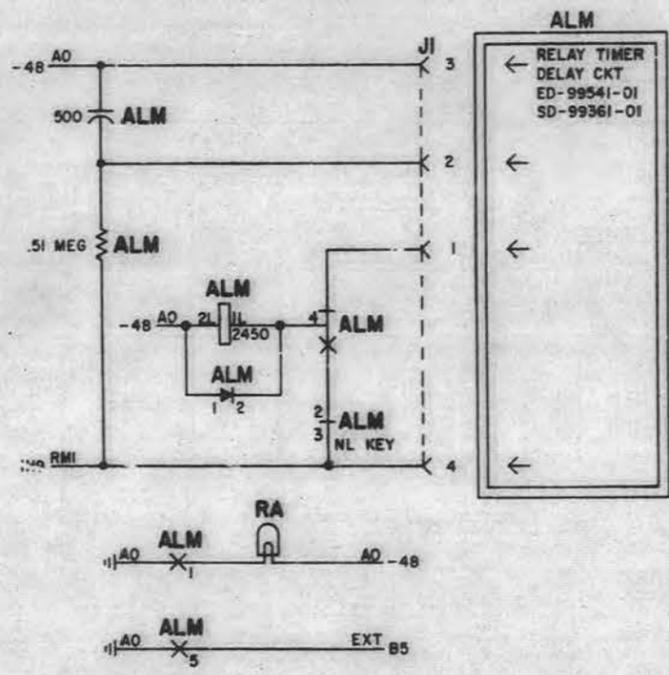
FS 6

IMPULSE CIRCUIT



FS 7

RELEASE MONITOR ALARM



NOTES:

- IMP RELAY CONTACT 1M CONNECT TO FIRST SURCHARGE CIRCUIT FS 2, CONTACT 2M TO SECOND SURCHARGE CIRCUIT FS2, ETC.
- NO. 14 GAUGE SOLID WIRE, KS-13385 LI SHALL BE USED FOR THE +48V LEAD BETWEEN THE POWER SUPPLY AND THE +48V FUSES.

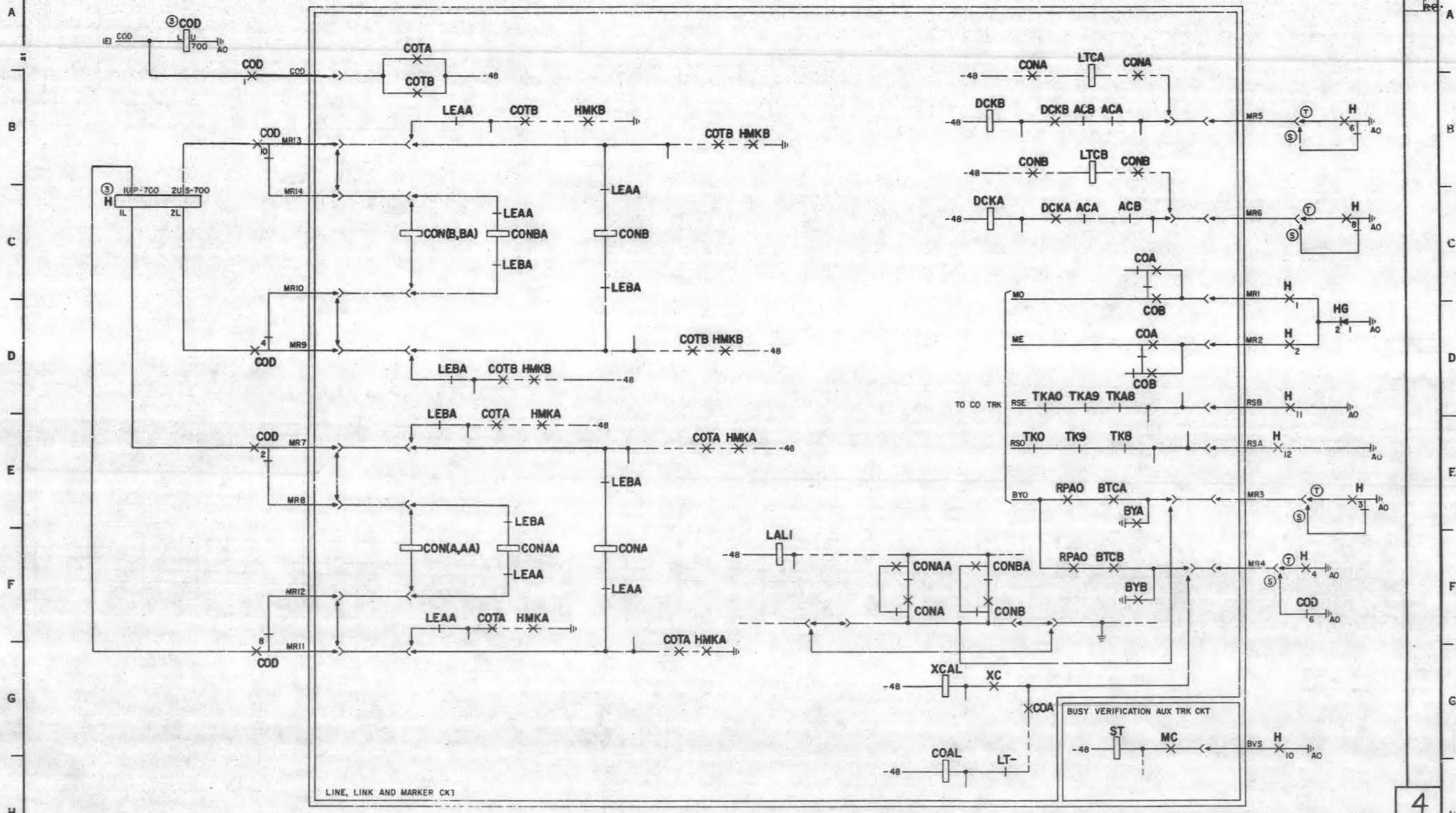
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0 1 2 3 4 5 6 7 8 9

FS 8
MARKER CONTROLLER CIRCUIT

DRAWING	PC2
ISSUE	30
	HW
	PC2
	40
	PC2
	PC2



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STATION MESSAGE REGISTER PULSE AND SURCHARGE CIRCUIT	(2)	SD-5E021-01-B3
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CIRCUIT NOTES

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER	TERM DESIG
A-0	1-1/3	-48V	APP FIG. 1, 2, 3 & 4	
A-1/9	1-1/3	-48V	APP FIG. 1 & 2	
MP	1-1/3	+48V	APP FIG. 1 & 2	
A-0		GRD	APP FIG. 1, 2, 3 & 4	
A-1/9		GRD	APP FIG. 1 & 2	
BATTERY SYMBOL		VOLTAGE RANGE		
-48V		45-52V		
+48V		45-52.6V		

103.

NETWORK NUMBER	NETWORK VALUES	
	RESISTANCE IN OHMS	CAPACITANCE IN UF
1	470	.11
2	100	.5

INFORMATION NOTES

301. UNLESS OTHERWISE SPECIFIED RESISTANCE VALUES ARE IN OHMS. CAPACITANCE VALUES ARE IN MICROFARADS. VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE 1% VOLTS.

105.

STRAPPING AT TS FOR SURCHARGE	
SURCHARGE	STRAP AT TS
6	NONE
5	36 TO 25
4	36 TO 16
3	36 TO 4
2	36 TO 3
1	36 TO 25
0	36 TO 15

106. IF THE LINE, LMK AND MARKER CIRCUIT IS EQUIPPED WITH OPTION XK, OPTION T MUST BE PROVIDED. IF THE MARKER CIRCUIT IS EQUIPPED WITH OPTION WV, OPTION S MUST BE PROVIDED.

104.

CHANGED ON ISS	RECORD OF APP FIGURE, WIRING AND APPARATUS CHANGES				USE IN CIRCUIT		
	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT			
				STD	A & M	MD	
4D	S	T	106	S	T		
	R	NONE		Q		R	
	N	M		N		M	
5B	K	NONE	204	K			
	J	V,W	205	J		V,W	

102.

FEATURE OR OPTION	APP FIG	APP OR RFG	PROVIDE	
			QUANTITY	
WITHOUT SURCHARGE: COMMON APPARATUS			3	ONE PER PBX
+48V BAT (SYSTEM BATTERYLESS OPR) NO VOLTAGE ALARM FUSE ALARM				
WITH SURCHARGE: COMMON APPARATUS (SEE NOTES 201 & 205)			3,4	ONE PER PBX
+48V BAT (SYSTEM BATTERYLESS OPR) NO VOLTAGE ALARM FUSE ALARM IMPULSE RELAY CKT REL MONITOR ALARM				
STA MESSAGE REGISTER ARR FOR SINGLE OR MULTIPLE PULSE DURING CALL			1	X
	NO SURCHARGE			
			1,2	
STA MESSAGE REGISTER ARR TO CUT-OFF M LEAD SINGLE LONG PULSE			1	Z
	NO SURCHARGE			
			1,2	
STA MESSAGE REGISTER ARR TO HOLD FOR SGL POST CALL M LEAD PULSE			1	X,Y
	NO SURCHARGE			
			1,2	

EQUIPMENT NOTES

- 201. SURCHARGE FEATURE PROVIDED BY CONNECTION TO APPLIQUE UNIT.
- 202. LEADS ARE PROVIDED ONLY WHEN APP. FIG. 4 IS PROVIDED.
- 203. THE REQUIRED LENGTH OF A25C CONNECTOR CABLE SHALL BE ORDERED SEPARATELY.
- 204. INSTALLER SHALL REMOVE K OPTION ONLY FOR THOSE C.O. TRUNKS REQUIRING MESSAGE REGISTRATION.
- 205. INSTALLER SHALL REMOVE J OPTION ONLY FOR THOSE C.O. TRUNKS REQUIRING SURCHARGE REGISTRATION.

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DRAWING ISSUE
1
4D
5B
LJ

5

STATION MESSAGE REGISTER
PULSE AND SURCHARGE CIRCUIT

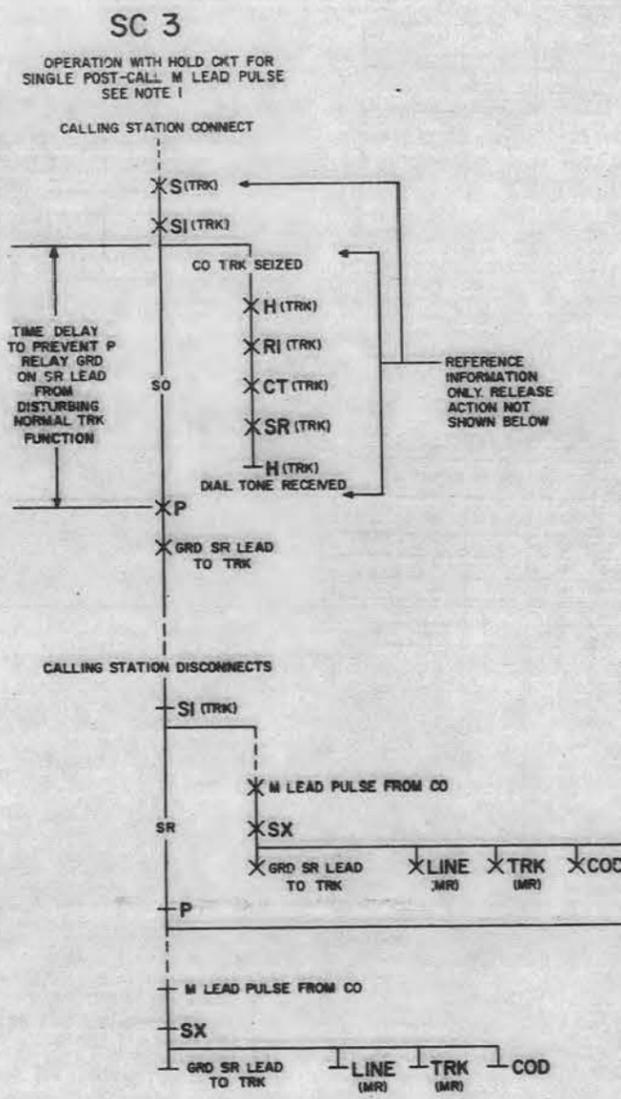
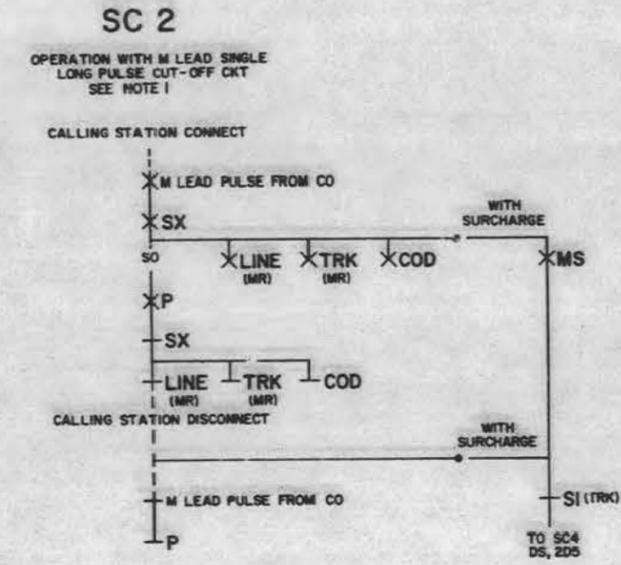
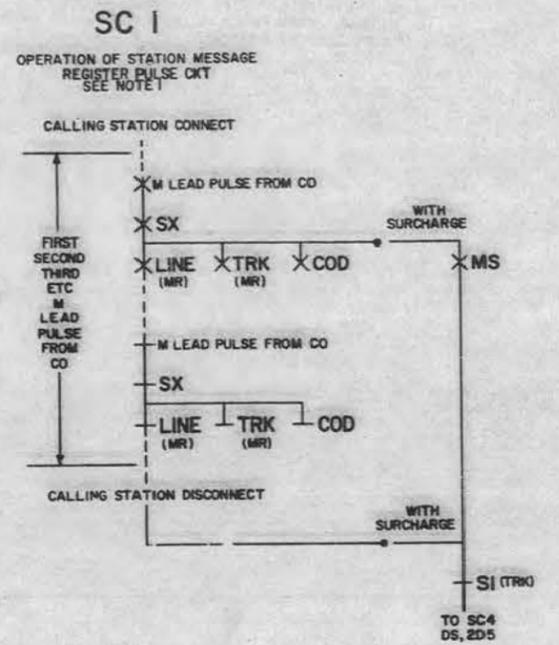
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DRAWING	1
ISSUE	1
3D	2
4D	3



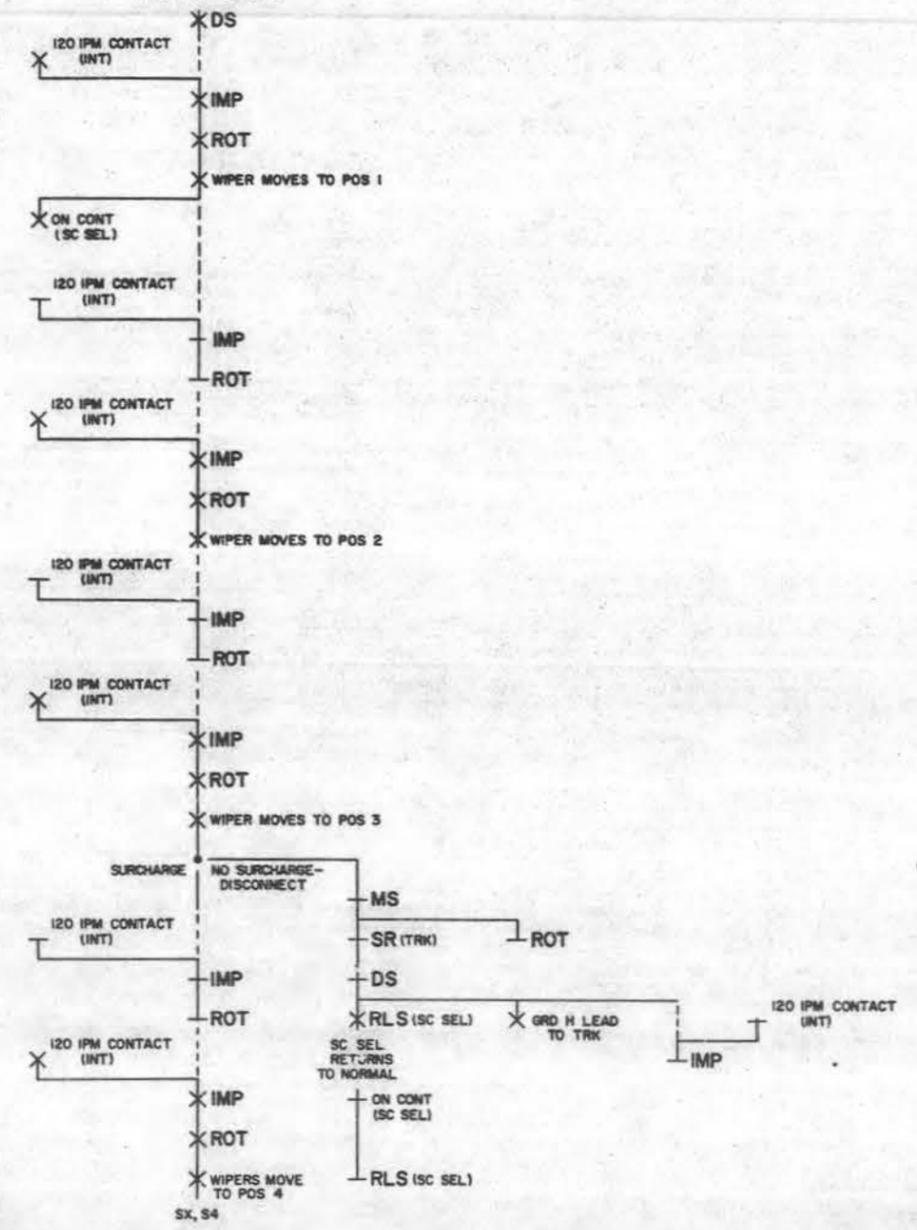
SHEET NOTES:
1. SC1, 2, AND 3 ASSUMES THAT THE LINE, LINK AND MARKER CIRCUIT IS NOT IN THE PROCESS OF COMPLETING A CAMP-ON CALL TERMINATION.

4

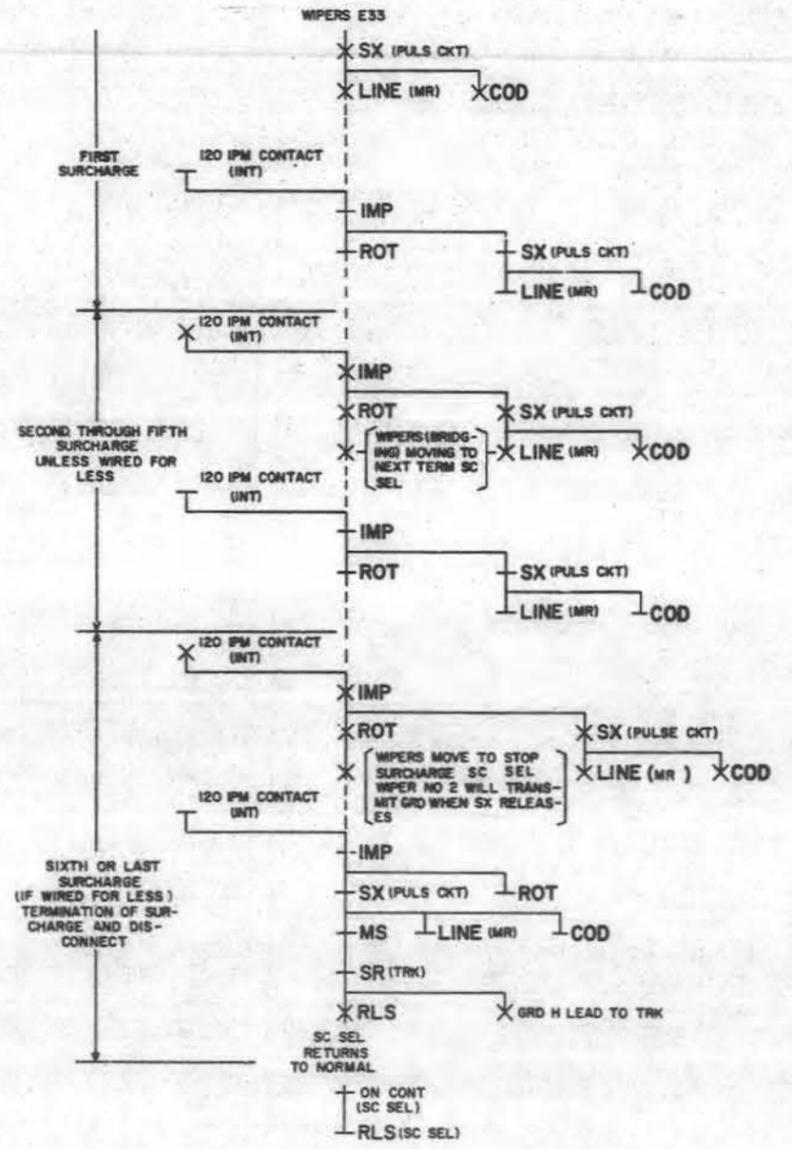
STATION MESSAGE REGISTER PULSE AND SURCHARGE CIRCUIT	SD-5E021-01-E1
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SC 4
SURCHARGE
SEE NOTE 1
FROM SC1 SJ, IH 16
FROM SC2 SJ, IH 33
FROM SC3 MS, IX 26

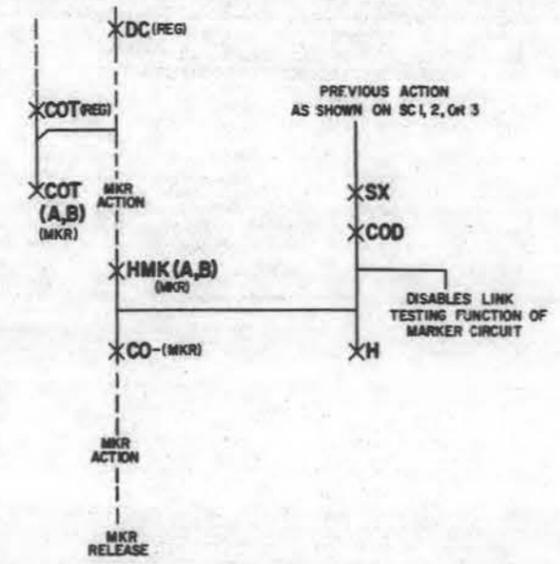


SHEET NOTE:
1. SC 4 ASSUMES THAT THE LINE, LINK AND MARKER CIRCUIT IS NOT IN THE PROCESS OF COMPLETING A CAMP-ON CALL TERMINATION.
2. SC 5 ASSUMES THAT RELAY COD OPERATES PRIOR TO THE OPERATION OF MARKER RELAYS HMK A,B.



SC 5

REGISTRATION PULSE APPLIED-MARKER
IN CAMP-ON CALL TERMINATION
SEE NOTE 2



DRAWING	HW
ISSUE	
1	
40	

4

STATION MESSAGE REGISTER PULSE AND SURCHARGE CIRCUIT	SD-5E021-01-E2
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CIRCUIT REQUIREMENTS															
APPARATUS				MECH REQ			CIRCUIT PREPARATION			TEST SET PREP	SEE TEST NOTE	DIRECT CURRENT FLOW REQ			REMARKS
DESIG	CODE	OPT	FIG	BSP FIG.	CONT PRES	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA				TEST WDG	TEST FOR	AFTER SOAK MA	
								CONN BAT	CONN GRD						
RELAYS															
ALM	1/2AK4		4	202			4(ALM)	1L(ALM)		G		0	11.9	11.3	MOUNTED WITH (SPARE)
COD	AF20		3	205				L(COD)		B		0	35.5	34	
DS	AF84		2	236			1(DS)	1U(DS)	1U(DS)	B/G		0	5.7	5.4	
H	AJ72		3	64				1J(H)	1L(H)	B/G		0	35.6	32	
								2U(H)	2L(H)	B/G		0	35.2	33.5	
IMP	AF83		4	8				L(IMP)	U(IMP)	B/G	1	0	8.2	7.8	
MR	1/2AK4		3	202			1M(MR)	2L(MR)		B	5	0	11.9	11.3	MOUNTED WITH (NV)
MS	AF140		2				1(MS)		1U(MS)	G		0	9.0	8.6	
NV	1/2AK4		3	202			10B(NV)		L(NV)	G	3	0	11.9	11.3	MOUNTED WITH (MR)
P	AG24	Y	1	168					U(P)	G	4	D	F.S.	13.7	13.0
												H		1.6	1.5
												R		0.7	1.0
P	AG24	Z	1	168			5M(P)	L(P)	U(P)	B/G		D	F.S.	13.7	13.0
												H		1.6	1.5
												R		0.7	1.0

CIRCUIT REQUIREMENTS															
APPARATUS				MECH REQ			CIRCUIT PREPARATION			TEST SET PREP	SEE TEST NOTE	DIRECT CURRENT FLOW REQ			REMARKS
DESIG	CODE	OPT	FIG	BSP FIG.	CONT PRES	ARM. TRVL	BLOCK OR INSULATE	TEST CLIP DATA				TEST WDG	TEST FOR	AFTER SOAK MA	
								CONN BAT	CONN GRD						
SX	AF73		1	203				1L(SX)	1U(SX)	B/G		P	0	15.5	
									2U(SX)	G		S	0	16.5	15.5
SEL															
SC	204C		2						B(SC)	G	2	ROT	0	300	
									PM1(SC)	G	2	RLS	0	150	

- TEST NOTES**
1. OPEN FF1 LEAD AT U WDG OF IMP RELAY.
 2. ADJUST PER BSP A468,003.
 3. OPEN GRD LEAD 1U (NV) RELAY.
 4. OPEN LEAD S1 AT T.S.
 5. REMOVE ALL MR- FUSES.

DRAWING ISSUE
1
30

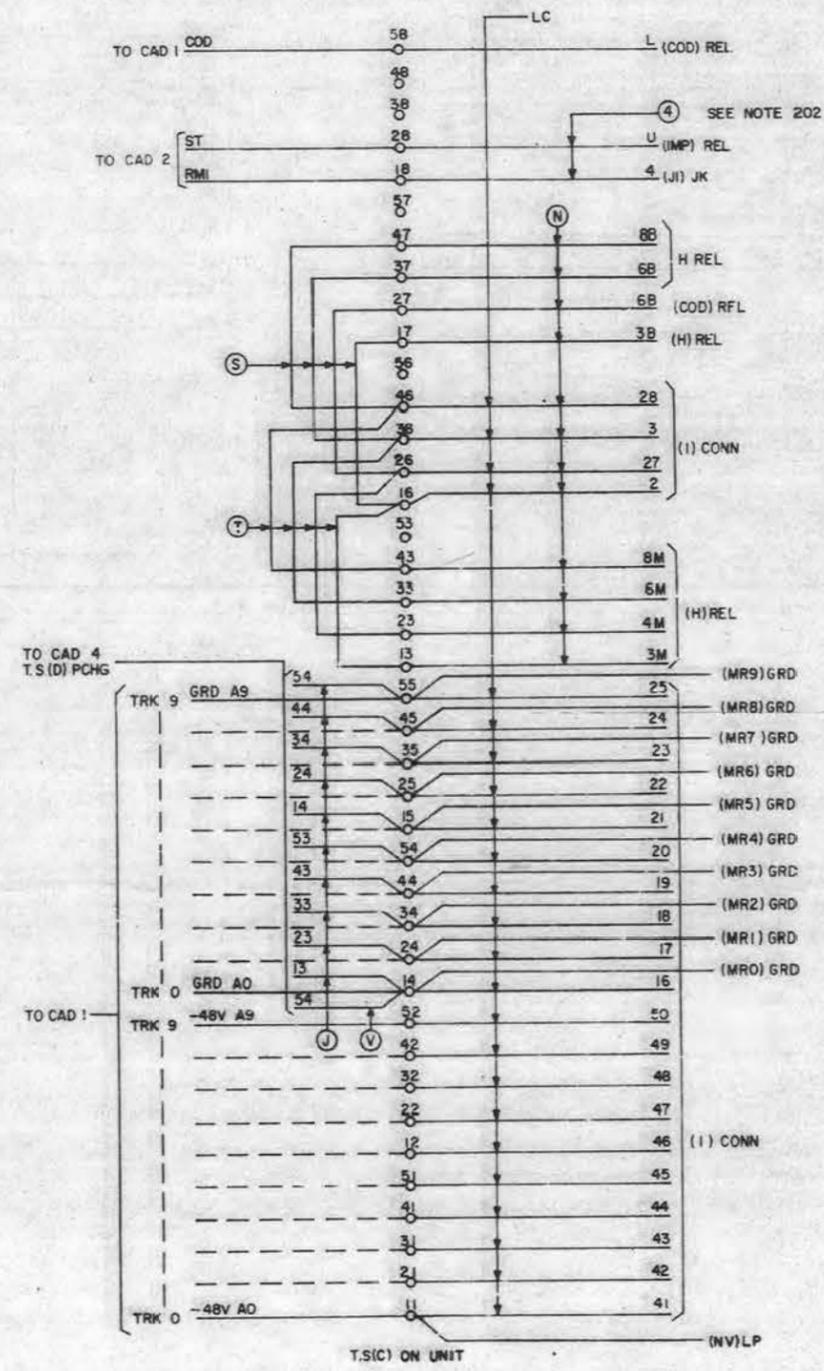
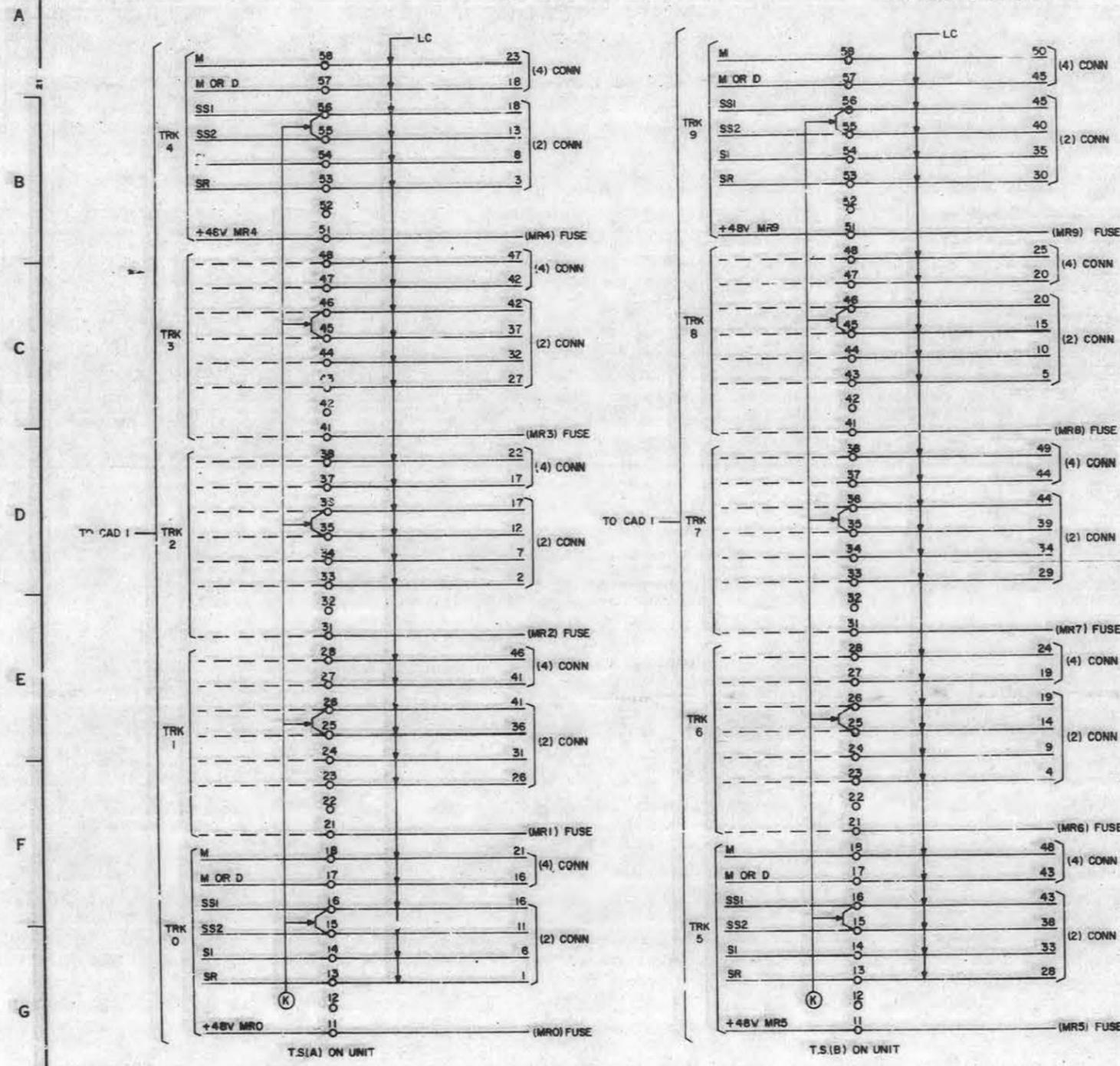
3

STATION MESSAGE REGISTER PULSE AND SURCHARGE CIRCUIT	SD-5E021-01-F1
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PART OF CAD 3

(FOR APP FIG. 3)
(SEE NOTE 204 AND 205)

DRAWING ISSUE	
1	HW
3D	PJC
4D	PJC
5B	PJC
6A	PJC



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STATION MESSAGE REC'TER
PULSE AND SURCHARGE
CIRCUIT

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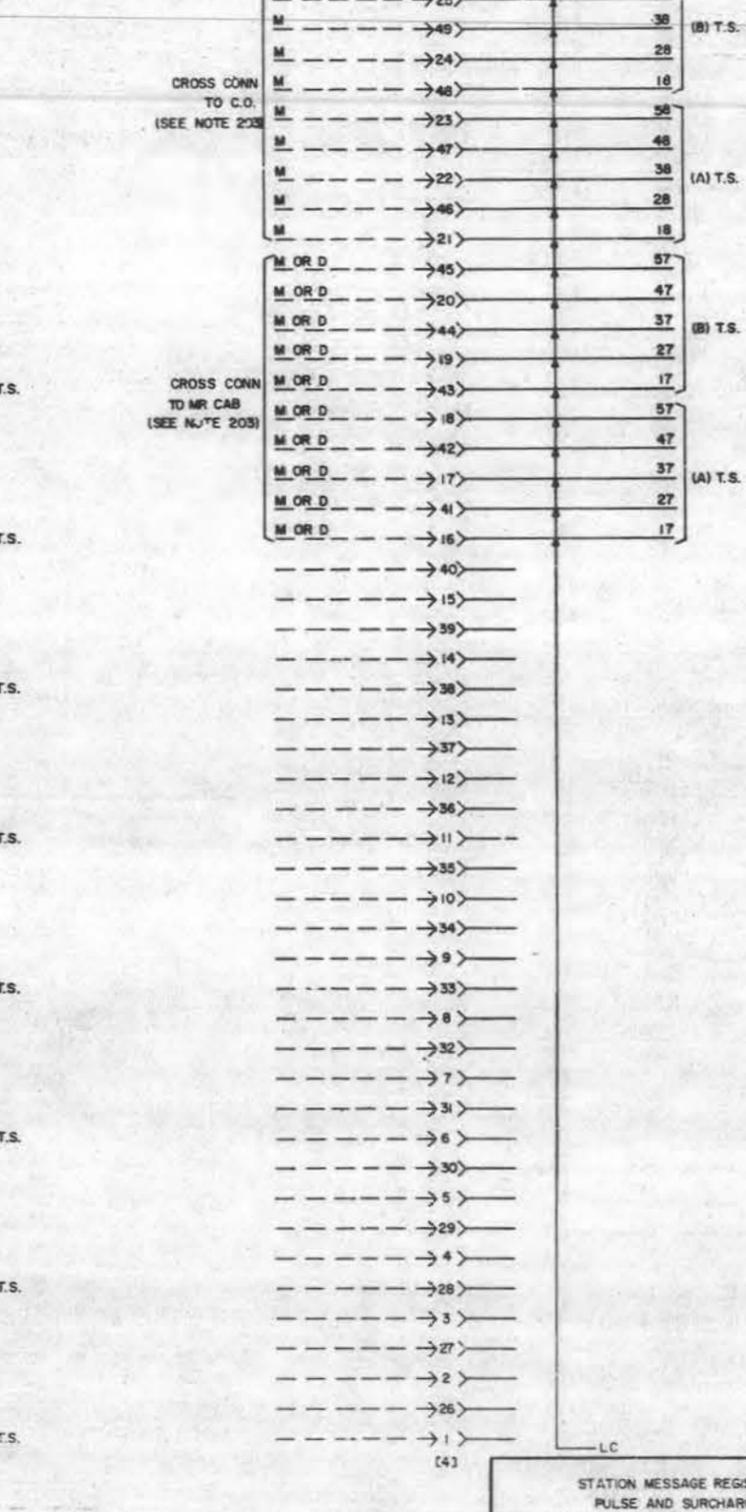
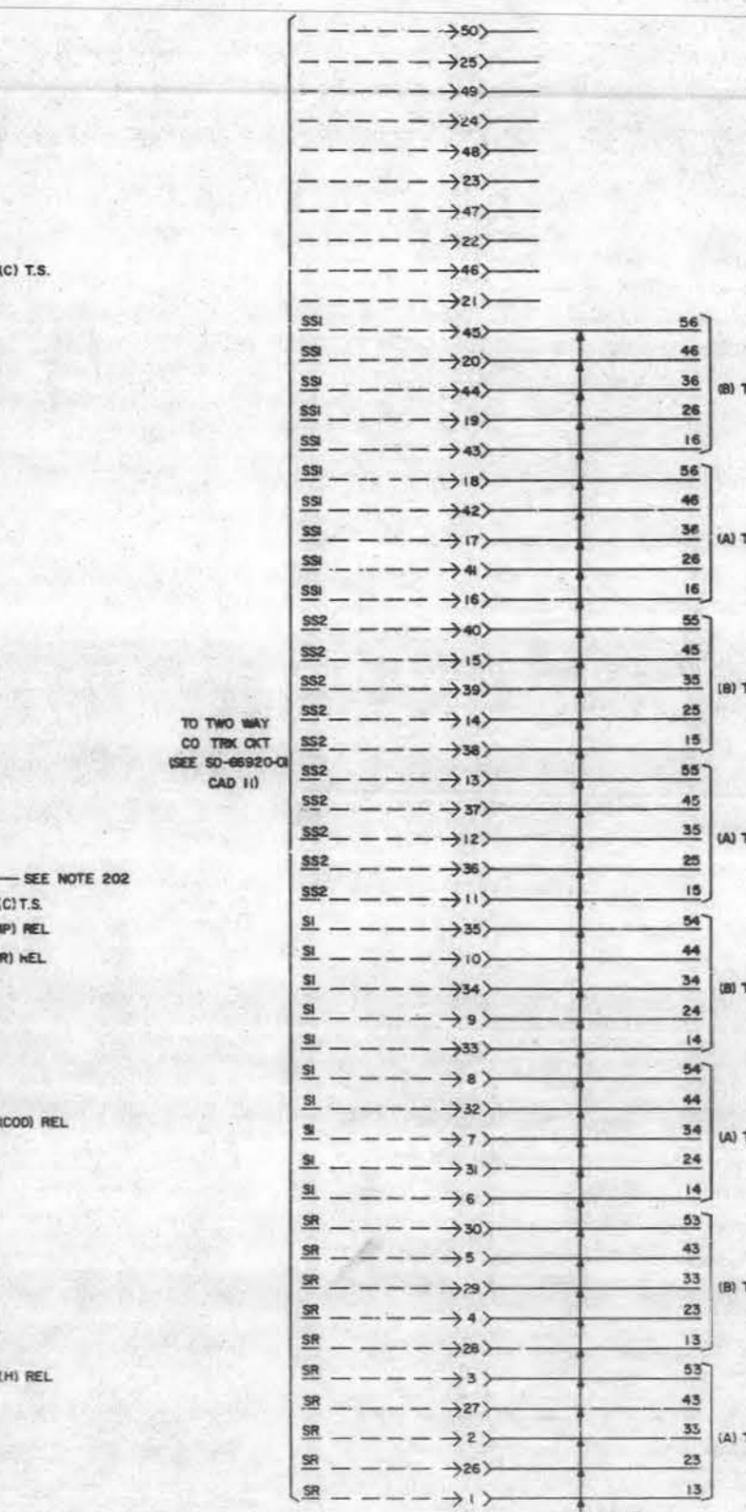
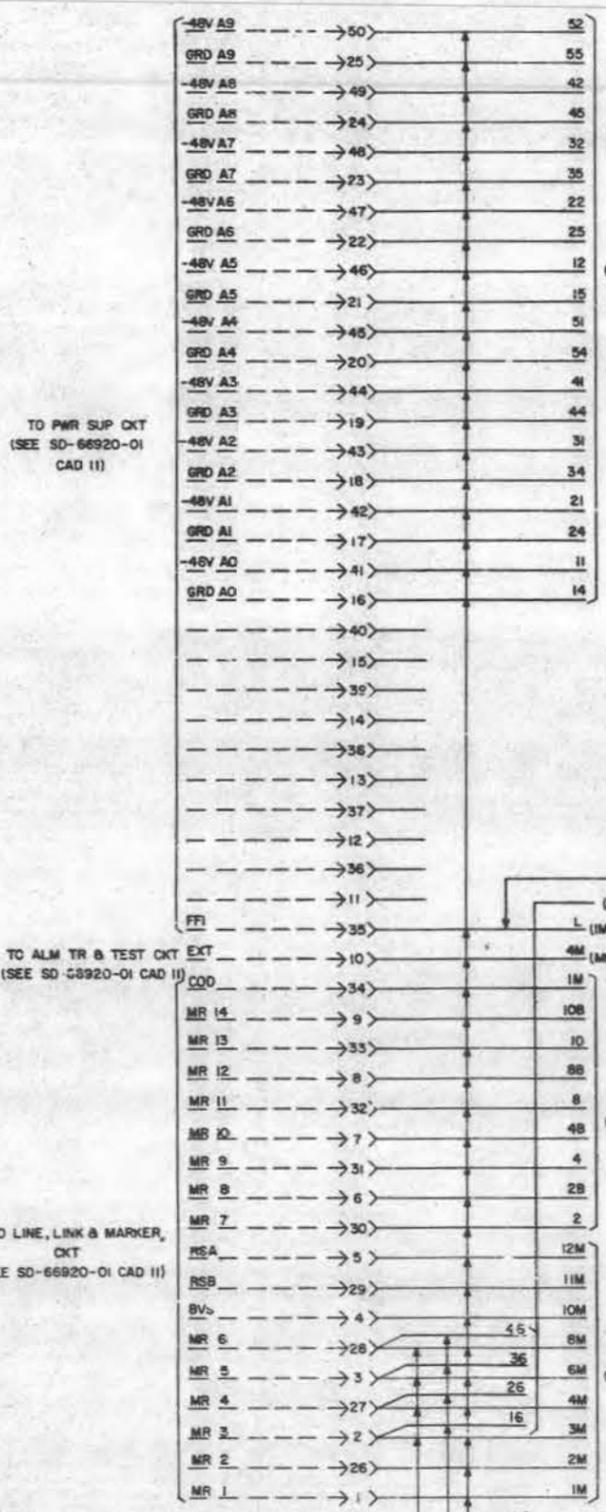
65

6

PART OF CAD 3
(FOR APP FIG. 3)

DRAWING ISSUE	
1	HW
2	HW
3D	PPH
4D	PPH

A
B
C
D
E
F
G
H



SD-5E021-01-G3

STATION MESSAGE REGISTER
PULSE AND SURCHARGE
CIRCUIT

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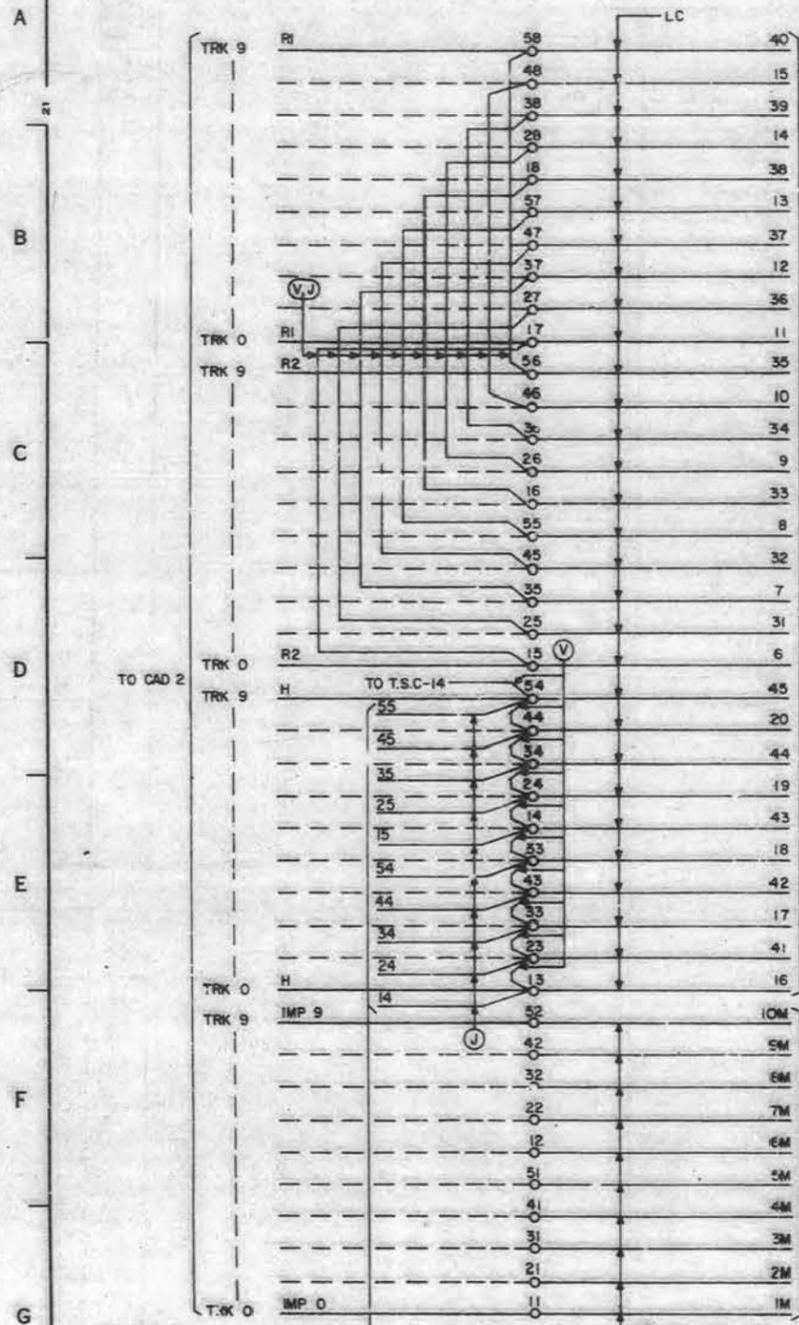
SD-5E021-01-G3

6S

4

CAD 4
(SEE NOTE 205)

DRAWING
ISSUE
1
EWE
DAK
EPH
RWC
RUS
LDJ
5B

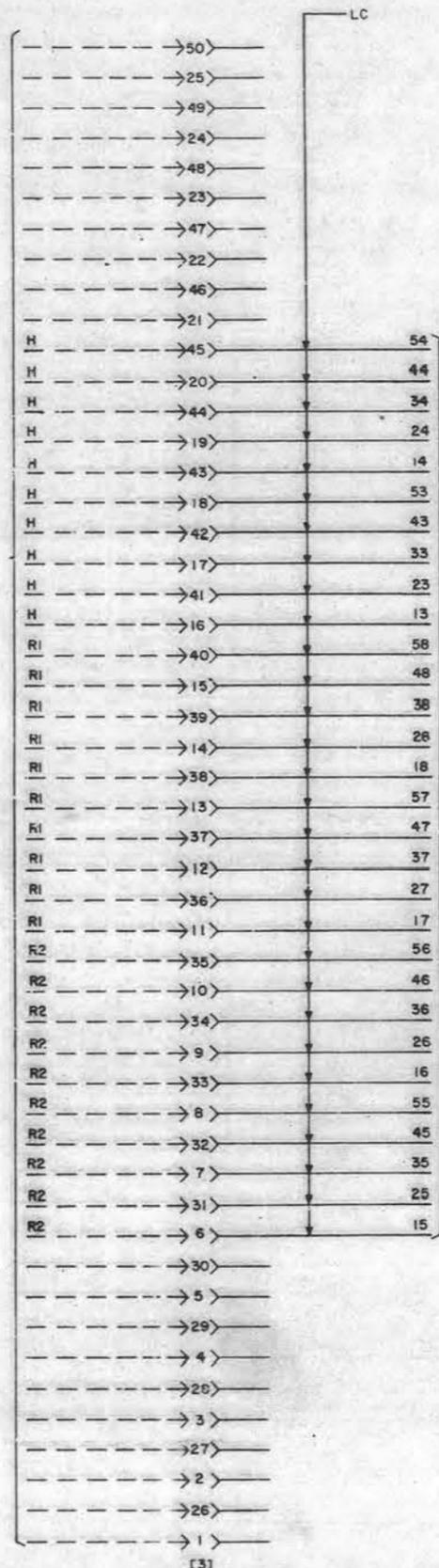


(3) CONN

TO TWO WAY
CO TRK CKT
(SEE SD-66920-01
CAD 11)

(IMP) REL

T.S.(D) ON UNIT (4) SEE NOTE 202



TO CAD 2
TO CAD 3
T.S. (C) PCHG

SD-5E021-01-G4

STATION MESSAGE REGISTER
PULSE AND SURCHARGE
CIRCUIT

SD-5E021-01-G4

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