

0 1 2 3 4 5 6 7 8 9

SHEET INDEX

CONTENTS	SHEET NO.	SHEET ISSUE NO.
SHEET INDEX OPTION INDEX SUPPORTING INFORMATION	A1	3
AS 1 DIGITAL TEST ACCESS FEATURE HOUSED IN ASSOCIATED UNIT CABINET	B1	3
AS 2 DTLU-E EQUIPPED LTP CABINET ARRANGEMENT	B2	3
AS 3 DIGITAL TEST ACCESS ARRANGEMENT	B3	3
AS 4 POWER & GROUND ARRANGEMENT	B4	3
AS 5 LED FUSE/FILTER BEZEL ALARMS	B5	3
AS 6 DTAU PANEL ASSEMBLY CONNECTOR DESIGNATIONS	B6	3
APP FIG. 1 & 2	C1	3
CIRCUIT NOTES EQUIPMENT NOTES INFORMATION NOTES	D1	3
INFORMATION NOTES (CONT)	D2	3
	D3	3
CADS 001 & 002	G1	3
CADS 003 & 004	G2	3
CADS 005 & 006	G3	3
CADS 007 & 008	G4	3
CADS 009 & 010	G5	3
CADS 011 & 012	G6	3
CADS 013 & 014	G7	3
CADS 015, 016, 017, 018 & 019	G8	3
CADS 020 & 021	G9	3

OPTION INDEX

APP OR WRG	RATED ON ISSUE	REF NOTES	LOCATION
Z	2		APP FIG. 1, B1/B6, B3/A3, B5/D2, B5/D4
Y	2		APP FIG. 1, B1/B6, B3/A3, B5/E8, B9/G5

DWG ISS	CD ISS	DATE ISSD	DRN	APP
1	1	02-23-90		
2M	1	01-10-91		
3M	1	07-11-91		

A
B
C
D
E
F
G
H

A
B
C
D
E
F
G
H

SUPPORTING INFORMATION

CATEGORY	NO.

SHEET INDEX NOTES

1. ONLY THE LATEST ISSUE, OR ISSUES IF CONCURRENT, ARE SHOWN IN THE INDEX.
2. FOR REISSUES, A CHANGED OR NEW SHEET IS ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.
3. THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

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BT13

5ESSSM SWITCHING EQUIPMENT

**DIGITAL TEST ACCESS
CIRCUIT**

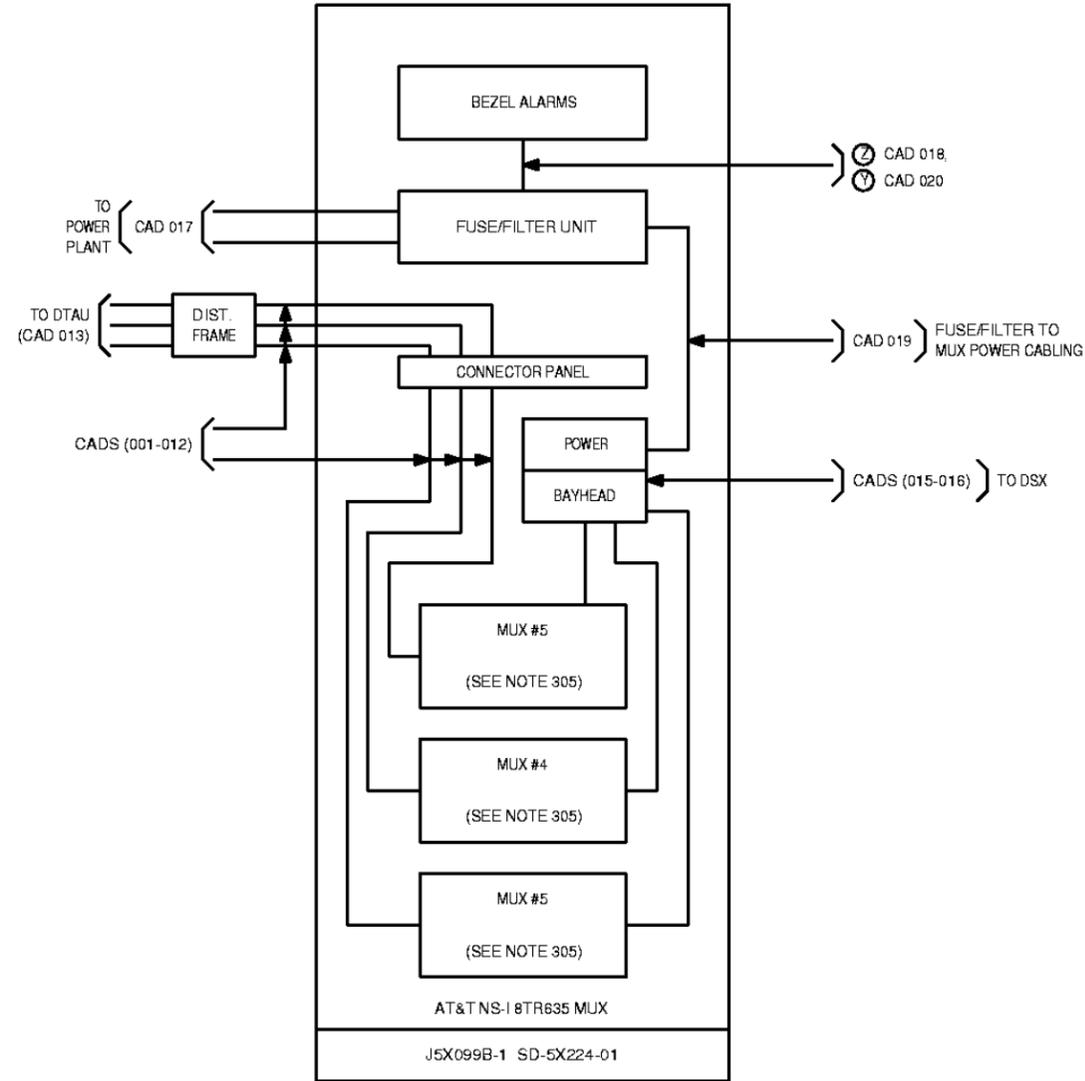
DWG SIZE C2	ISSUE 3M
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AT&T SD-5X224-01 SHEET 20 **A1**

0 1 2 3 4 5 6 7 8 9 PRINTED IN U.S.A.

AS 1

DIGITAL TEST ACCESS FEATURE HOUSED
IN ASSOCIATED UNITS CABINET



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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M

AT&T

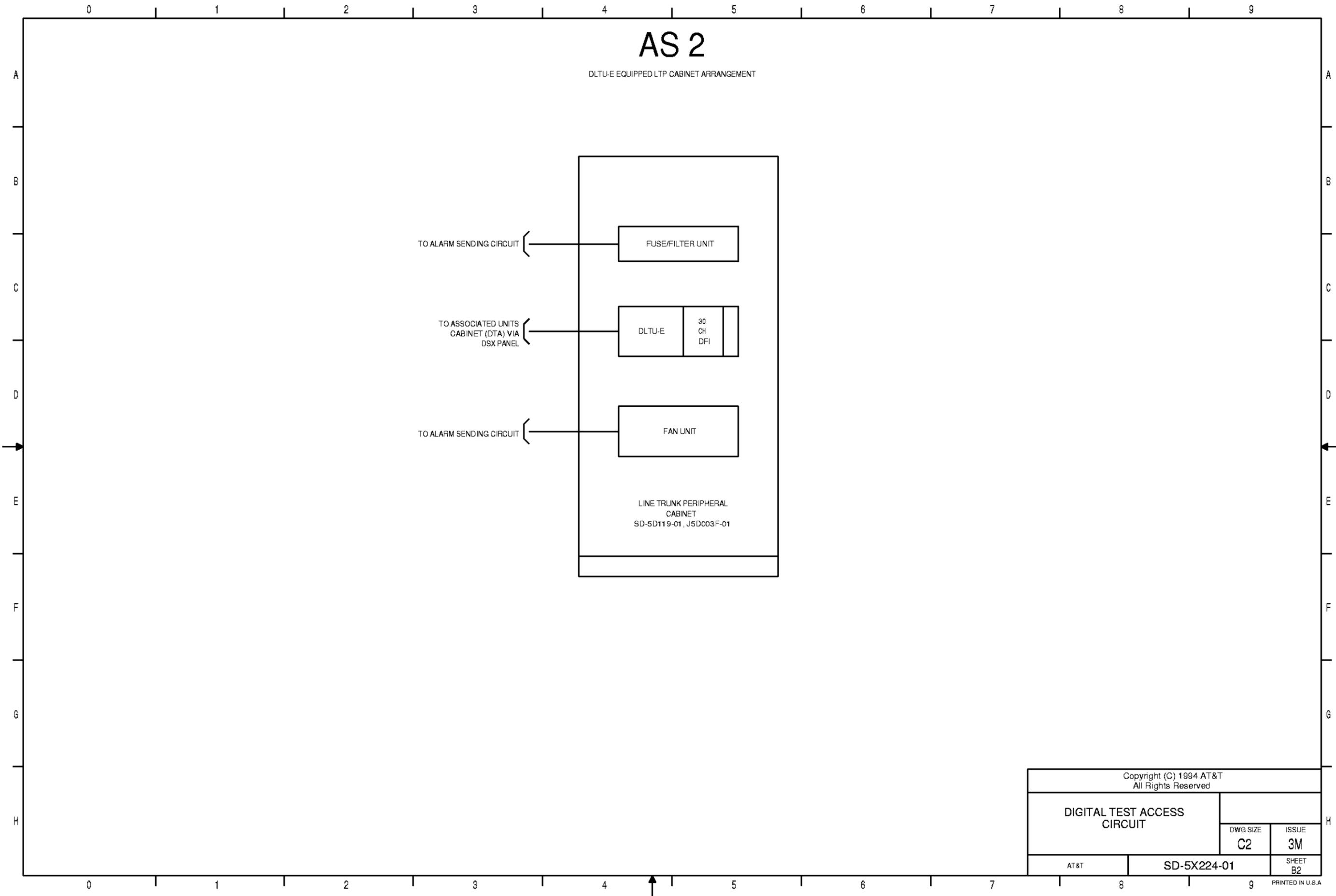
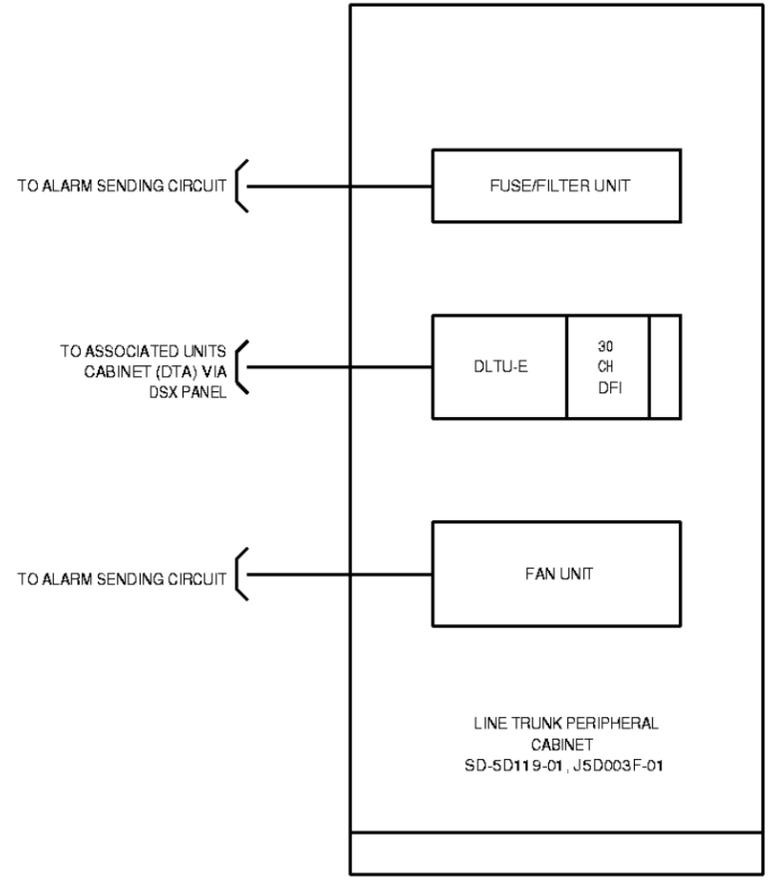
SD-5X224-01

SHEET
B1

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AS 2

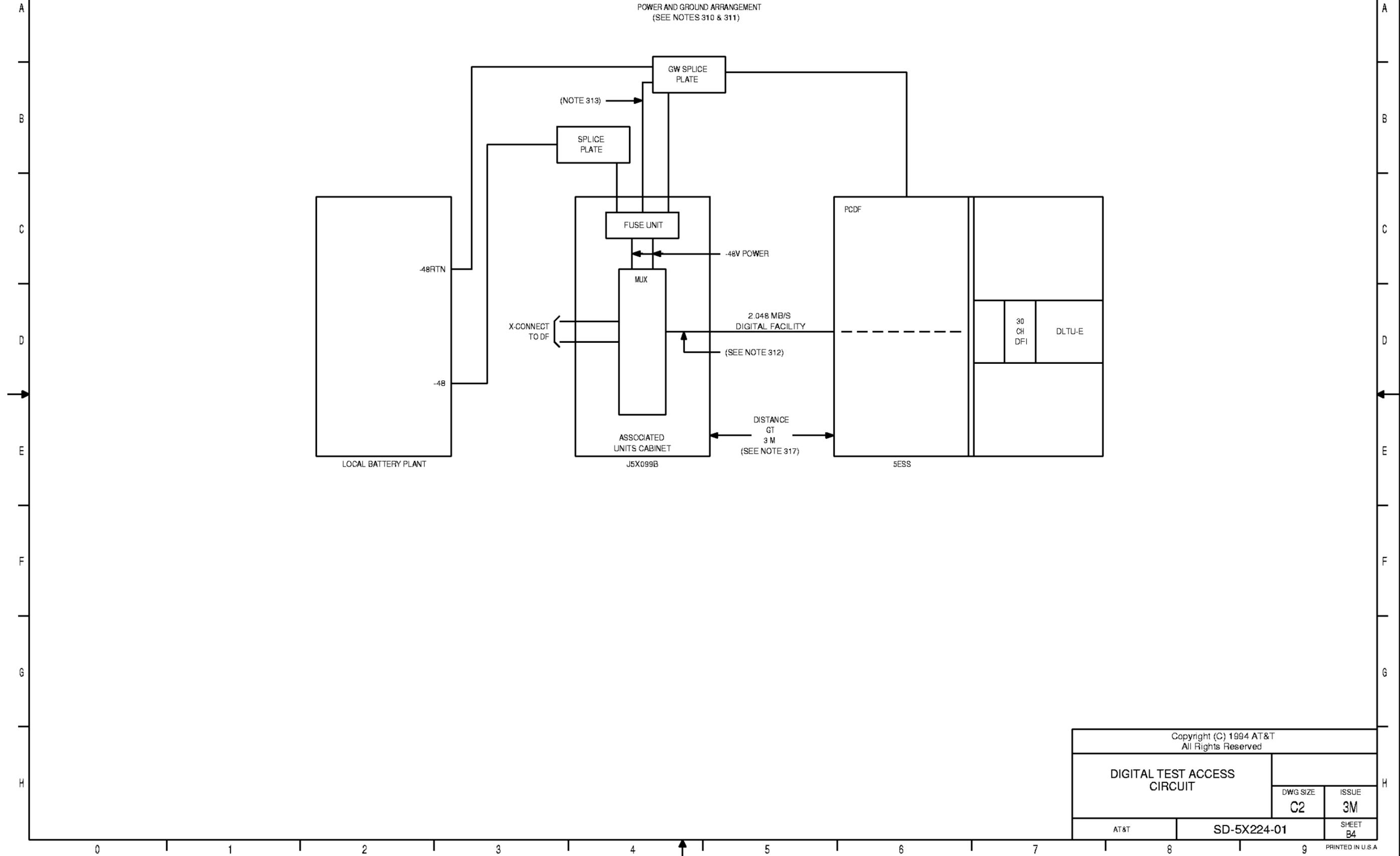
DLTU-E EQUIPPED LTP CABINET ARRANGEMENT



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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET B2

AS 4

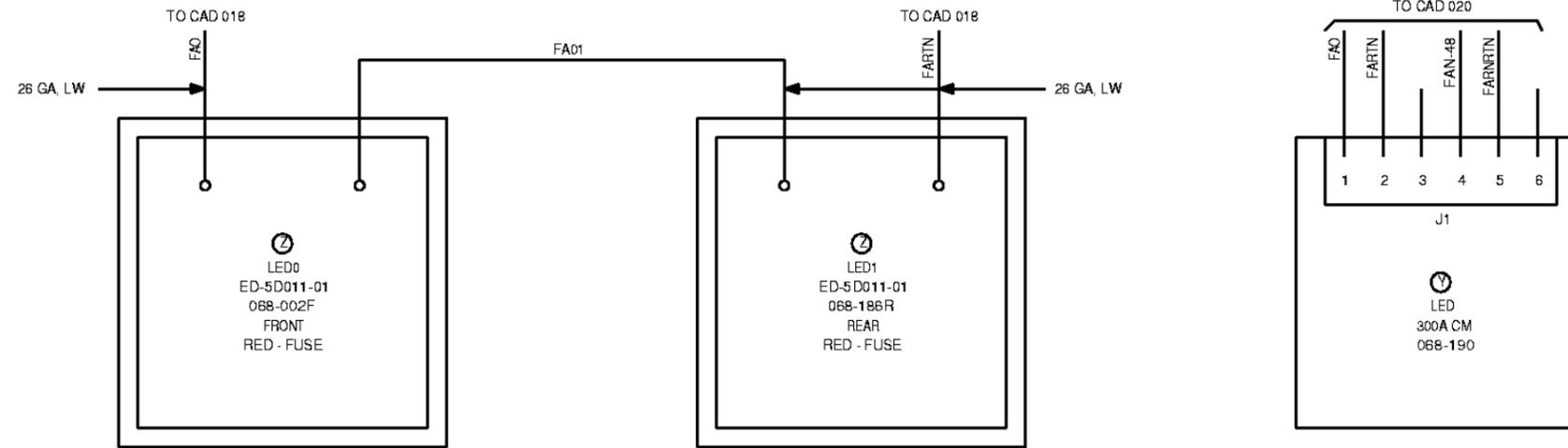
POWER AND GROUND ARRANGEMENT
(SEE NOTES 310 & 311)



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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET B4

AS 5

LED FUSE/FILTER BEZEL ALARMS



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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M

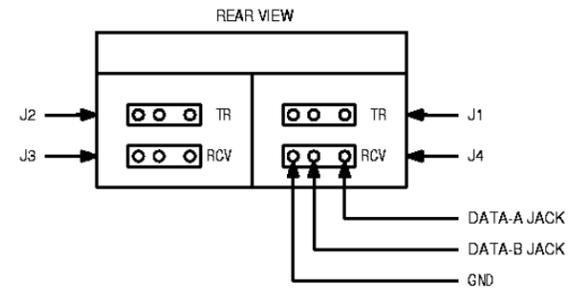
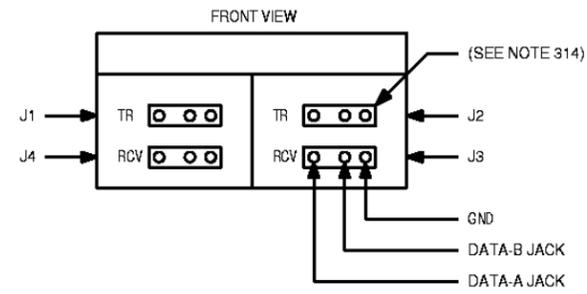
AT&T	SD-5X224-01	SHEET B5
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AS 6

DTAU PANEL ASSEMBLY CONNECTOR DESIGNATION

DTAU FACEPLATE PINOUTS



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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M

AT&T

SD-5X224-01

SHEET
B6

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APP FIG. 1

DIODE, LIGHT EMITTING

<u>OPT</u>	<u>DESIG</u>	<u>EQUIP LOC</u>	<u>FS/LOC</u>	<u>CODE</u>
Z	LED0	068-002F-1	5D2	549D
Z	LED1	068-186R-2	5D4	549D
Y	LED	068-190	5D7	300A CM

APP FIG. 2

<u>OPT</u>	<u>DESIG</u>	<u>LOC</u>	<u>CODE</u>
		01-044-107	J5D003AU-2

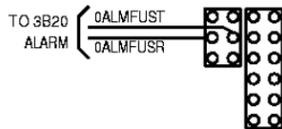
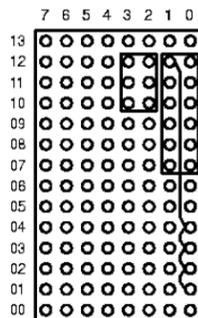
Copyright (C) 1994 AT&T All Rights Reserved		
DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET C1

INFORMATION NOTES (CONT):

320. APP FIG. 2 IS PROVIDED TO ELIMINATE A FALSE ALARM SIGNAL BETWEEN THE FAN/FUSE UNIT AND THE MCC TERMINAL. THE 982SC APPARATUS PROVIDED IN APP FIG. 2 SHALL BE PLUGGED ONTO EQUIPMENT LOCATION 01-044-107.

321. SCAN ALARM

VIEWED FROM
FRAME WIRING SIDE
068-044-



322. 8TR 635 SYSTEM DESCRIPTION
THIS NOTE SHOWS THE TIME SLOT ASSIGNMENTS IN THE MUX.

CONNECTORS	X1	X2	X3	X4	X5
TIME SLOT NUMBERS	1	9	17	25	
	2	10	18	26	
	3	11	19	27	
	4	12	20	28	
	5	13	21	29	
	6	14	22	30	
	7	15	23	31	
	8	16*	24		

SYSTEM SUBBAY

ALL TIME SLOTS (TS1 . . . TS31) USED TO 64 KBIT/S DATA TRANSFER.

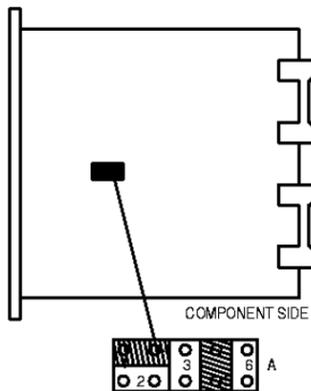
DCU = DATA CONVERTER UNIT (8 CHANNEL)

SESS	PIDB	TIMESLOTS	30 CHANNEL	OPERATION
TS0		FRAME ALIGNMENT CHANNEL CONTROL BITS		
TS1-TS15		TRAFFIC		
TS16		SIGNALING		
TS17-TS31		TRAFFIC		

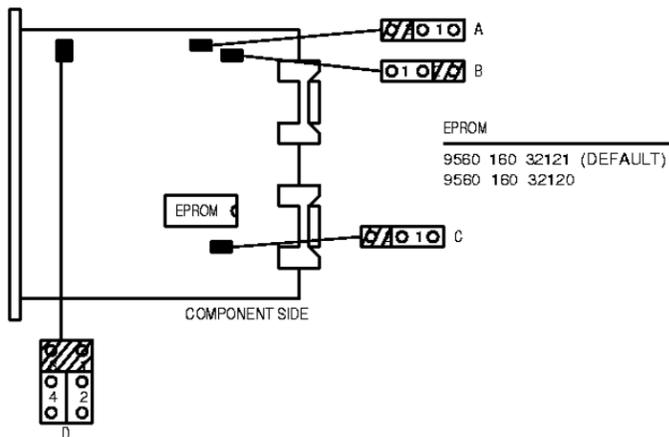
* TS16 - THE MIU IS OPTIOND FOR 30 CHANNEL OPERATION. THIS CHANNEL CONTAINS SIGNALING.

INFORMATION NOTES (CONT):

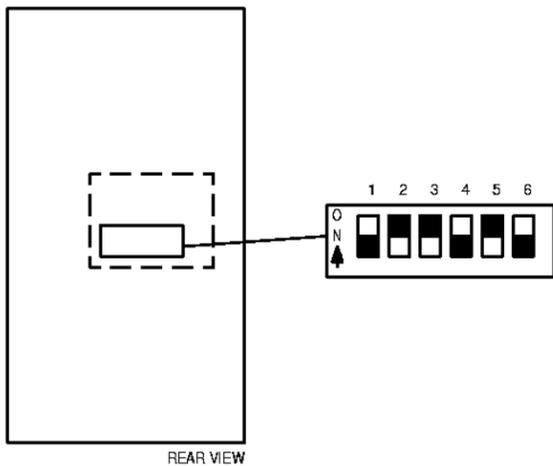
323. 8TR LINING-UP
STATION INTERFACE UNIT (SIU) DTA APPLICATION.
THIS NOTE SHOWS THE OPTION PLUG SETTINGS FOR THE SIU PACK.



B. CENTRAL PROCESSING UNIT (CPU) DTA APPLICATION.
THIS NOTE SHOWS THE OPTION PLUG SETTING FOR THE CPU.



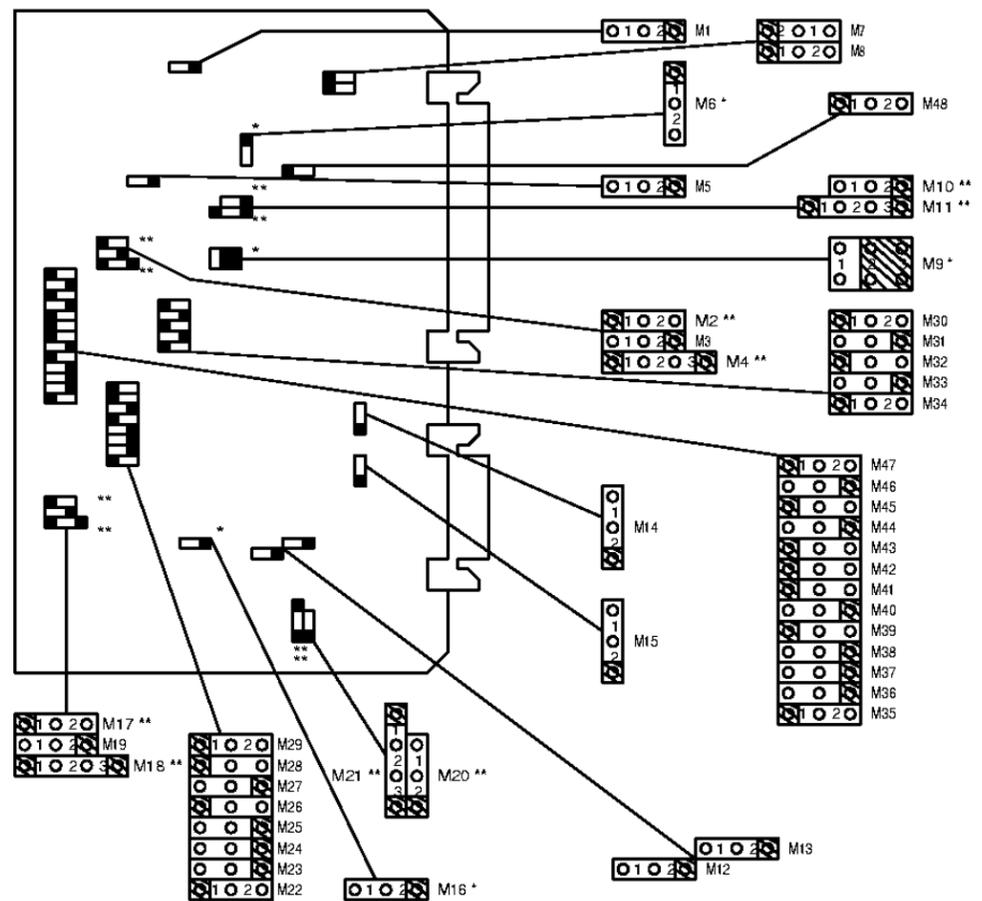
C. POCKET TERMINAL, DTA APPLICATION.
THIS NOTE SHOWS THE DIP SWITCH SETTINGS FOR THE POCKET TERMINAL.



INFORMATION NOTES (CONT):

323. 8TR LINING-UP (CONT)
D. MULDEX- AND INTERFACE UNIT (MIU), DTA APPLICATION, 120 OHM TWISTED PAIR
M2, M10, M17, M20
M4, M11, M18, M21
M6
M9, M16

THIS NOTE SHOWS THE OPTION PLUG SETTINGS FOR THE MIU, 120 OHM TWISTED PAIR. THE ASTERICK INDICATES ON OPTION CHANGE FOR THE DTA.



* CHANGED FOR DTA
** CHANGED FOR DTA, 120 OHM

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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M
AT&T	SHEET D2

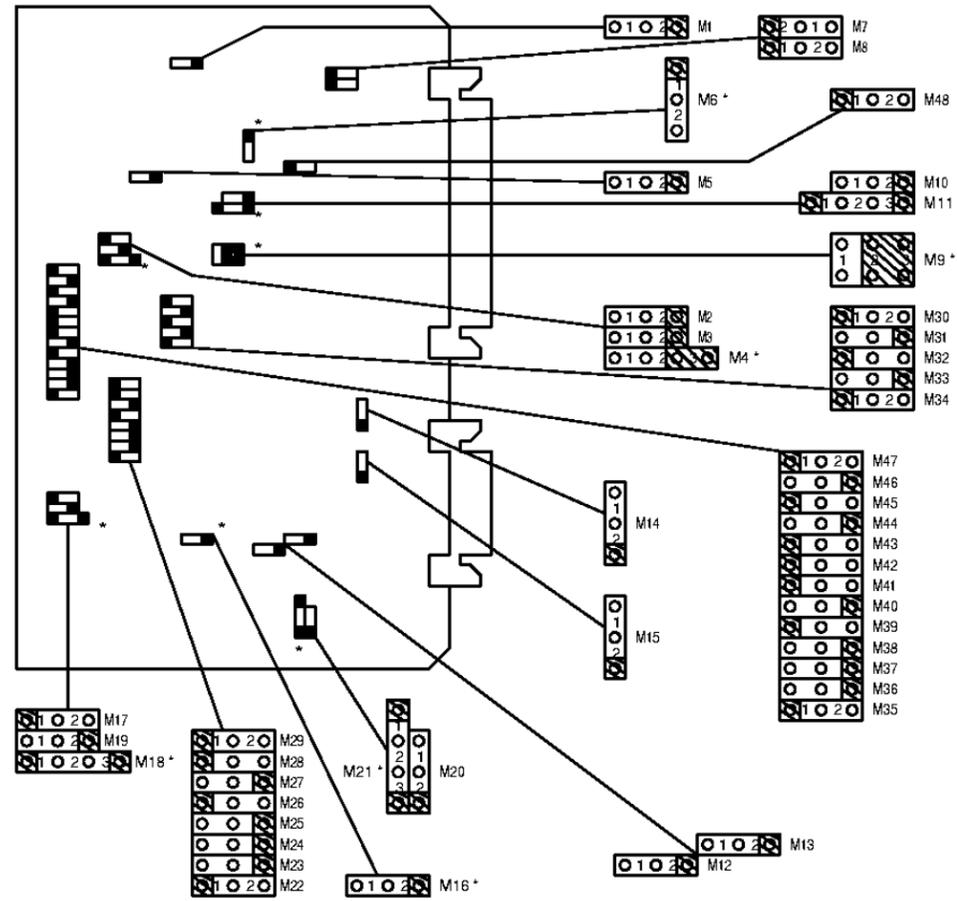
SD-5X224-01

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INFORMATION NOTES (CONT):

323. 8TR LINING-UP (CONT)
 E. MULDEX- AND INTERFACE UNIT (MIU), DTA APPLICATION, 75 OHM COAX
 M4, M11, M18, M21
 M6
 M9, M16

THIS NOTE SHOWS THE OPTION PLUG SETTINGS FOR THE MIU, 75 OHM COAX.
 THE ASTERICK INDICATES ON OPTION CHANGE FOR THE DTA.

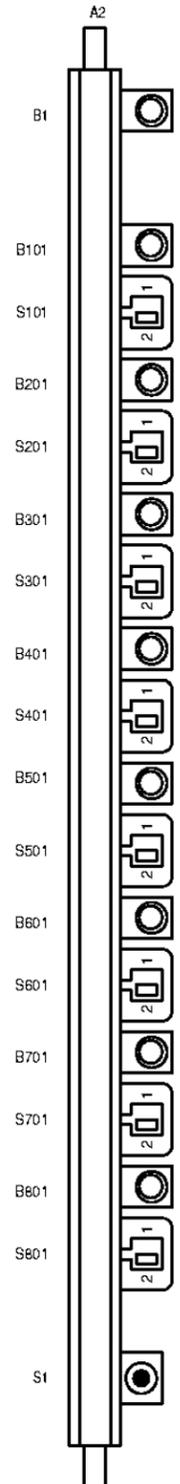


* CHANGED FOR DTA

INFORMATION NOTES (CONT):

323. 8TR LINING-UP (CONT)
 F. DATA CONVERTER UNIT (DCU), S101-S801 OFF POS-2

THIS NOTE SHOWS THE OPTION PLUG SETTINGS FOR THE DCU.
 THE ASTERICK INDICATES ON OPTION CHANGE FOR THE DTA.

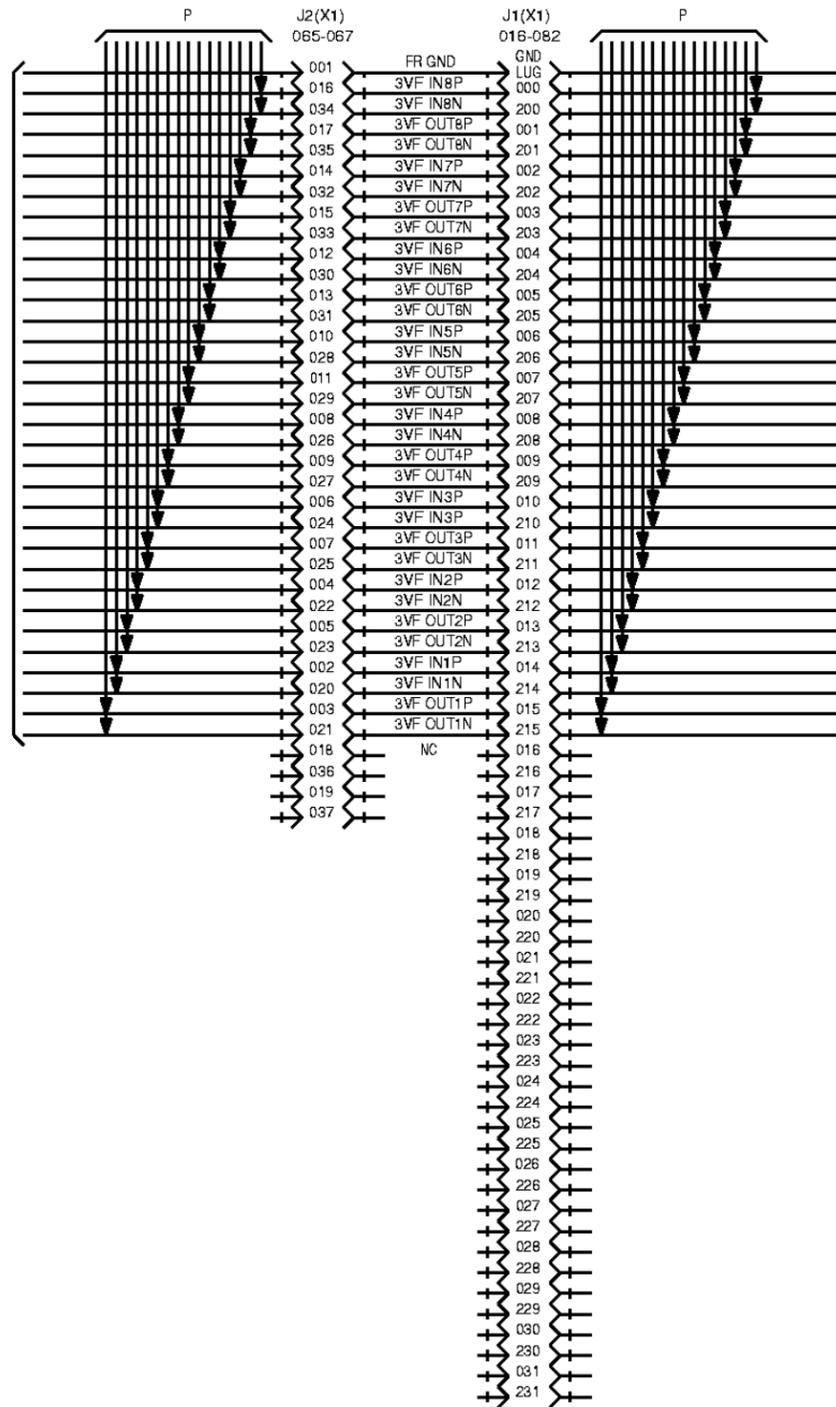


- B1:
 EQUIPMENT ALARM INDICATION (LED);
 LIGHTS IF THE INPUT SIGNAL OF ONE
 OR MORE CHANNELS - OF WHICH THE
 LIS-ALARM (*) ARE NOT SUPPRESSED
 - DROPS OFF.
 *) LIS = LOSS OF INCOMING SIGNAL.
- B101 ... B801:
 INDIVIDUAL ALARM INDICATIONS
 (LED) FROM EACH OF THE 8 DATA
 CHANNELS IN THIS UNIT. THESE LEDS
 MAY BURN AT THE CHANNELS WITHOUT
 SUPPRESSED LIS-ALARM.
 IF THE INCOMING SIGNAL FOR ONE OR
 MORE CHANNELS DROPS OFF (LIS-ALARM),
 B1 WILL BURN (EQUIPMENT ALARM), AFTER
 WHICH THE ALARM-CAUSING WILL BE
 INDICATED BY THE CORRESPONDING LED
 AFTER PRESSING PUSH BUTTON S1.
- S1:
 PUSH BUTTON TO LOCATE THE ALARM-
 CAUSING CHANNELS IN CASE OF
 EQUIPMENT ALARM
 (LIGHTS B101 ... B801).
- S101 ... S801:
 SWITCHES OF THE 8 DATA CHANNELS
 TO SELECT THE POSSIBILITY TO GIVE
 AN INDIVIDUAL LIS-ALARM FOR EACH
 OF THE CHANNELS. THE ALARM CIRCUITS
 OF UNUSED DATA CHANNELS MUST BE
 SWITCHED OFF (SUPPRESSED INDIVIDUAL
 LIS-ALARM).
 POSITION 2 SWITCHES LIS-ALARM OFF.

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DIGITAL TEST ACCESS CIRCUIT		DWG SIZE C2
		ISSUE 3M
AT&T	SD-5X224-01	SHEET D3

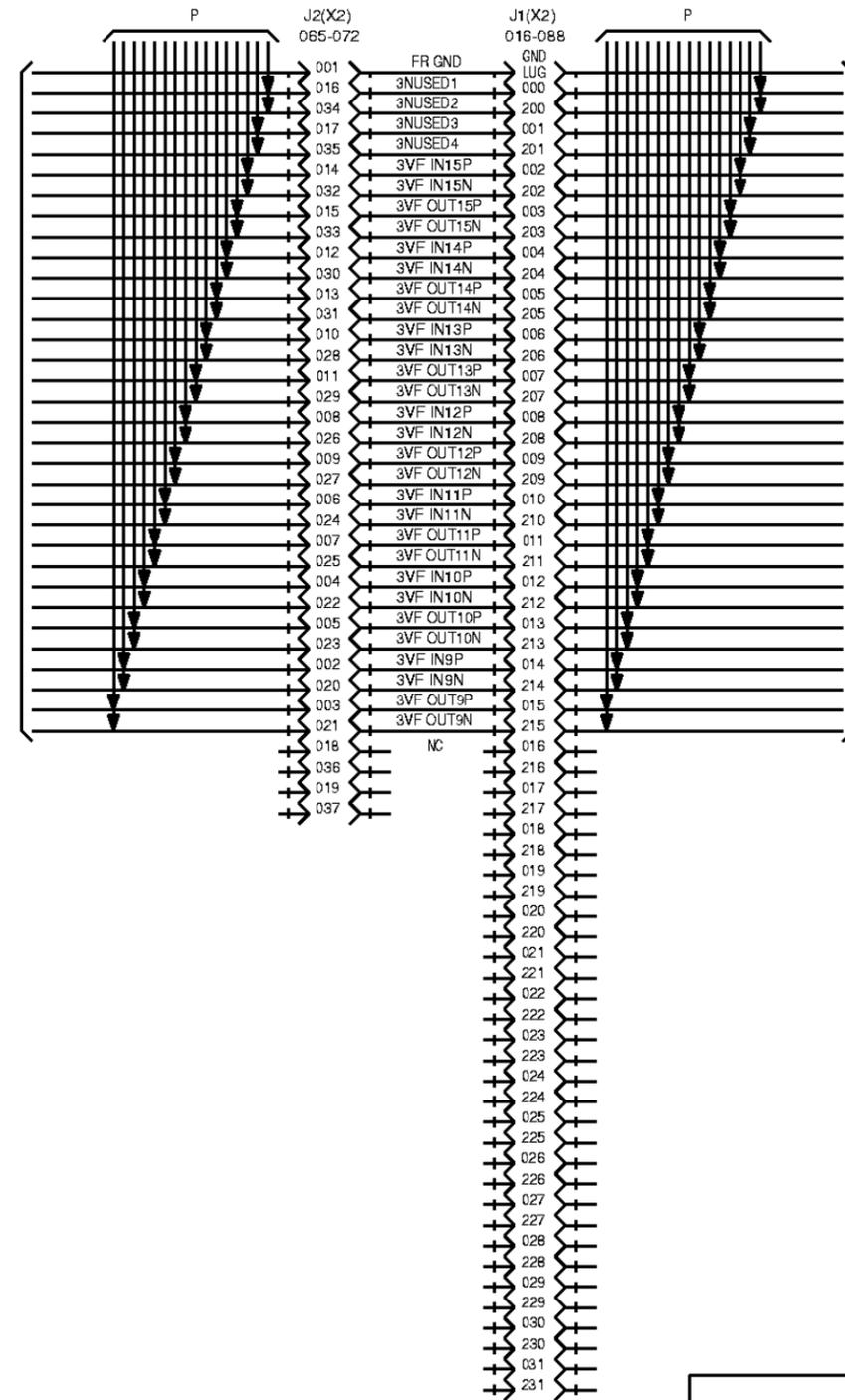
CAD 001

MULTIPLEXER #3 (64 KB) OUTPUT



CAD 002

MULTIPLEXER #3 (64 KB) OUTPUT

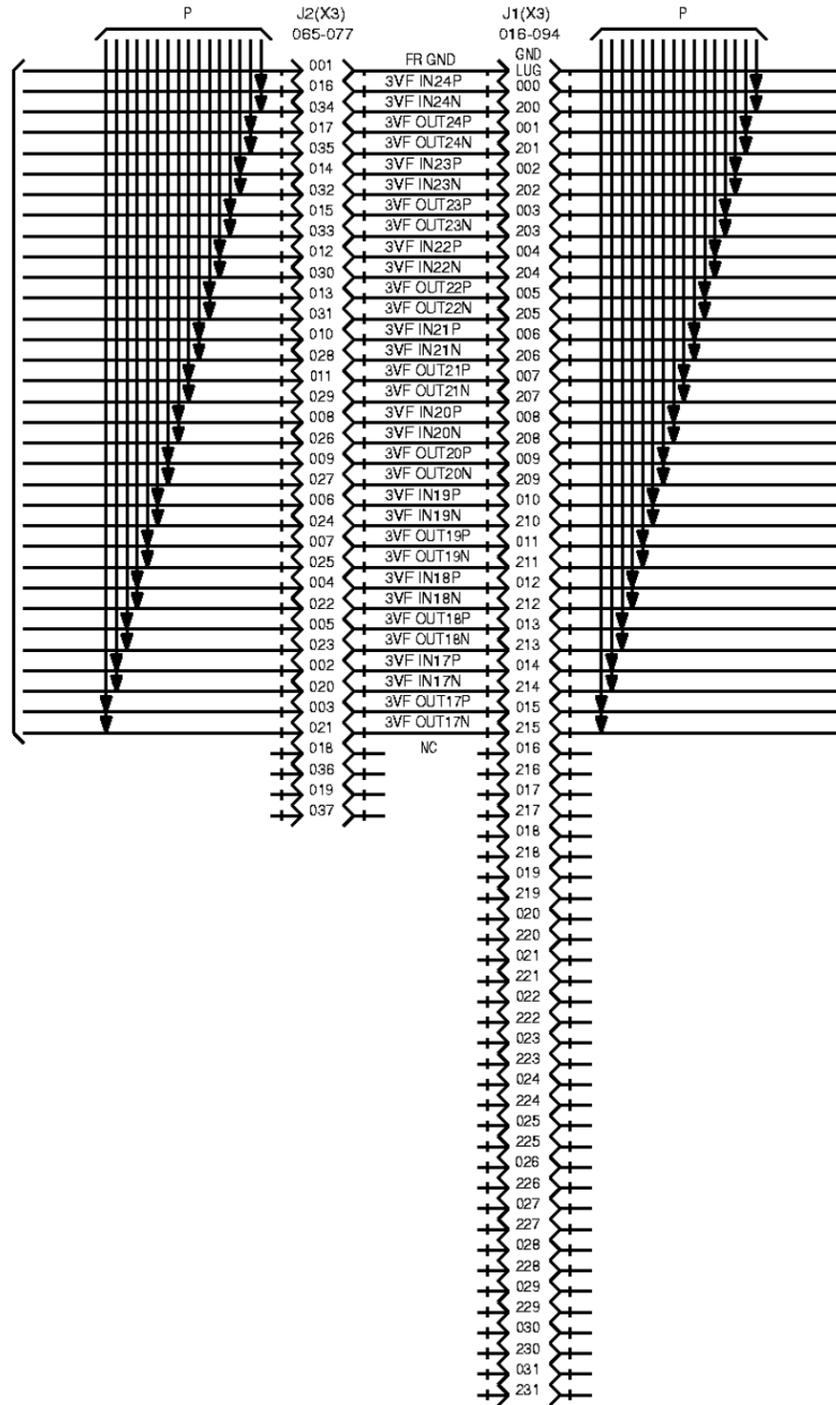


DATA
CONVERTER
UNIT

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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G1

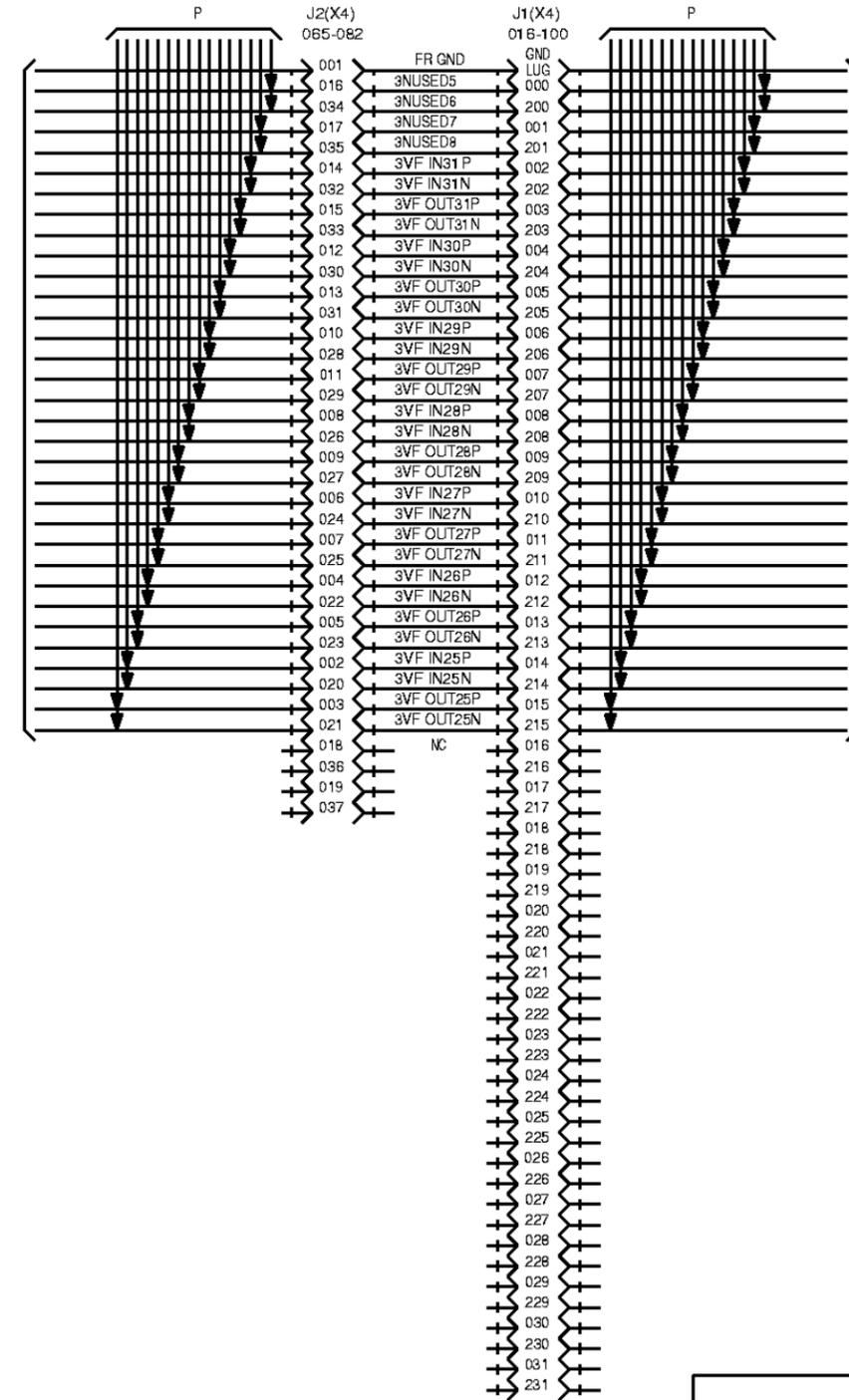
CAD 003

MULTIPLEXER #3 (64 KB) OUTPUT



CAD 004

MULTIPLEXER #3 (64 KB) OUTPUT

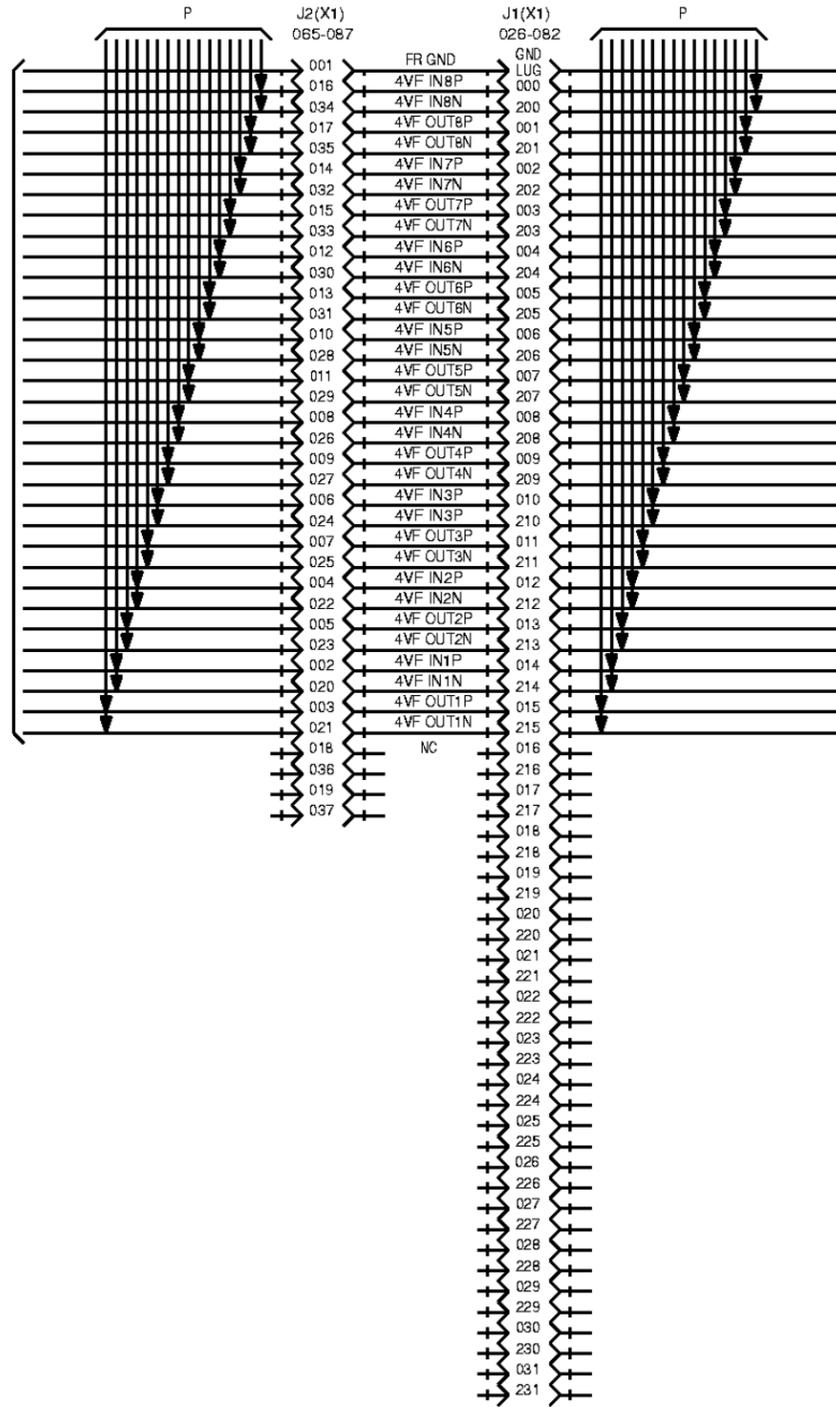


DATA
CONVERTER
UNIT

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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G2

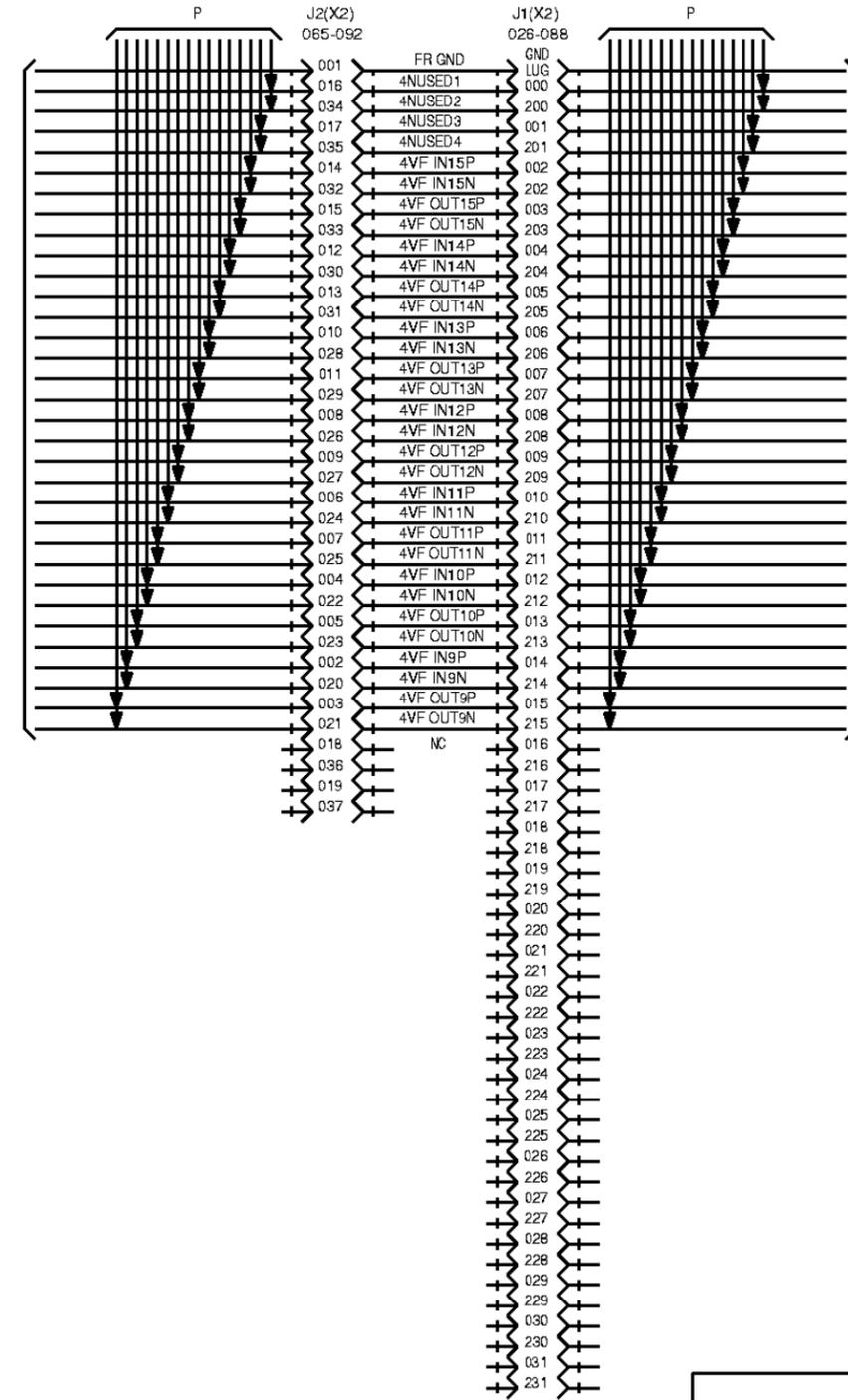
CAD 005

MULTIPLEXER #4 (64 KB) OUTPUT



CAD 006

MULTIPLEXER #4 (64 KB) OUTPUT

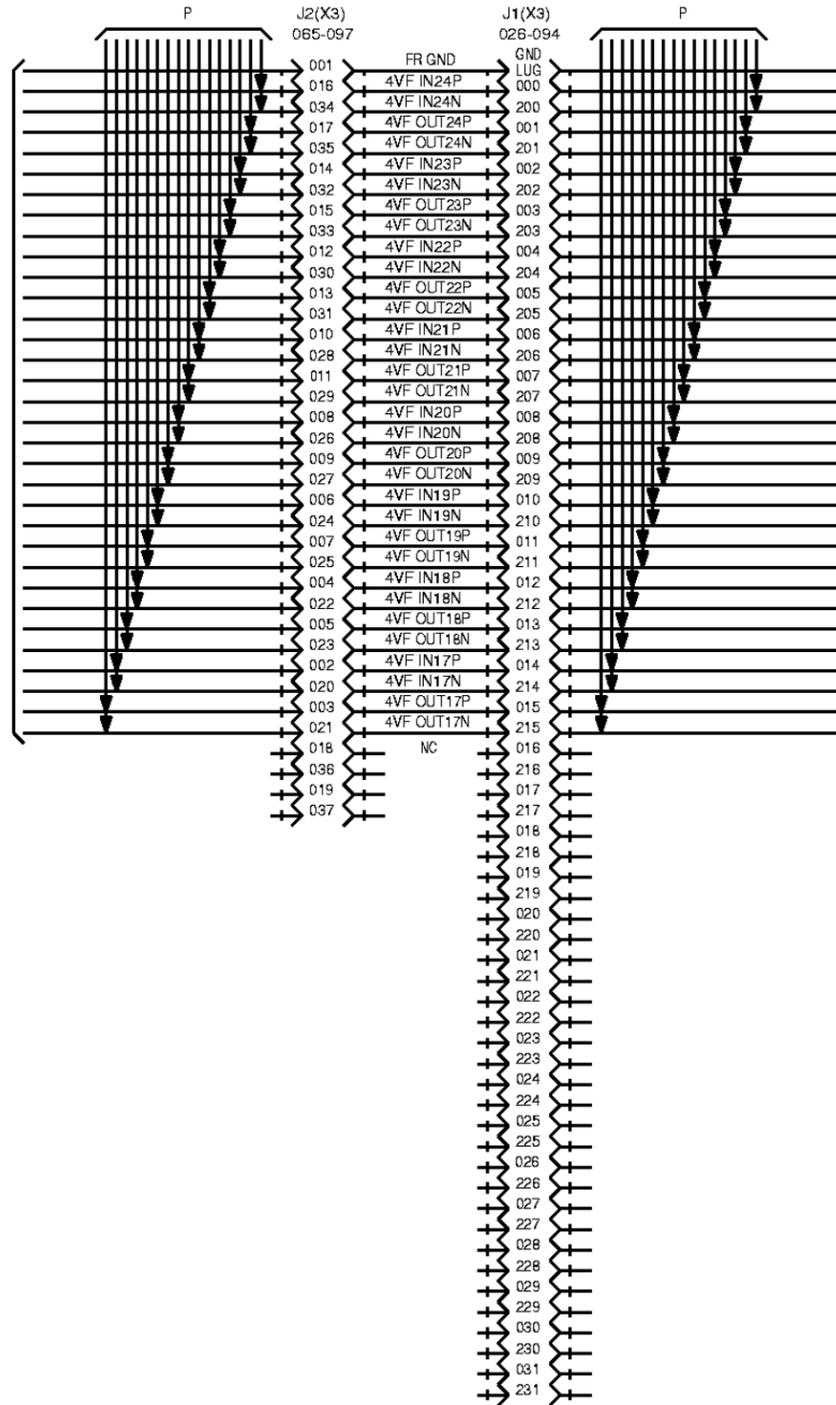


DATA
CONVERTER
UNIT

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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G3

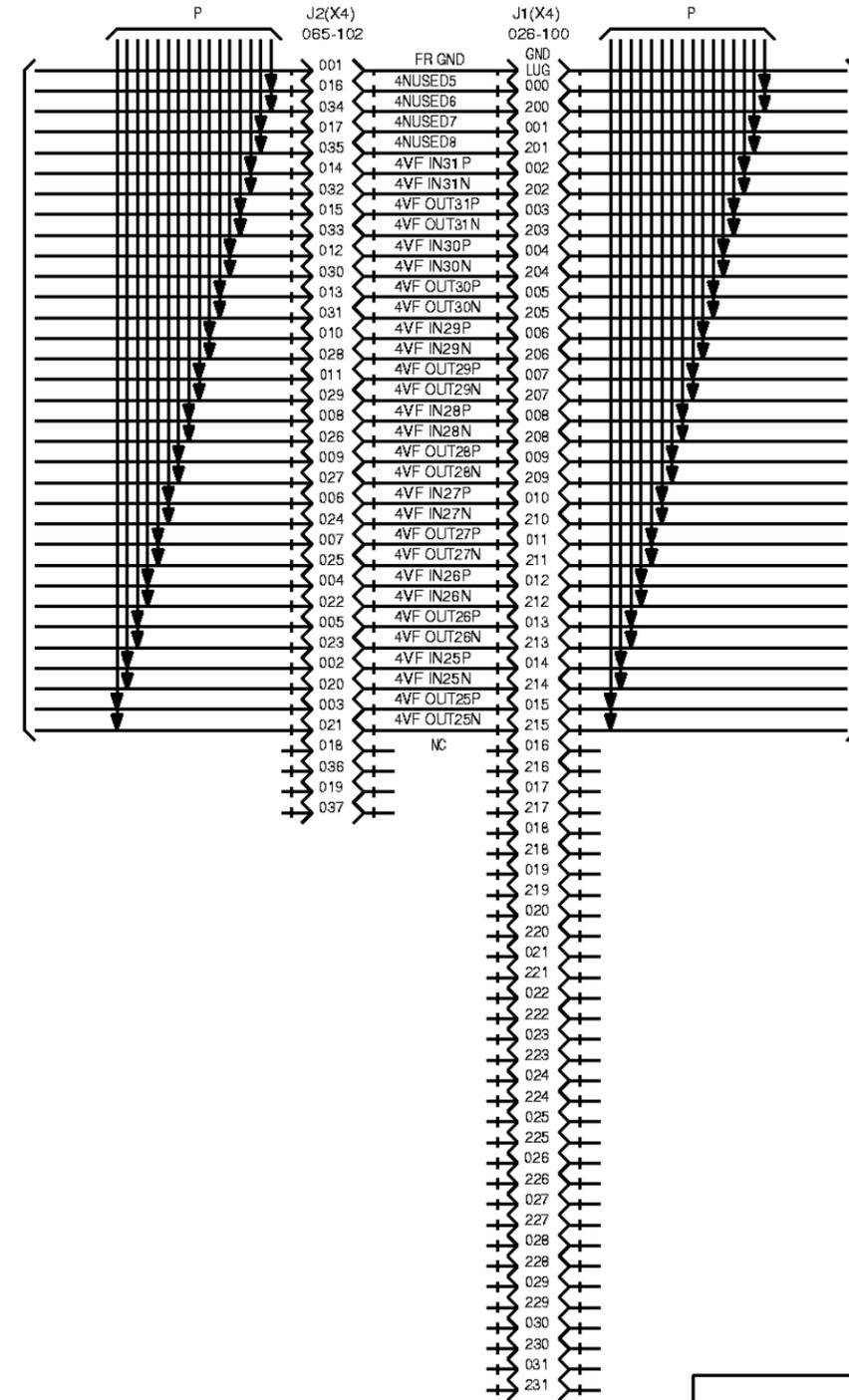
CAD 007

MULTIPLEXER #4 (64 KB) OUTPUT



CAD 008

MULTIPLEXER #4 (64 KB) OUTPUT

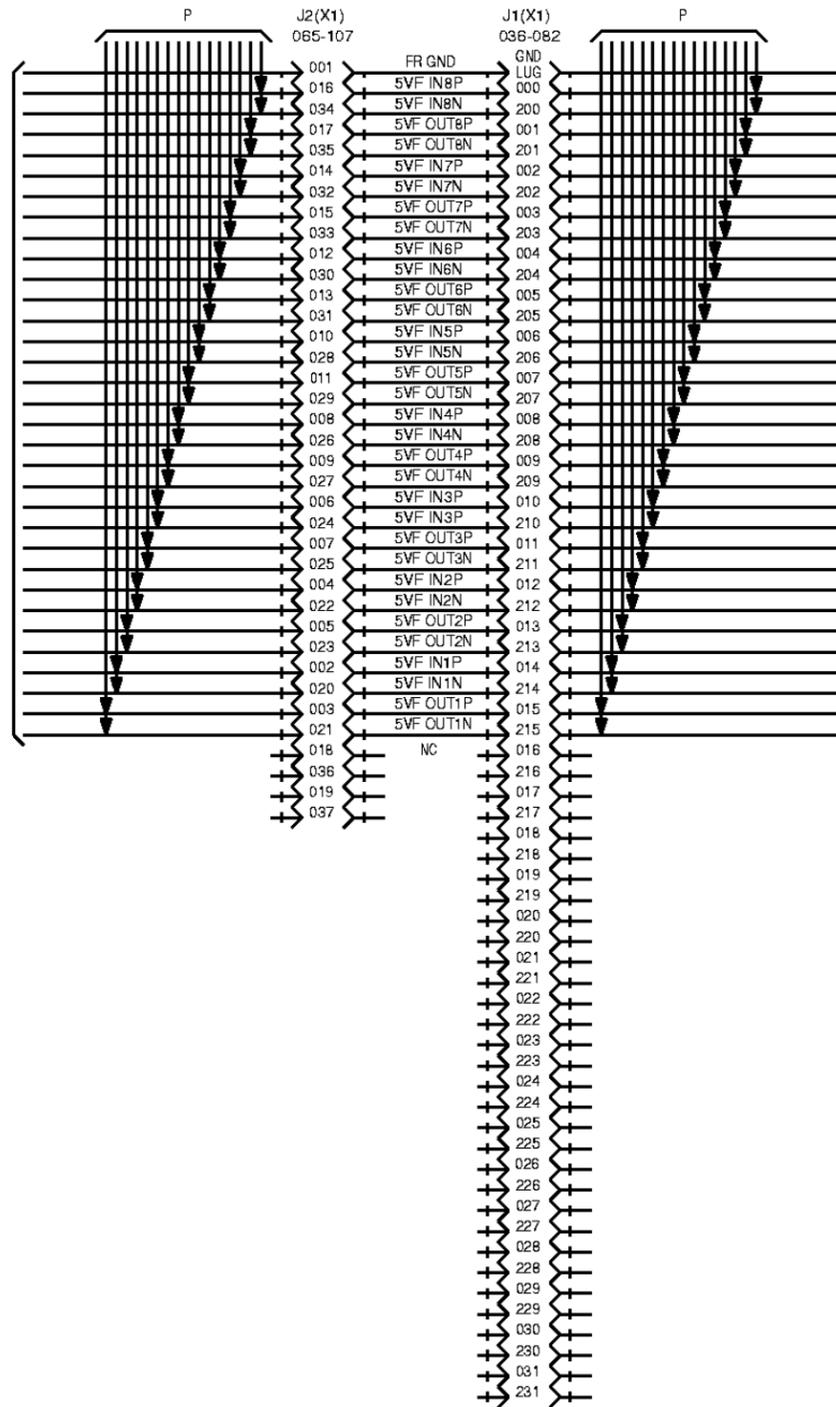


DATA
CONVERTER
UNIT

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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G4

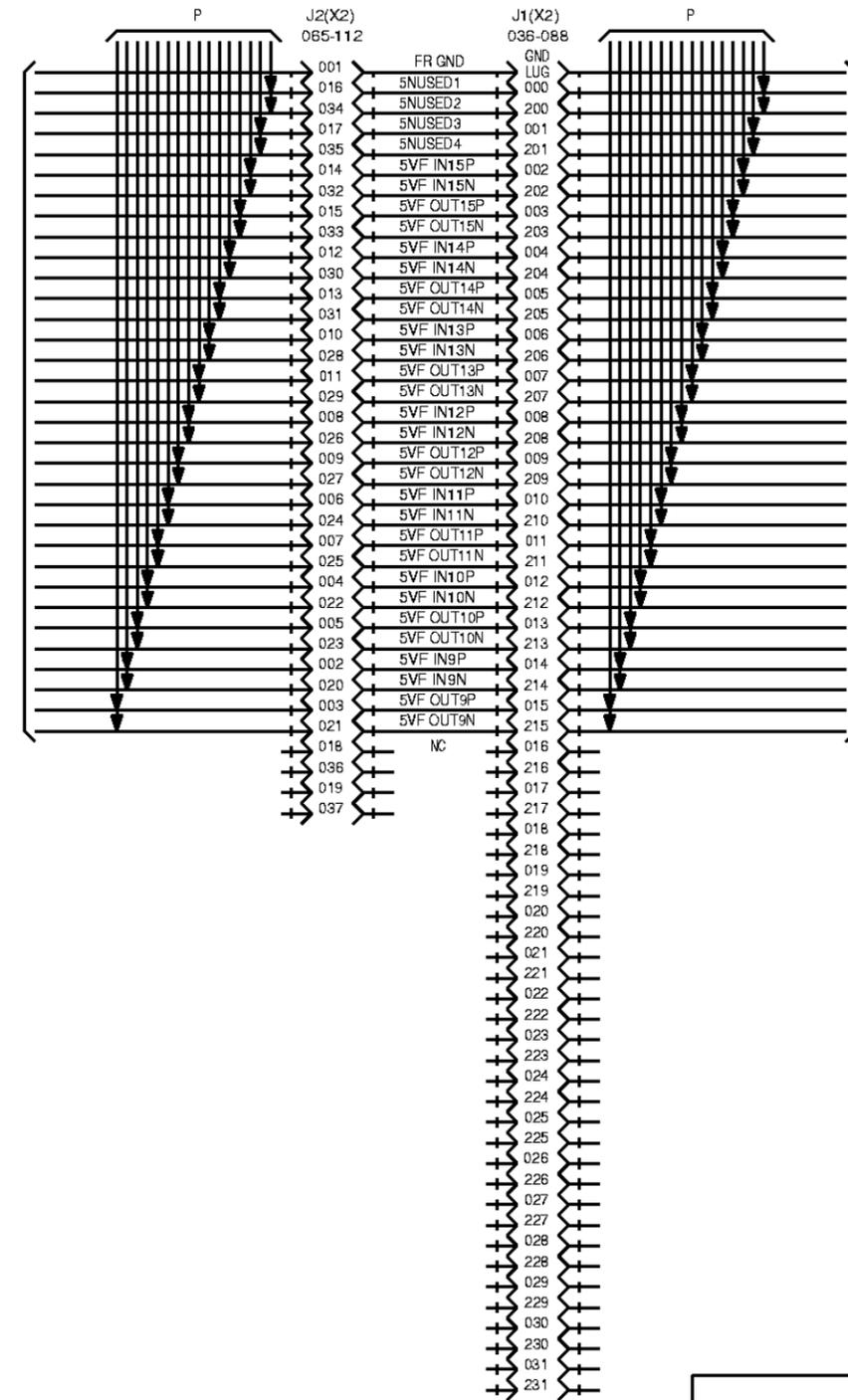
CAD 009

MULTIPLEXER #5 (64 KB) OUTPUT



CAD 010

MULTIPLEXER #5 (64 KB) OUTPUT

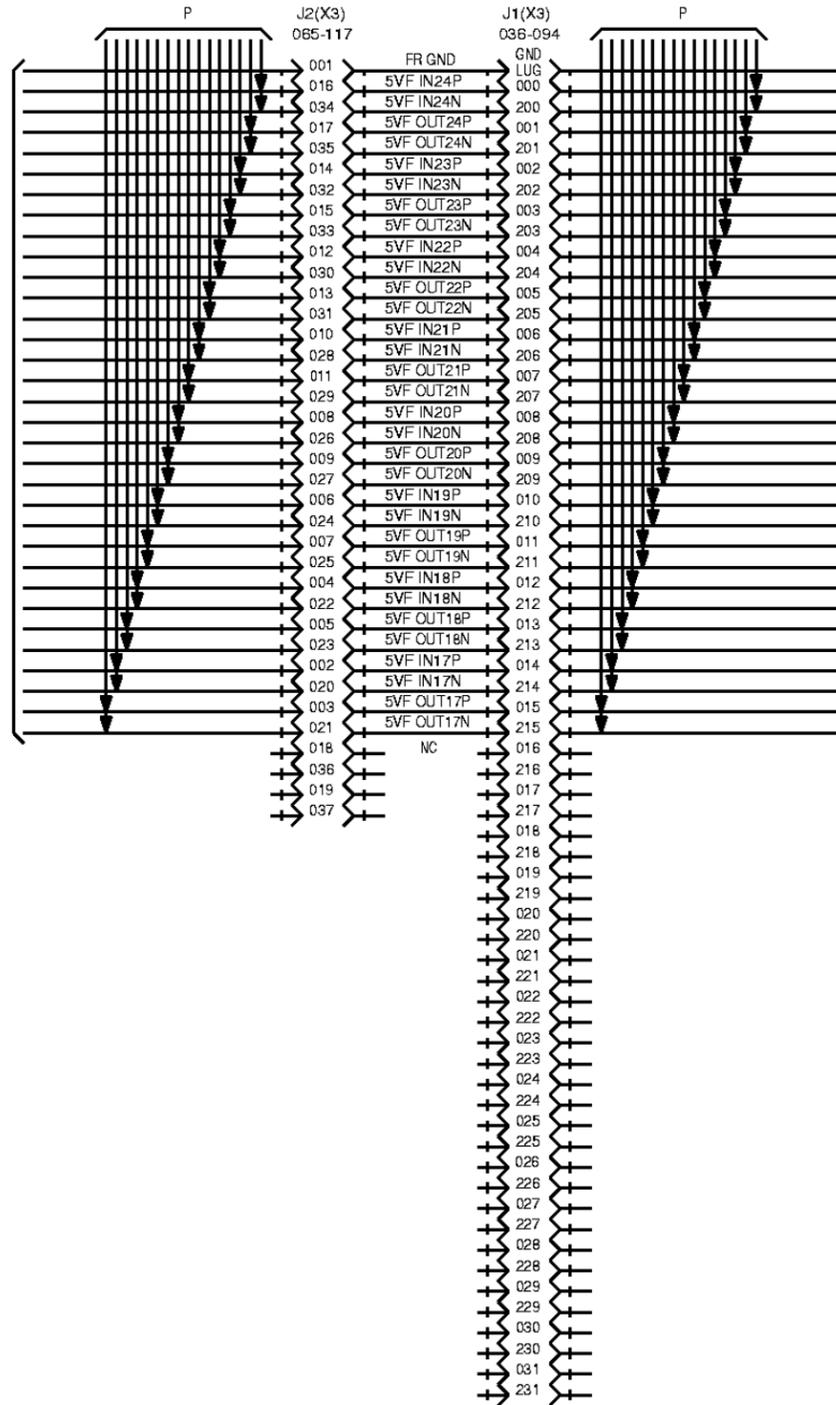


DATA
CONVERTER
UNIT

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DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G5

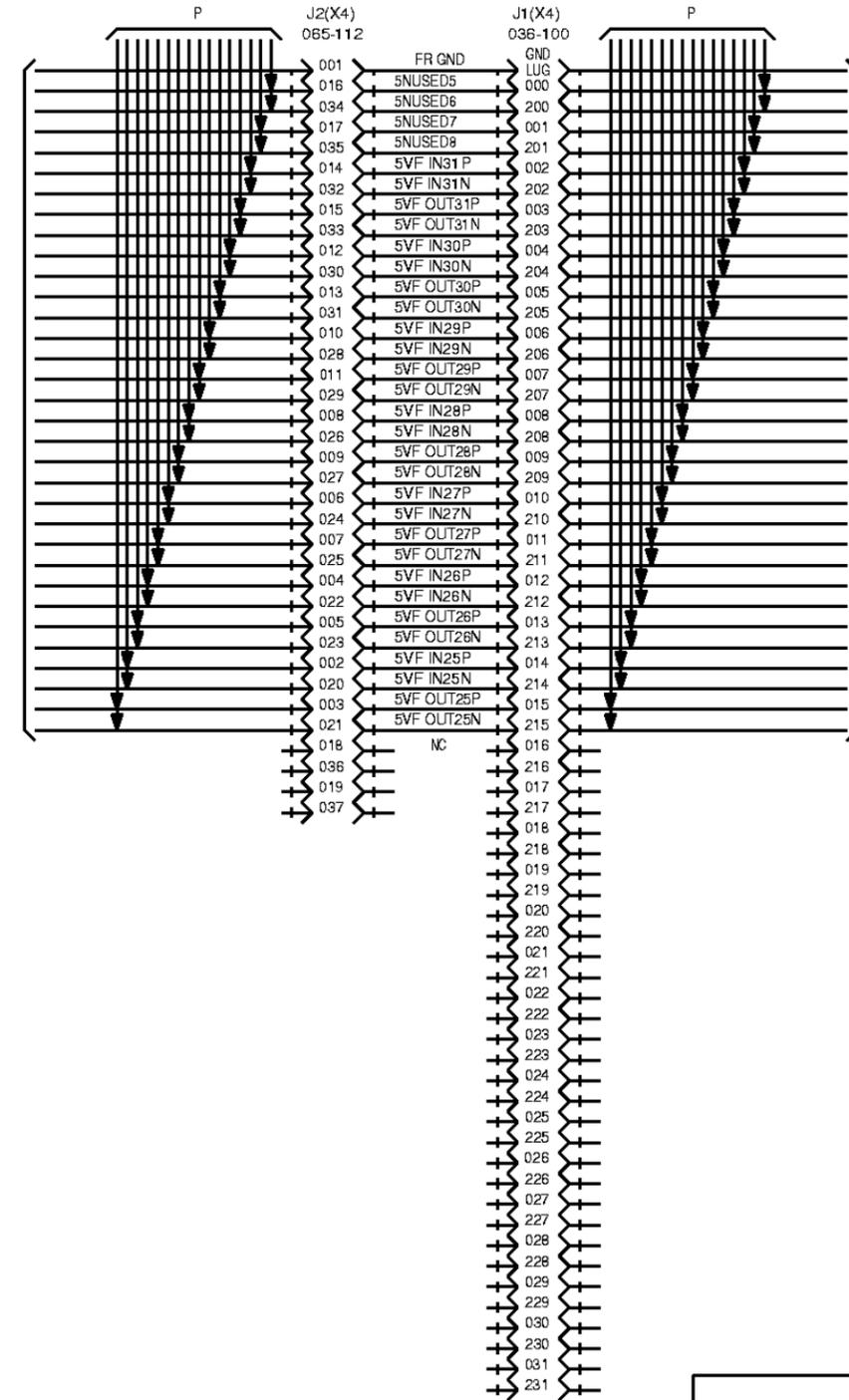
CAD 011

MULTIPLEXER #5 (64 KB) OUTPUT



CAD 012

MULTIPLEXER #5 (64 KB) OUTPUT

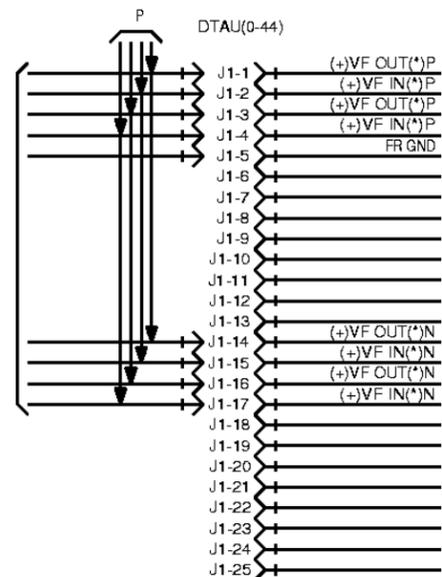


DATA
CONVERTER
UNIT

Copyright (C) 1994 AT&T All Rights Reserved		
DIGITAL TEST ACCESS CIRCUIT		
DWG SIZE C2	ISSUE 3M	
AT&T	SD-5X224-01	SHEET G6

CAD 013

DTAU (64 KB) OUTPUT



(*) - SEE TABLE A FOR LEAD DESIGNATIONS.
 (+) - SPECIFIES MULTIPLEXER NUMBERS 3, 4 OR 5.

TABLE A
LEAD DESIGNATIONS

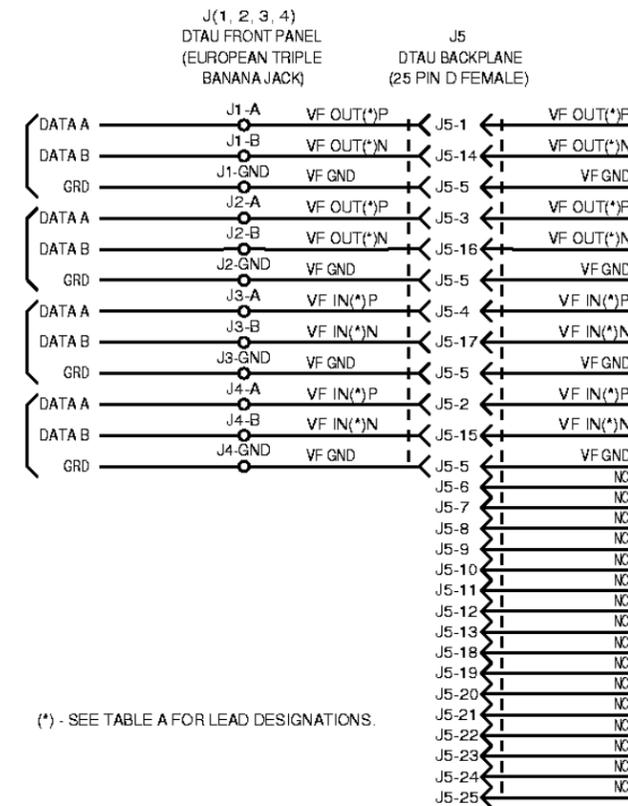
MULTIPLEXER #3																
CONNECTOR	DTAU NO.															
J1-PIN	NAME	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14
J1-1	3VF OUT(*)P	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-2	3VF IN															
J1-3	3VF OUT(*)P	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-4	3VF IN															
J1-14	3VF OUT(*)N	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-15	3VF IN															
J1-16	3VF OUT(*)N	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-17	3VF IN															

MULTIPLEXER #4																
CONNECTOR	DTAU NO.															
J1-PIN	NAME	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29
J1-1	3VF OUT(*)P	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-2	3VF IN															
J1-3	3VF OUT(*)P	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-4	3VF IN															
J1-14	3VF OUT(*)N	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-15	3VF IN															
J1-16	3VF OUT(*)N	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-17	3VF IN															

MULTIPLEXER #5																
CONNECTOR	DTAU NO.															
J1-PIN	NAME	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
J1-1	3VF OUT(*)P	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-2	3VF IN															
J1-3	3VF OUT(*)P	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-4	3VF IN															
J1-14	3VF OUT(*)N	1	3	5	7	9	11	13	15	18	20	22	24	26	28	30
J1-15	3VF IN															
J1-16	3VF OUT(*)N	2	4	6	8	10	12	14	16	19	21	23	25	27	29	31
J1-17	3VF IN															

CAD 014

DTAU INTERNAL WIRE HARNESS



(*) - SEE TABLE A FOR LEAD DESIGNATIONS.

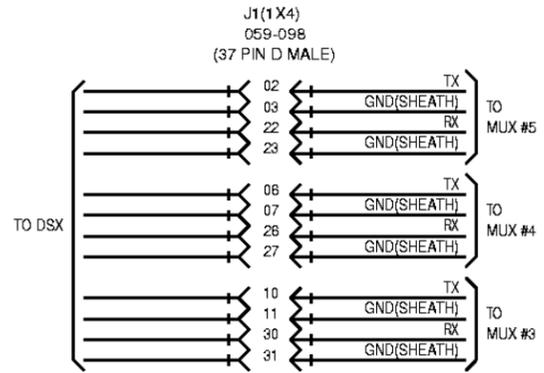
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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M
AT&T	SHEET
SD-5X224-01	G7

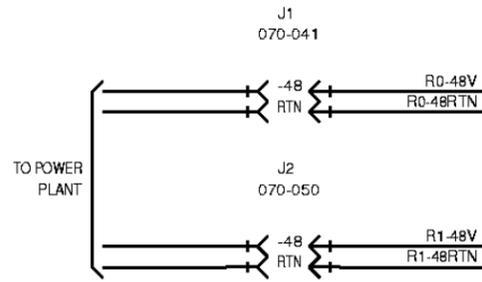
CAD 015

MULTIPLEXER (2 MB COAX) INPUT



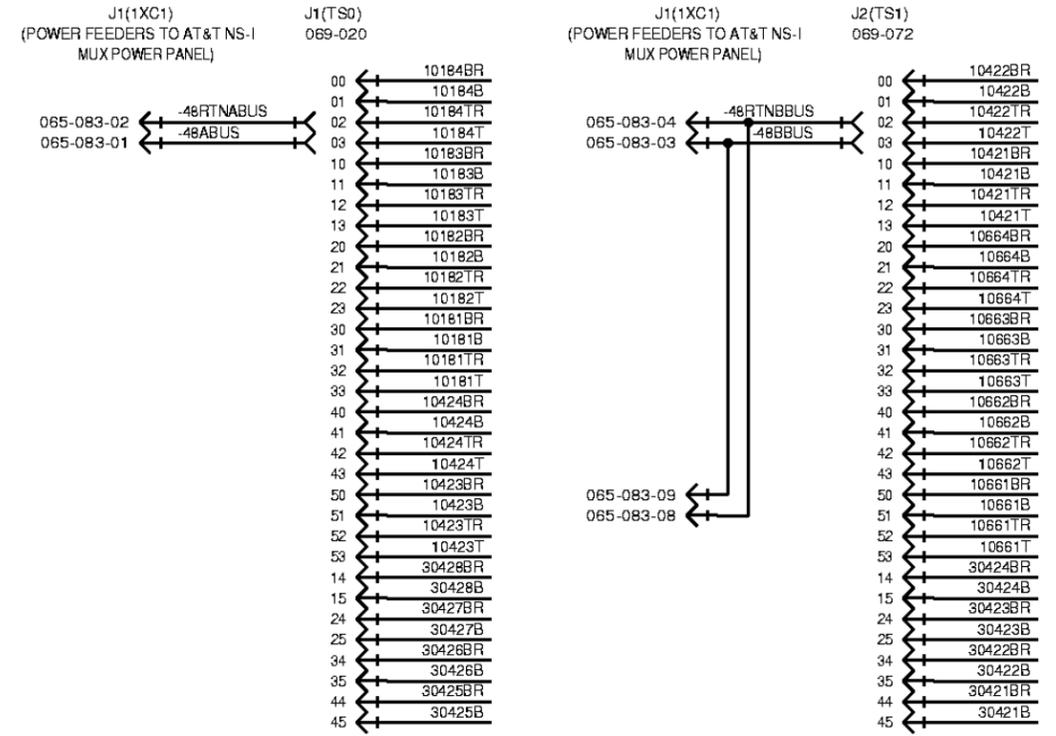
CAD 017

POWER FEEDERS TO FUSE/FILTER PANEL, J5D003AU-2



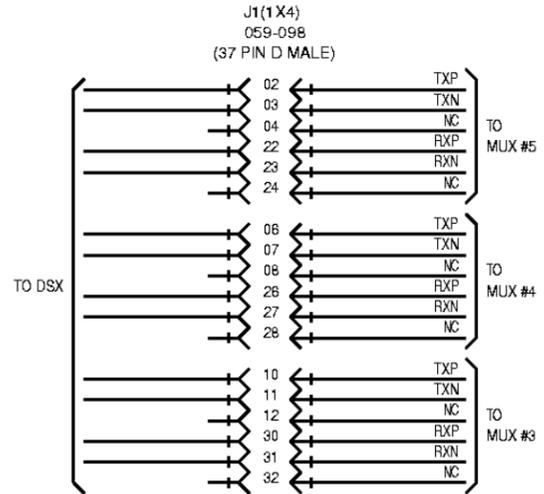
CAD 019

FUSING ON FUSE/FILTER PANEL
CIRCUIT 0 & 1, J5D003AU-2



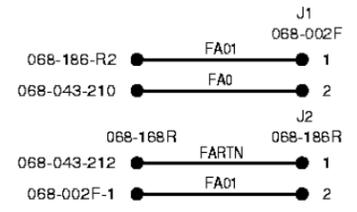
CAD 016

MULTIPLEXER (2 MB TWISTED PAIR) INPUT



CAD 018

LEDS AT TOP OF CABINET FOR
FUSE/FILTER UNIT, J5D003AU-2



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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M
AT&T	SD-5X224-01
	SHEET 68

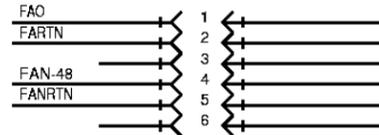
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CAD 020

LED AT OPT OF CABINET FOR
FUSE/FILTER UNIT, J5D003AU-2

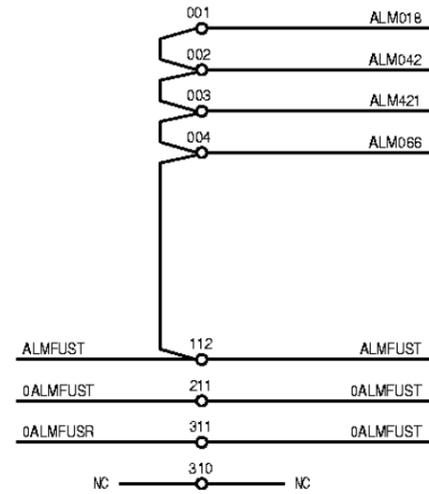
APP FIG. 1
300A CM

J1
068-190



LOOSE WIRE

068-044

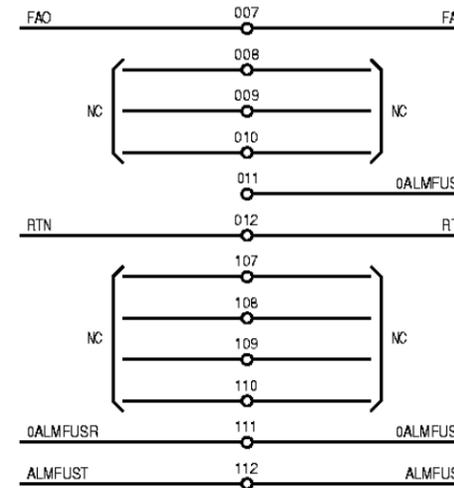


CAD 021

ALARM CIRCUIT OF FUSE/FILTER PANEL
J5D003AU-2
(SEE NTOE 319)

962SC PADDLEBOARD

(SEE NOTE 320)



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DIGITAL TEST ACCESS
CIRCUIT

DWG SIZE	ISSUE
C2	3M

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