

SHEET INDEX

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2	FS 1 JUNCTOR CIRCUIT
3	APP FIG. 1 SC 1 SEIZURE & RINGING SC 2 RING TIP & CUT THRU SC 3 DISCONNECT SC 4 DISCONNECT CKT REQ TABLE

OPTION INDEX

APP OR WIRING	LOCATION
Z	2A5, 2B4
Y	2B4
X	2C5
W	2C5
V	2B2, 2C3
T	2B2, 2C3
S	2G2
R	2G5
N	2B3, 2B4, 2D3, 2D4
H	2B3, 2B4, 2D3, 2D4

CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER	TERM. DESIG
JT	1-1/3	-48 TALK	JUNCTOR CKT	
		BATTERY SYMBOL	VOLTAGE RANGE	
		-48	45-52V	
		20~	75-110V AC, 45-52V DC	

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WIRING	QUANTITY
JUNCTOR CIRCUIT		I	1 PER CKT
3A CODE CALL CKT PROVIDED WITH 756A PBX (SEE NOTE 202)	YES	W	1 PER CKT
	NO	X	1 PER CKT

CHANGES ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	ABM	MD
4D	Y OR Z	Z		Y		Z
5B	W OR X	W	102	W&X		
6B	T OR V			T		V
7D	S	NONE		S		
8D	R	NONE		R		
10D	M OR N	N		M		N

SUPPORTING INFORMATION

CATEGORY	NO.
CABLING DIAGRAM EQUIPMENT DRAWINGS	SD-65746-01 J 58829P
OPERATION TEST BSP	551-173-501

DWG ISSUE	CD ISSUE	DATE ISSUED	APPROVED	INITIALS
1	1	4/30/58	EDG, R.P.P.	W.S.
2D	2D	5/29/57	CFL, R.A.P.	RAC
3D	2D	9-24-57	G.W.B., R.D.M.	RAC
4D	3D	4-20-59	G.W.B., M.H.A.	RAC
5B	4B	5-3-63	E.W.B., L.E.T.	
6B	4B	8-19-63	E.W.B., H.F.H.	
7D	4B	5-11-67	R.T., H.G.S.	
8D	4B	8-15-69	B.C.M., J.J.M.	
9D	4B	2-26-74	J.P.L., L.D.J.	
10D	5D	1-27-75	G.D.T., W.V.S.	

EQUIPMENT NOTES:

- 201. CAPACITORS DESIGNATED (T) AND (R) SHALL MATCH WITHIN 0.11UF.
- 202. WHEN 3A CODE CALL IS SPECIFIED, "X" WIRING SHALL BE DISCONNECTED AT THE (D) RELAY BY THE INSTALLER.

LEAD INDEX

DESIG	LOCATION	
	FS	CAD
LINE LINK & MARKER		
JT-BAT	2G0, G9	
OHM-	2G1	
RO	2D2	
RT	2D7	
SO	2E2	
ST	2E7	
THM-	2G7	
TO	2B2	
TT	2B7	

POWER SUPPLY

DESIG	LOCATION
MS	2A5
RI	2A6
RING G	2A6

MAKE BUSY & BUSY DISPLAY CIRCUIT

DESIG	LOCATION
OHM-	2F5

TRAFFIC MEASUREMENT SYSTEM IA REMOTE SCANNER

DESIG	LOCATION
TU(J-)	2H2

TRANSMISSION TEST REQUIREMENTS
(1000 CYCLE LOSS BETWEEN 600Ω LINES)

MAX. ALLOWABLE CIRCUIT LOSS (db)
0.4

ALLOWABLE INDIVIDUAL APPARATUS LOSSES (db)					
APPARATUS	DESIG	CODE	MAX. LOSS	MIN. LOSS	REMARKS
CAPACITORS	T, R	2UF	13.7	11.7	SEE NOTE 1
RELAYS	A, D		0.1		

NOTES:
1. THE INDIVIDUAL LOSSES FOR THE (T) AND (R) CAPACITORS SHALL BE WITHIN 0.5 db OF EACH OTHER.

WORKING LIMITS
MAX. EXT CKT LOOP RES - 1410
MIN. INS RES - 20,000

ISSUE
10D

SD-65750-01 3J07

PBX SYSTEMS
NO. 756A
JUNCTOR CIRCUIT

MFR DISC.

SD-65750-01-1
3 SHEETS

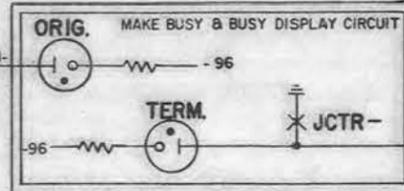
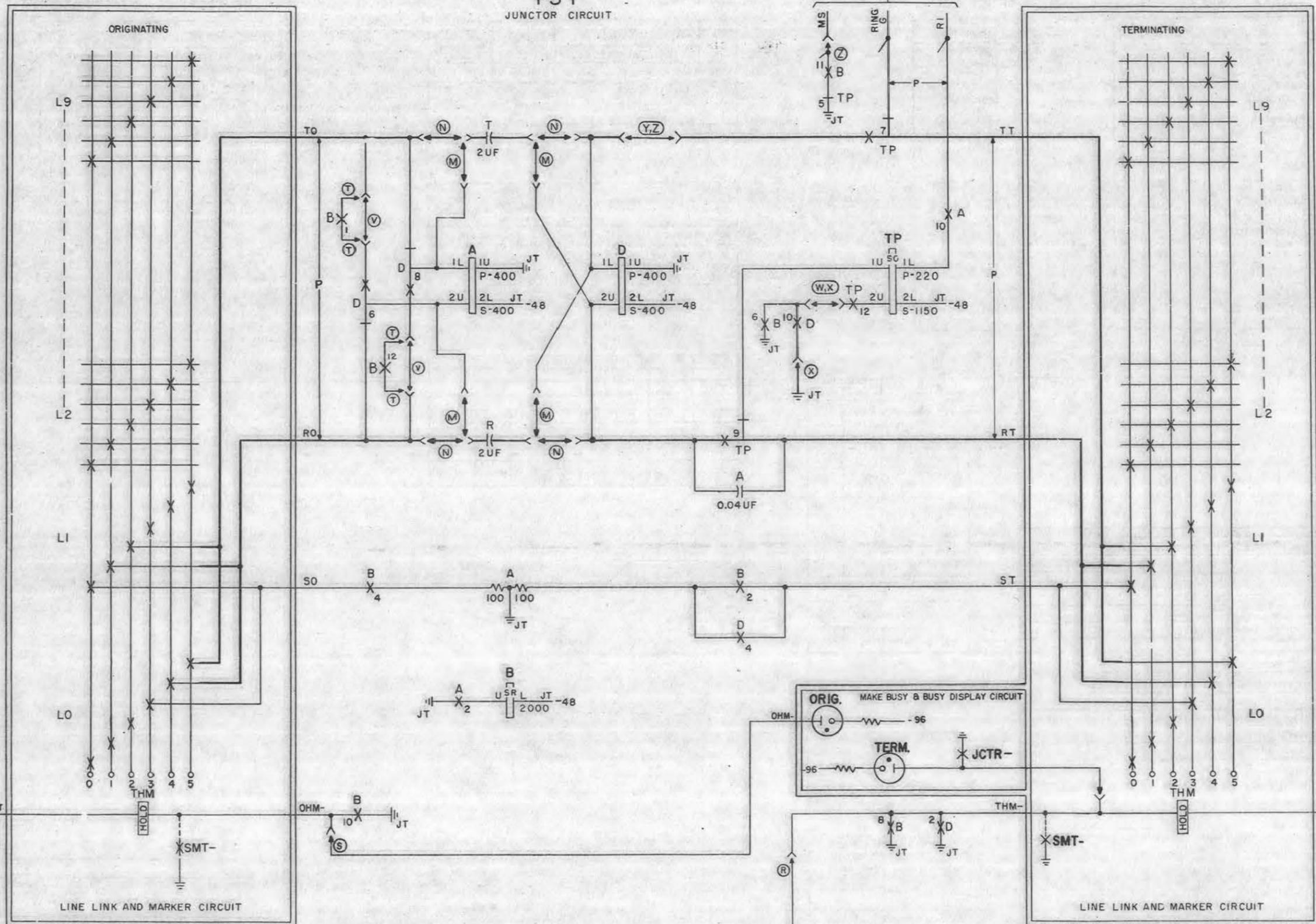
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INCORPORATED

65

0 1 2 3 4 5 6 7 8 9

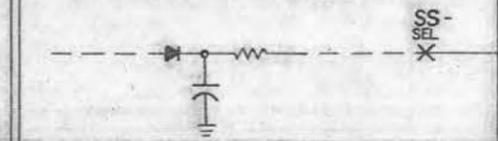
FS I JUNCTOR CIRCUIT

TO POWER SUPPLY CIRCUIT



LINE LINK AND MARKER CIRCUIT

TRAFFIC MEASUREMENT SYSTEM 1A REMOTE SCANNER



LINE LINK AND MARKER CIRCUIT

BELL TELEPHONE LABORATORIES
INCORPORATED

ISSUE 10D

JUNCTOR CIRCUIT SD-65750-01-2

65 PRINTED IN U.S.A.

SD-65750-01-2

ISSUE	DATE	BY	REASON
1	10/24/65	WVH	REVISED
2	11/10/65	WVH	REVISED
3	11/10/65	WVH	REVISED
4	11/10/65	WVH	REVISED
5	11/10/65	WVH	REVISED
6	11/10/65	WVH	REVISED
7	11/10/65	WVH	REVISED
8	11/10/65	WVH	REVISED
9	11/10/65	WVH	REVISED
10	11/10/65	WVH	REVISED
11	11/10/65	WVH	REVISED
12	11/10/65	WVH	REVISED
13	11/10/65	WVH	REVISED
14	11/10/65	WVH	REVISED
15	11/10/65	WVH	REVISED
16	11/10/65	WVH	REVISED
17	11/10/65	WVH	REVISED
18	11/10/65	WVH	REVISED
19	11/10/65	WVH	REVISED
20	11/10/65	WVH	REVISED

DR H

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CIRCUIT REQUIREMENTS
756A PBX JUNCTION CIRCUIT (JTR)

APPARATUS				MECH. REST.				CIRCUIT PREPARATION				DIRECT CURRENT FLOW REST.				REMARKS	
DESIG	CODE	OPTION	FIG.	WSP FIG.	CONT PRESS.	ARM TRVL.	BLOCK OR INSULATE	TEST CLIP DATA		TEST SET PREP.	SEE TEST NOTE	TEST WDG.	TEST FOR	AFTER SOAK	TEST		READ
				CFWP BAT.	CONW CRD.			TEST WDG.	TEST FOR	MA.	MA.	MA.	MA.	MA.	MA.		MA.
RELAYS																	
A	AJ52		I	303				1L(A)	2U(A)	M		P/S	O	13.3	12.6		
								1L(A)	2U(A)	M		P/S	R	3.1	3.3		
B	AG'B		I	488				1U(B)	GRD			P	O	20	7.6		
								1U(B)	GRD			P	H	20	1.4		
								1U(B)	GRD			P	R	20	0.7	0.9	
D	AJ52		I	303				1L(D)	2U(D)	M		P/S	O	13.3	12.6		
								1L(D)	2U(D)	M		P/S	R	3.1	3.3		
TP	AJ49		I	302				1L(TP)	1U(TP)	B/G		P	O	28	26.5		
								1L(TP)	1U(TP)	B/G		P	NO	20	21.5		
								2U(TP)	GRD			S	O	19			

DRAWING ISSUE
1 1/2
2D RAC
3D RAC
4D RAC
5B RAC
6B RAC
7D RAC
8D RAC

PAGES: PAGE

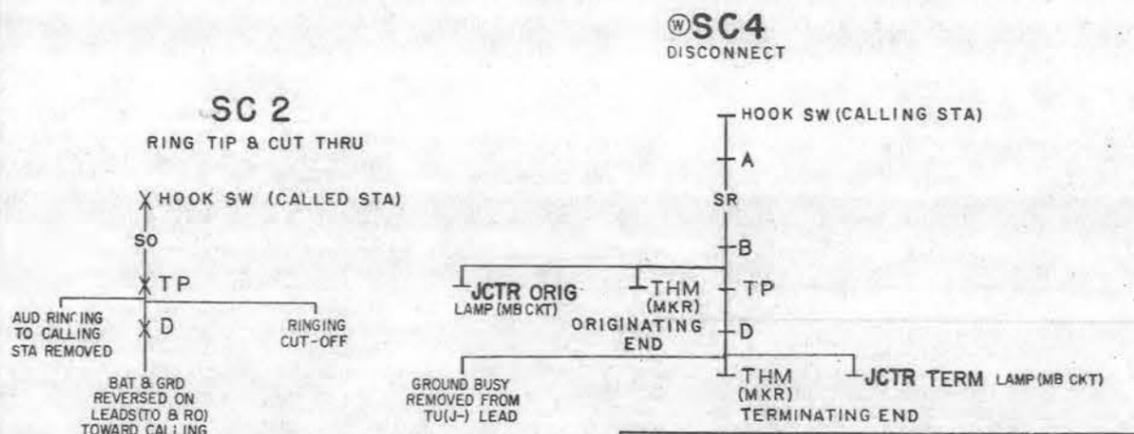
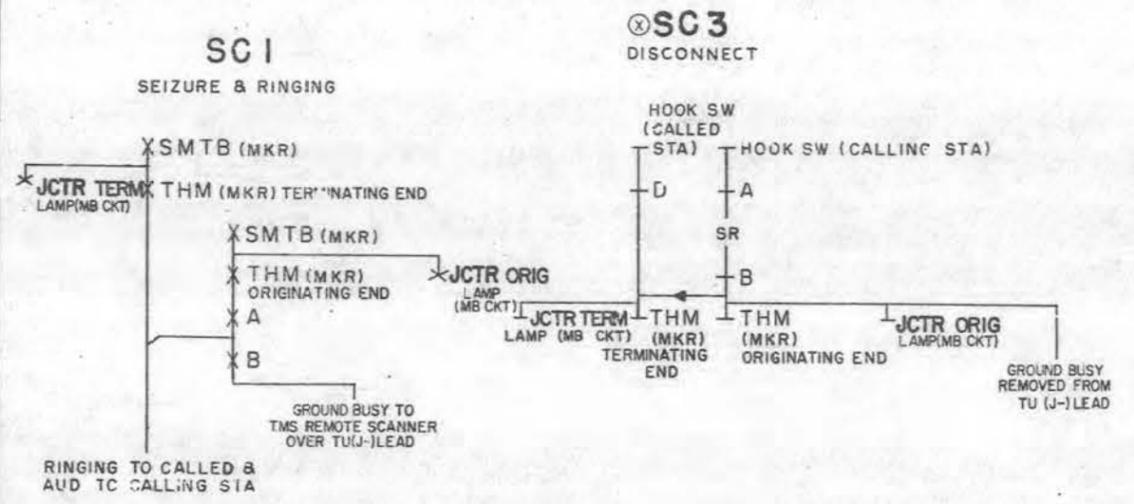
JUNCTION CIRCUIT
SD-65750-01-3
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SD-65750-01-3

APP FIG. 1

DESIG	A	B	D	TP	DESIG
CODE	AJ52	AG18	AJ52	AJ49	CODE
OPTION					OPTION
CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC
12		M	2C3	EM	2C6
11		M	2A5		
10	M	2B6	M	2C5	
9				BM	2D5
8	EMB	M	2C6	EMB	2C3
7		B		BM	2B6
6	EMB	M	2C5	EMB	2C2
5				BM	2A5
4	EBM	M	2E2	EBM	2E5
3					
2	M	2F3	M	2E5	M
1				M	2B2
COIL	2C3	2F3	2C4	2C6	COIL

CAPACITOR			RESISTOR		
DESIG	LOC	CODE	DESIG	LOC	CODE
A	2D5	441U	A	2E3	19FG
R	2D3	437E			
T	2B3				



JUNCTION CIRCUIT
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DRAWING ISSUE
1 1/2
2D RAC
3D RAC
4D RAC
5B RAC
6B RAC
7D RAC
8D RAC

ISSUE
100