SHEET INDEX

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CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER
	1/2	-48V	SUPPLY WHEN USED ALONE
RB2	1-1/3	-48V	SUPPLY WHEN USED WITH POWER SUPPLY CKT IN 756A PBX

BATTERY SYMBOL	VOLTAGE RANGE
-48	45-52V

102.

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY

103.

NETWORK VALUES		
NETWORK NO.	RESISTANCE IN OHMS	CAPACITANCE IN UF

104. RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES

CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT			
				STD	A&M	MD	
5D	Y	Z		Y		Z	

105. DISCONNECT BATTERY SUPPLY BEFORE REMOVING OR INSERTING NETWORKS (PRINTED WIRE BOARDS.)

EQUIPMENT NOTES:

201. LEADS NOT OTHERWISE SPECIFIED SHALL BE 22GA KS-13385, SOLID.

202. DESIGNATIONS IN [ ] ARE FOR INFORMATION ONLY.

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIP DESIGN REQ	J86464 AA367.117
EQUIP DWG	J86464G

OPTION INDEX

APP OR WIRING	LOCATION
Z	APP FIG.1
Y	APP FIG.1

DWG ISSUE	CD ISSUE	DATE ISSUED	DRAWN	APP'D
1	1	10-9-56	J.N. W.F.S	J.W.O. J.M.D.
2D	2D	1-31-57	R.G. J.J.T.S	J.W.O. J.M.D.
3D	3D	12-19-57	J.J.T. M.S.	J.W.O. J.M.D.
4A	4A	5-19-58	H.C. W.M.	J.W.O. J.M.D.
5D	5D	9-30-59	W.F.S. E.H.E.	J.W.O. J.M.D.

T.E.B. T.E.B. W.F.O P.O.B. P.O.B.

SD-81328-01

SD-81328-01

SD-81328-01

POWER SYSTEMS
SIGNALING CIRCUIT
TRANSISTOR LOW TONE SUPPLY
756A PBX
J86464G

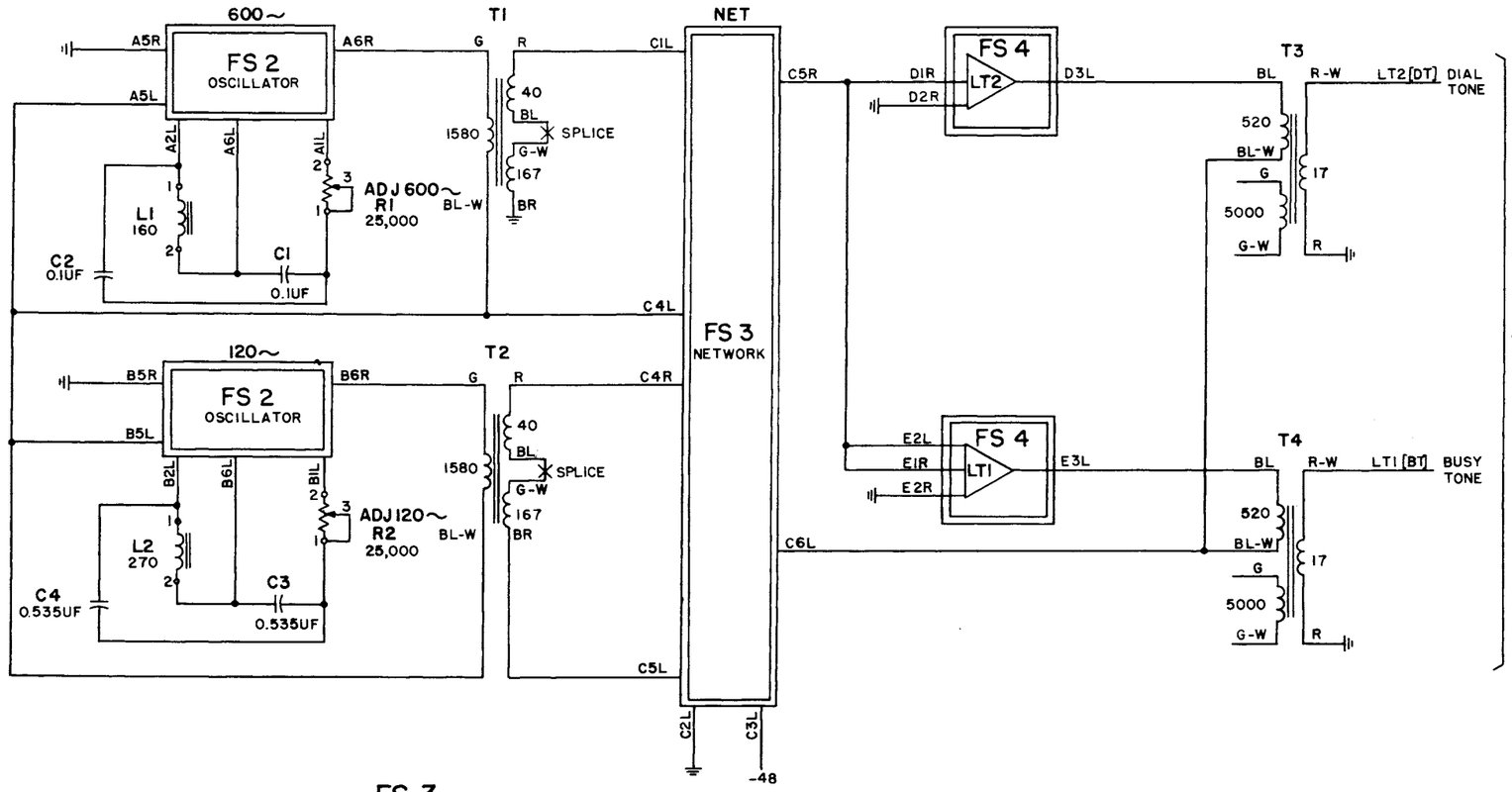
(L T)

AT&TCO STANDARD 5

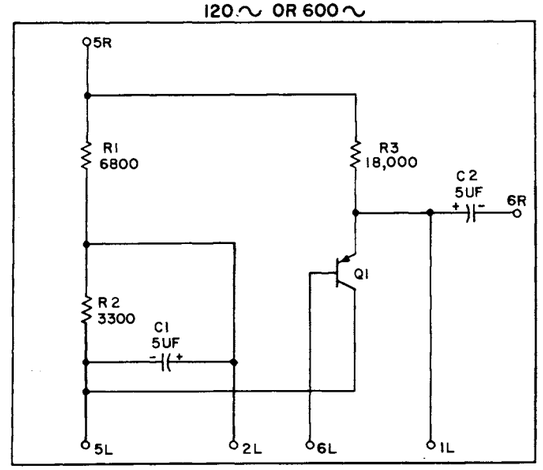
SD-81328-01-1
3 SHEETS

BELL TELEPHONE LABORATORIES INCORPORATED — CODE IDENT NO. 6S — PRINTED IN U.S.A.

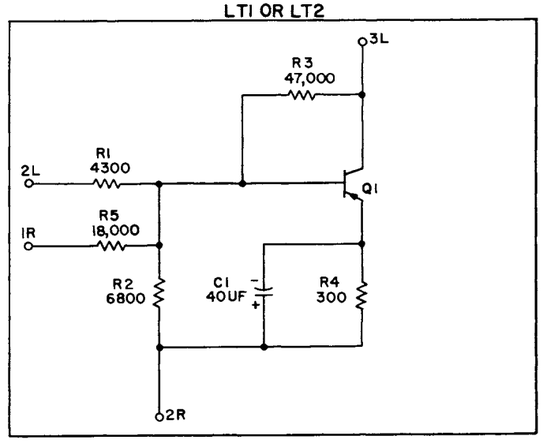
FS 1
LOW TONE SUPPLY



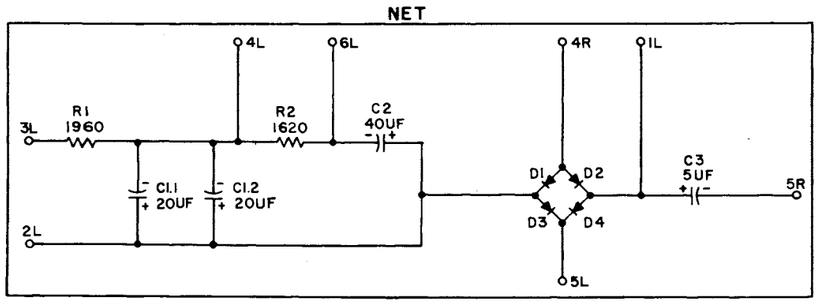
FS 2
OSCILLATOR



FS 4
AMPLIFIER



FS 3
NETWORK



SHEET NOTES:

1. PREFIX LETTER DESIGNATIONS (A,B,C,ETC.) ON LEADS TO NETWORKS (PRINTED WIRE BOARDS) REFER TO DESIGNATIONS OF CONNECTORS.

DRAWING ISSUE		
1	J.N. WFS	TEB
2D	R.G. J.T.S.	TEB
3D	J.L.T. M.S.	W.F.A.
4A	H.C. W.F.	P.O.B. A
5D	L.M.C. W.F.	P.O.B.

SIGNALING CIRCUIT

SD-81328-01-2 5

BELL TELEPHONE LABORATORIES INCORPORATED — Dwg Size 6S — PRINTED IN U.S.A.

APP FIG. 1

AMPLIFIER

DESIG	LOC	CODE
LT1	2D4	820A NETWORK
LT2	2A4	820A NETWORK

CAPACITOR

DESIG	LOC	CODE
C1	2B1	441QU
C2	2B0	441QU
C3	2D1	441QG
C4	2D0	441QG

INDUCTOR

DESIG	LOC	CODE
L1	2B0	293AA
L2	2D0	231R

NETWORK

DESIG	LOC	CODE
NET	2C3	819A NETWORK

OSCILLATOR

DESIG	LOC	CODE
120~	2C0	818A NETWORK
600~	2A0	818A NETWORK

POTENTIOMETER

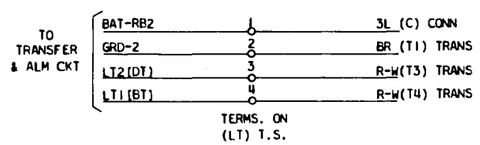
DESIG	LOC	CODE
R1	2B1	KS-13790,L1 25,000
R2	2D1	KS-13790,L1 25,000

TRANSFORMER

DESIG	LOC	CODE
T1	2B2	(Z) 530A OUTPUT, (Y) 530C OUTPUT
T2	2C2	(Z) 530A OUTPUT, (Y) 530C OUTPUT
T3	2B5	667B INPUT
T4	2D5	667B INPUT

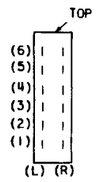
CAD.1

(FOR APP FIG. 1)



SHEET NOTES:

1. THE FIG. BELOW SHOWS TERMINAL LOCATION ON CONNECTOR ASSEMBLIES.



DRAWING ISSUE

1	J.N. WFS	TEB
2D	R.G. JJS	TEB
3D	J.B.F. M.A.S.	W.F.D.
4A	N.C. M.M.	P.O.B.
5D	J.N.F. M.A.S.	POB.

SIGNALING CIRCUIT		SD-81328-01-3
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S	5