SD-95283-01

SHEET INDEX

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SUPPORTING INFORMATION

CATEGORY	NO.

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

OPTION INDEX

APP OR ORG	LOCATION	APP OR ORG	LOCATION
Z	1G6	G	2G4
Y	1G6	F	1B4,1D1,2G1,2G4
X	2A9	E	2F6
W	2A9	D	2F6
V	1B0,1C0		
S	2G4	C	2G8
R	1D0	B	2G8
P	2G4		
N	1A1		
M	1F2,SHEET B2		
L	2F5,2F8		
K	1F1		
J	1F1		
H	2G4,2H6		

DWG ISSUE	CD ISSUE	DWG ISS	CD ISSUE	DWG ISS	CD ISSUE
1	1	2D	1 APP 1D	3D	1 APP 3D

DWG ISSUE	CD ISSUE	DATE ISSUED	INITIALS	APPR
4B	2B	5-15-64	APPR WER	MCH FFP DLH
5B	2B APP 1B	10-26-65	GDP -	MCH FFP DLH
6A	2B APP 2A	6-22-67	WF FJS	SJH FEM FC
7B	2B APP 3B	9-10-68	CJF JGT	NTL FEM FC
8B	2B APP 4B	10-27-69	G.R. AML	JTD AJS FC
9B	2B APP 5B	3-25-71	AT FC	EAH AJS FC

REPLACES SD-95268-01

9

SD-95283-01 1NO1

COMMON SYSTEMS

ANNOUNCEMENT CIRCUIT
EMPLOYING KS-16765 LIST 2 ANNOUNCEMENT SET

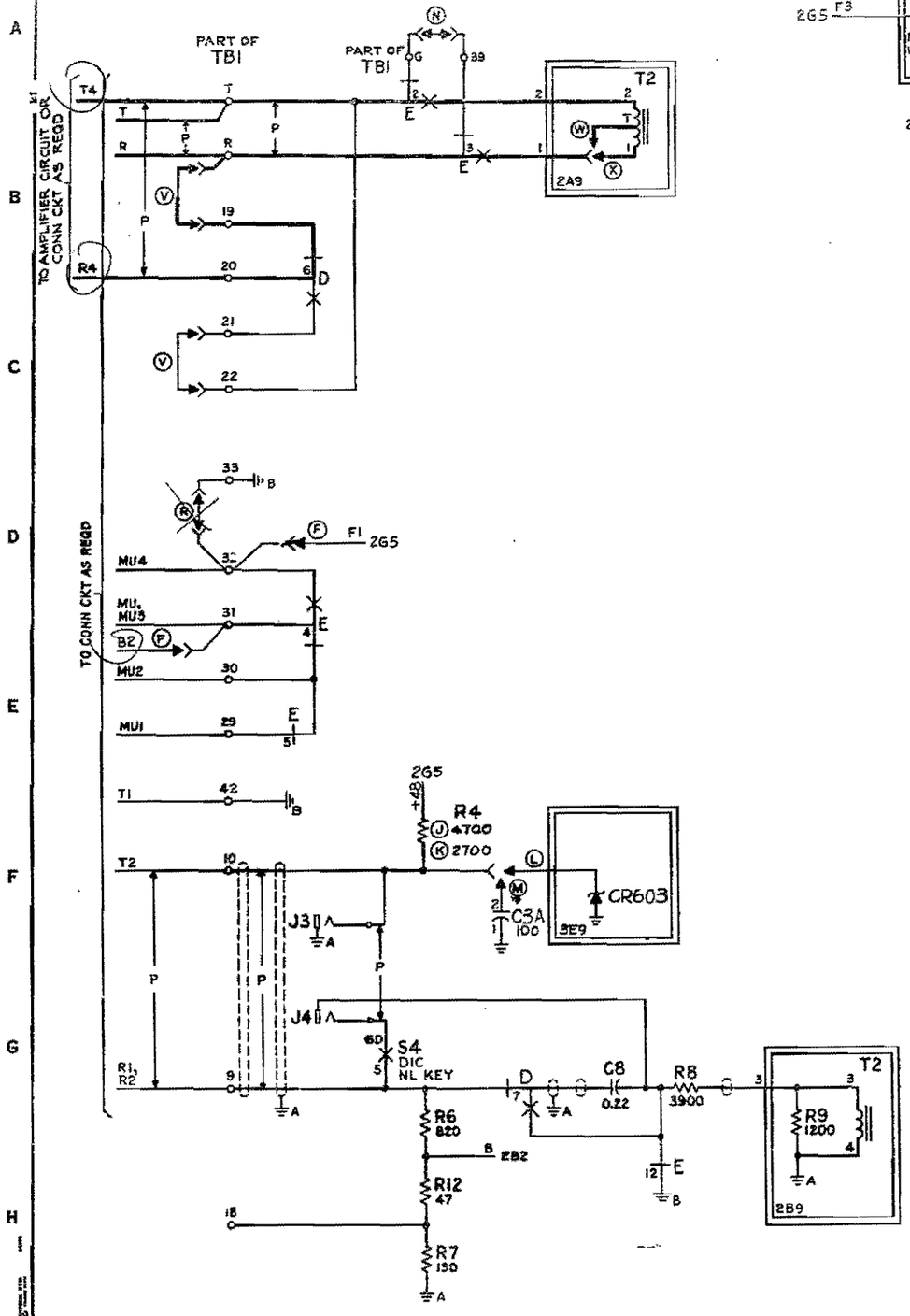
AT&TCO STANDARD

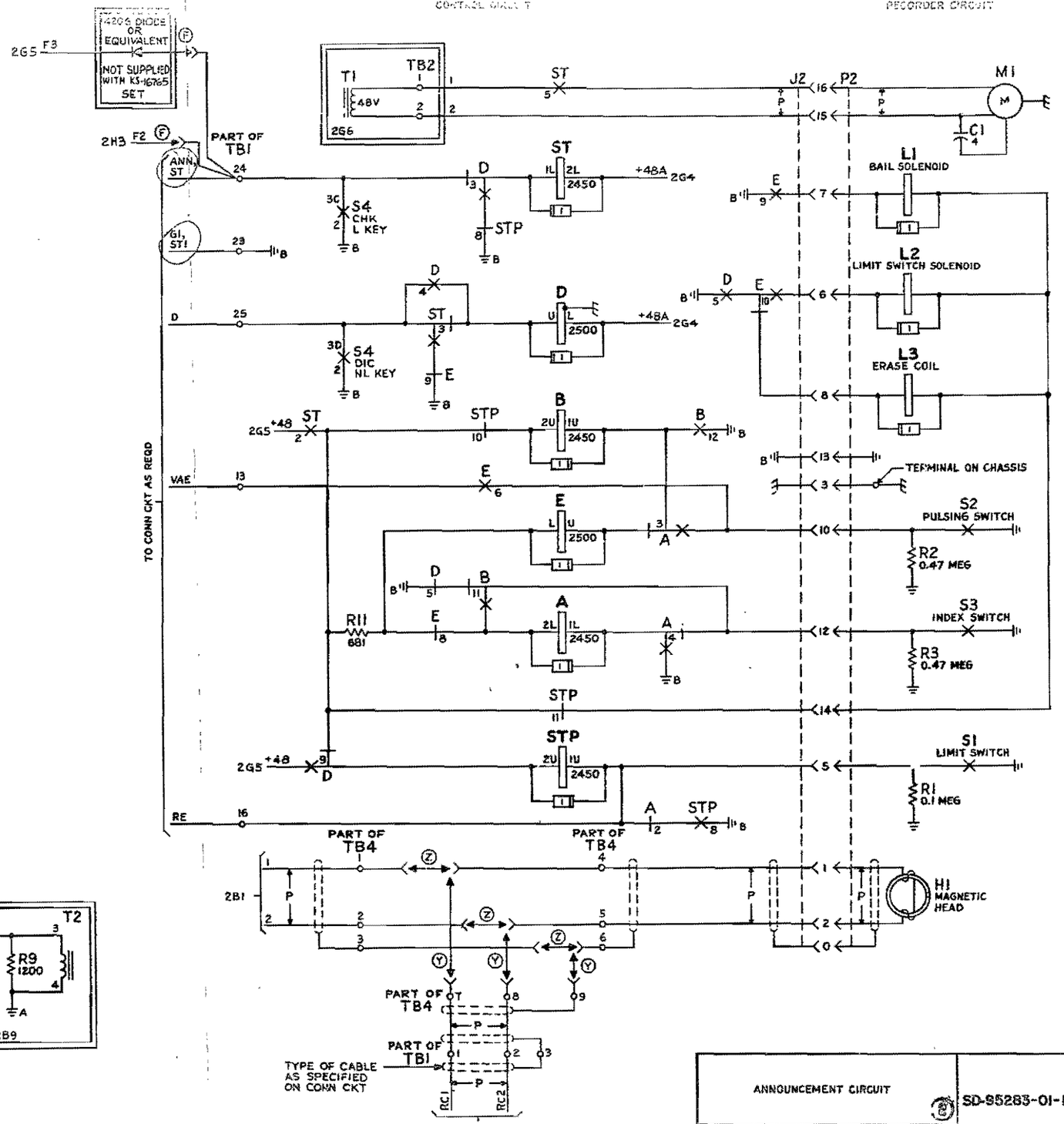
SD-95283-01-A1
11 SHEETS

BELL TELEPHONE LABORATORIES INCORPORATED

SIZE CODE 65

SD-95283-01-A1



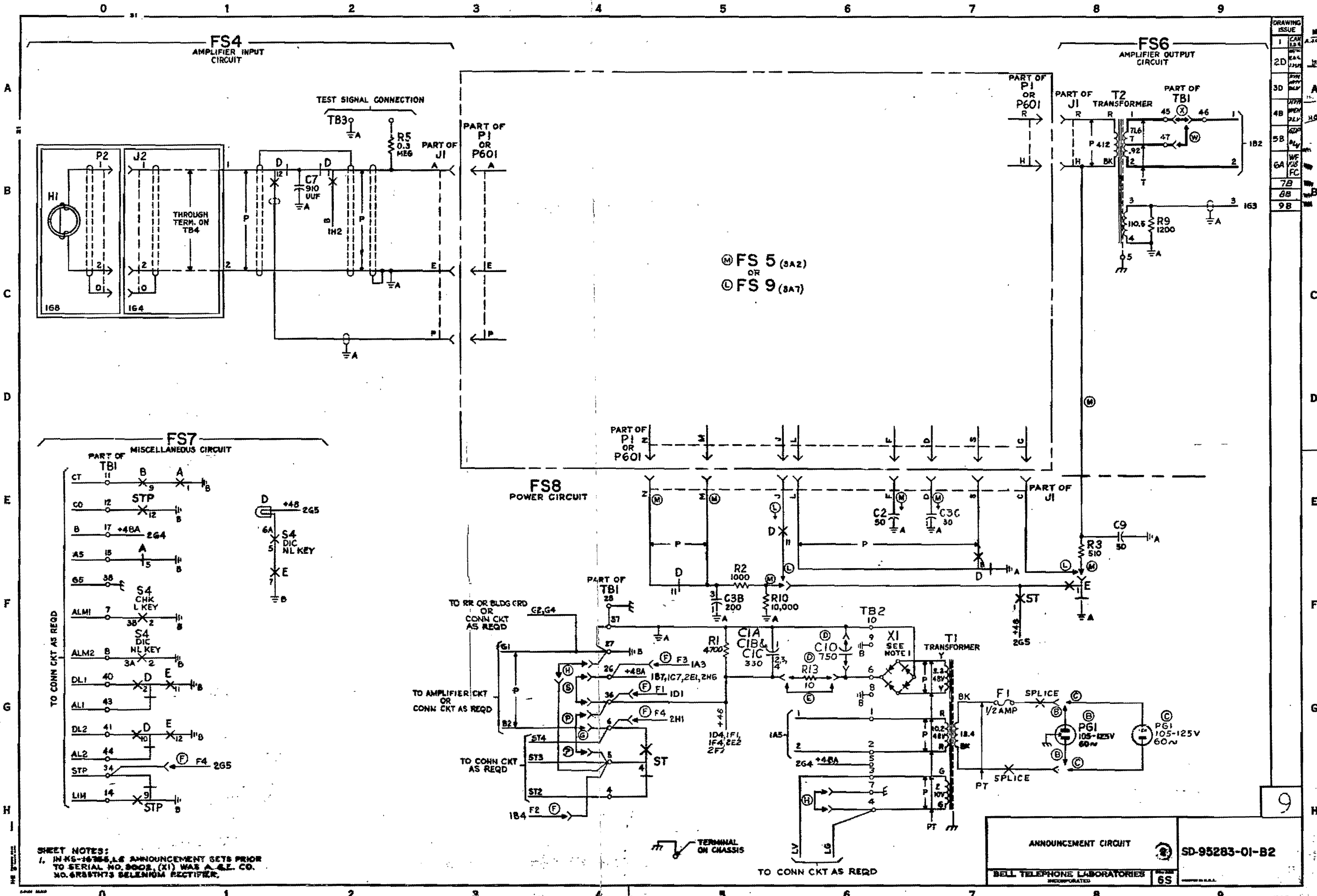


ANNOUNCEMENT CIRCUIT

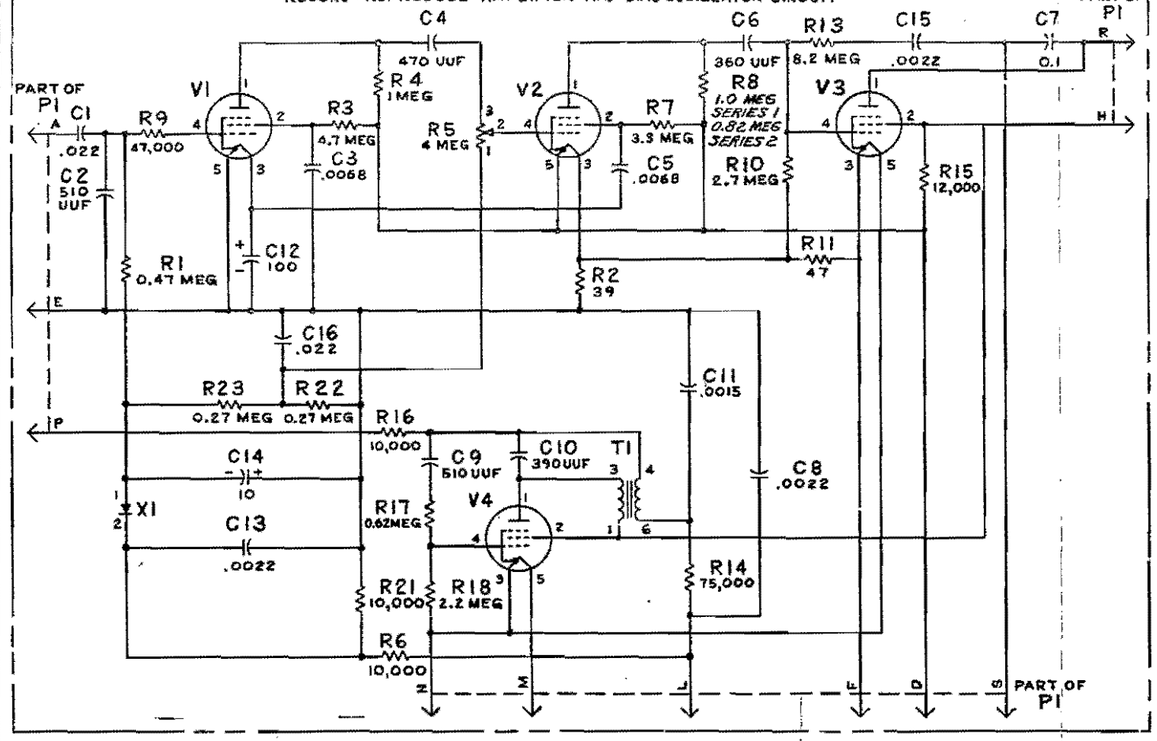
SD-95283-01-B1

BELL TELEPHONE LABORATORIES INCORPORATED

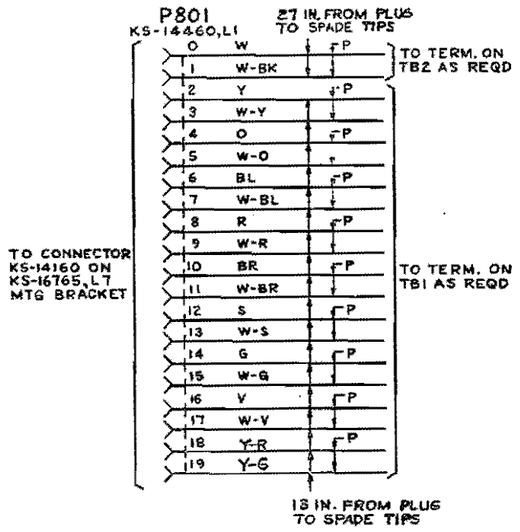
65


FS4
AMPLIFIER INPUT CIRCUIT
TEST SIGNAL CONNECTION
THROUGH TERM. ON TB4
PART OF J1
PART OF P1 OR P601
FS6
AMPLIFIER OUTPUT CIRCUIT
T2 TRANSFORMER
PART OF TB1
Ⓜ FS 5 (3A2)
OR
Ⓛ FS 9 (3A7)
FS7
MISCELLANEOUS CIRCUIT
S4 DIC NL KEY
S4 CHK L KEY
TO CONN CKT AS REQD
FS8
POWER CIRCUIT
TO RR OR BLDG GRD OR CONN CKT AS REQD
TO AMPLIFIER CKT OR CONN CKT AS REQD
X1 SEE NOTE 1
T1 TRANSFORMER
TERMINAL ON CHASSIS
TO CONN CKT AS REQD
SHEET NOTES:
1. IN KS-16766,L2 ANNOUNCEMENT SETS PRIOR TO SERIAL NO. 9002, (X1) WAS A G.E. CO. NO. 6RS5TH73 SELENIUM RECTIFIER.
ANNOUNCEMENT CIRCUIT
SD-95283-01-B2
BELL TELEPHONE LABORATORIES INCORPORATED

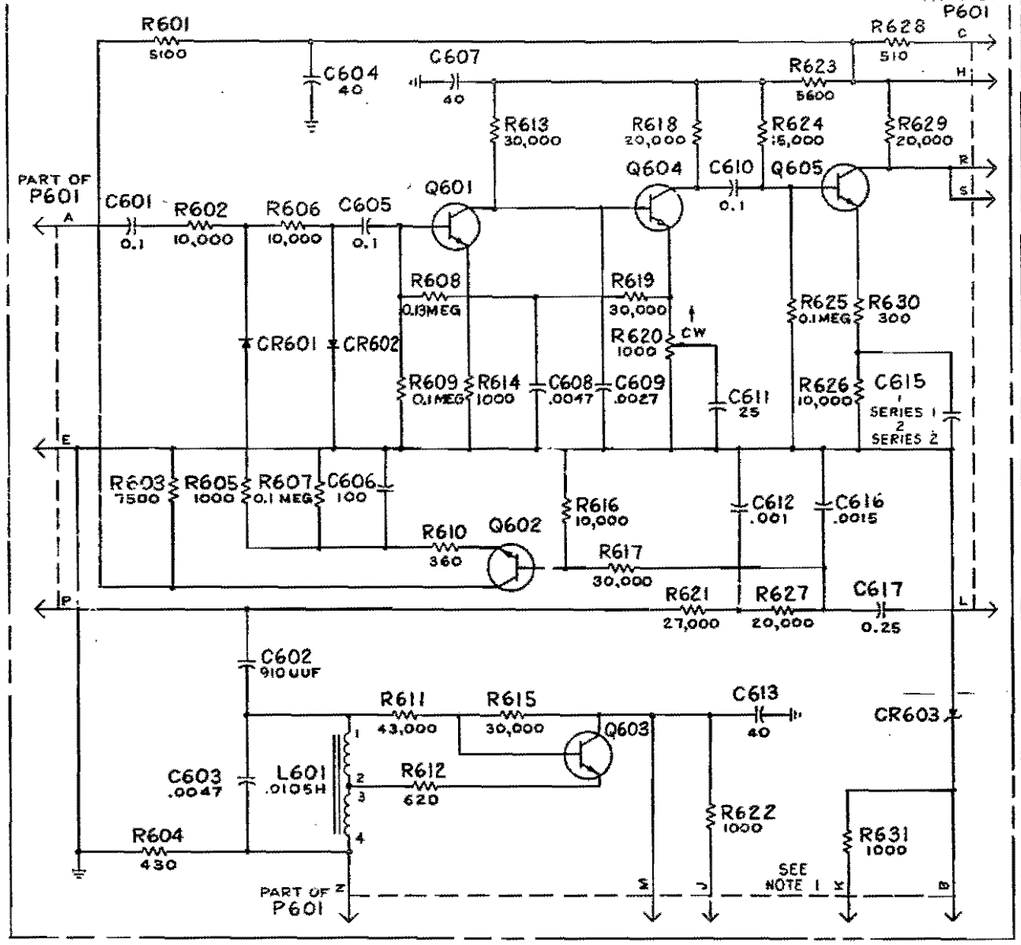
Ⓜ FS 5
RECORD-REPRODUCE AMPLIFIER AND BIAS OSCILLATOR CIRCUIT



FS 10
CONNECTING CIRCUIT



Ⓛ FS 9
AMPLIFIER



SHEET NOTES:
1. K TERMINAL NOT USED IN LIST 2 ANNOUNCEMENT SET. USED IN LIST 1 ONLY.

DRAWING ISSUE 4B

4

COMMON SYSTEMS ANNOUNCEMENT CIRCUIT

SD-95283-01-83

BELL TELEPHONE LABORATORIES INCORPORATED

2 6S

SD-95283-01-83

APP FIG. 1

RELAY

DESIG	A		B		D		E		ST		STP		DESIG
CODE	AK4				AJ5		AJ5		AK4				CODE
OPTION													OPTION
	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	
12			M	1D7	EBM	2B1	EBM	a			M	2E0	12
11			EBM	1E6	EBM	b	EBM	2G1			EBM	1F6	11
10			EBM		EBM	2G1	EBM	1B8			EBM	1D6	10
9			EMB	2E0	EBM	1F5	EBM	b			EMB	2H0	9
8			EMB		EBM	2F7	EBM	1E6			EMB	a	8
7					EBM	2G2	EBM	2F1					7
6					EBM	1B1	EBM	1D6					6
5	EMB	2F0			EBM	a	EBM	1E1	EMB	1A6			5
4	EMB	1E7			EBM	1B5	EBM	1D1	EMB	2G4			4
3	EBM	1D7			EBM	1B6	EBM	1B1	EBM	1C6			3
2	EBM	1F7			EBM	2G0	EBM	1A1	EBM	1D5			2
1	M	2E1			EBM	2B2	EBM	2F8	M	2F7			1
COIL		1E6		1C6		1C6		1D6		1B6		1F6	COIL
					a	1C7 1E6	a	1H2 2G1			a	1B6 1F7	
					b	(L) 2E5 (M) 2F5	b	1B8 1C6					

CORD & PLUG POWER

DESIG	LOC	CODE
(C) PG1	2G8	B-650422-2 OR EQUIV.
(B) PG1	2G8	B-650422-1 OR EQUIV.

FUSE

DESIG	LOC	CODE
F1	2G7	BUSSMAN, AGC 1/2 AMP

JACK

DESIG	LOC	CODE
J3	1F1	223A
J4	1G1	223A

LAMP

DESIG	LOC	CODE
D	2E1	2Y

NETWORK

DESIG	LOC	CODE
A	2E6	185A
B	2D6	185A
D	2C6	185A
E	2E6	185A
ST	2B6	185A
STP	2F6	185A

RECTIFIER

DESIG	LOC	CODE
X1	2F6	B-650051

RESISTOR

DESIG	LOC	CODE
R1	2F5	4700Ω ±10%, GB, ALLEN BRADLEY
(M) R2	2F5	WARD LEONARD, 5X1000
(M) R3	2F8	510Ω ±5%, EB — ALLEN BRADLEY
R4	1F1	(J) 4700Ω ±10%, GB; (K) 2700Ω ±10%, HB — ALLEN BRADLEY
R5	2A2	0.3 MEG ±5%, EB — ALLEN BRADLEY
R6	1G1	820 ±5%, GB — ALLEN BRADLEY
R7	1H1	1300 ±5%, EB — ALLEN BRADLEY
R8	1G3	3900Ω ±5%, EB — ALLEN BRADLEY
R9	2B9	1200Ω ±5%, EB — ALLEN BRADLEY
(M) R10	2F5	10,000Ω ±10%, EB — ALLEN BRADLEY
R11	1E5	681Ω, WARD LEONARD, 7/BY, AXIOHM
R12	1H1	47Ω ±5%, EB, ALLEN BRADLEY
(D) R13	2F6	10Ω ±5%, KS-14603, L3A

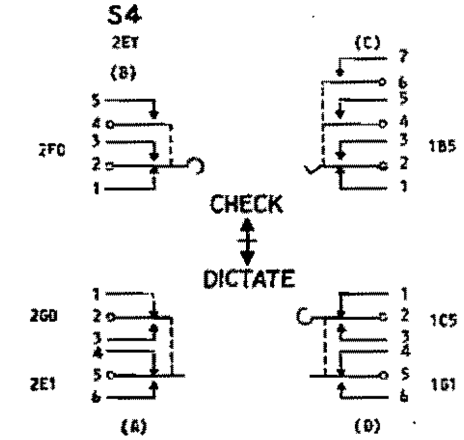
TERMINAL BOARD

DESIG	TB1				TB2		TB3		TB4	
CODE	B-190894				B-190895		CINCH MFG. CO. 1520		B-190906	
	NO.	LOC	NO.	LOC	NO.	LOC	NO.	LOC	NO.	LOC
	G	1A1	23	1B4	1	2G6	—	2A2	1	1G5
	R	1B0	24	1B4	2	2G6	—	2A2	2	1G5
	T	1A0	25	1C4	3	2H6			3	1G5
	1	1H6	26	2G4	4	2H6			4	1G7
	2	1H6	27	2F4	5	2H6			5	1G7
	3	3H6	28	2F4	6	2G6			6	1G7
	4	2H4	29	1E0	7	2G6			7	1H6
	5	2G4	30	1E0	8	2G6			8	1H6
	6	2G4	31	1D0	9	2F6			9	1H6
	7	2F0	32	1D0	10	2F6				
	8	2G0	33	1D0						
	9	1G0	34	2H0						
	10	1F0	35	-						
	11	2E0	36	2G4						
	12	2E0	37	2F4						
	13	1D4	38	2F0						
	14	2H0	39	1A1						
	15	2H0	40	2G4						
	16	1F4	41	2G0						
	17	2E0	42	1F0						
	18	1H0	43	2H4						
	19	1B0	44	2G0						
	20	1B0	45	2A9						
	21	1C0	46	2A9						
	22	1C0	47	2A9						

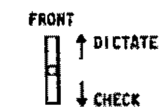
CAPACITOR

DESIG	LOC	CODE
[1] C1A	2F6	SPRAGUE, D-27842
[1] C1B	2F6	SPRAGUE, D-27842
[1] C1C	2F6	SPRAGUE, D-27842
(M) C2	2E6	CORNELL-DUBILIER, BR5015
(M) [1] C3A	1F2	SPRAGUE, D-27842
(M) [1] C3B	2F5	SPRAGUE, D-27842
(M) [1] C3C	2E7	SPRAGUE, D-27842
C7	2B2	ELMENCO, CM20D911J
C8	1G2	SPRAGUE, 91P22401S2
(M) C9	2E8	CORNELL-DUBILIER, BR5015
(D) C10	2F6	SPRAGUE, 39D757G075JLD

KEY UNIT



KEY UNIT TOP

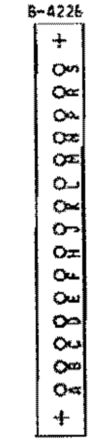


TRANSFORMER

DESIG	LOC	CODE
T1	2F7	B-191263 (POWER)
T2	2A8	529B (OUTPUT)

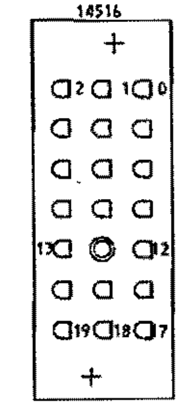
CONNECTOR

H.H. BUGGIE B-4226



DESIG	J1
CONN	SOCKET
OPTION	
NO.	LOC
S	2E7
R	2A8
P	2C3
N	2E4
M	2E5
L	2E6
K	-
J	2E5
H	2B8
F	3F9
E	2C3
D	2E7
C	2E7
B	-
A	2B3

CINCH 14516



DESIG	J2
CONN	SOCKET
OPTION	
NO.	LOC
19	-
18	-
17	-
16	1A8
15	1A8
14	1F8
13	1D8
12	1E8
11	-
10	3D8
9	-
8	1C8
7	1B8
6	1C8
5	1F8
4	-
3	1D8
2	1G8
1	1G8
0	1G8

SHEET NOTE:

1. UNLESS OTHERWISE SPECIFIED "B" NUMBERS REFERRED TO ARE B.T.L. DRAWING NUMBERS; ORDER AS FOLLOWS: "B-__(NO.)__ (PART OF KS-16765, L2)."

DRAWING ISSUE	
1	CAN EDA
2D	
3D	
4B	
5B	GDP
8B	
9B	

WORN DRAWING REPRODUCED WITHOUT CHANGE 9-15-71

9

ANNOUNCEMENT CIRCUIT

(2) SD-95283-01-C1

BELL TELEPHONE LABORATORIES INCORPORATED | 6S

SD-95283-01-C1

APP FIG. 2
(FS 3)

KS-16765,L3 RECORDER

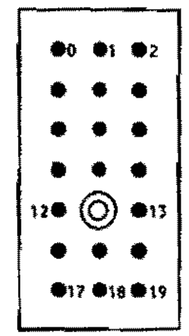
SOLENOID
B-190918

L1 — 1B8
L2 — 1C8

CAPACITOR

DESIG	LOC	CODE
C1	1A9	121P40591H5T13, SPRAGUE

CONNECTOR
CINCH, 14586



DESIG	P2
CONN	PLUG
OPTION	

NO.	LOC
19	
18	
17	
16	1A8
15	1A8
14	1F8
13	1D8
12	1E8
11	
10	1D8
9	
8	1C8
7	1B8
6	1C8
5	1F8
4	
3	1D8
2	1G8
1	1G8
0	1G8

ERASE COIL
L3
B-190922
1C8

HEAD

DESIG	LOC	CODE
H1	1G8	B-191264

MOTOR

DESIG	LOC	CODE
M1	1A9	B-650418 PART OF B-650412 DRIVE ASSEMBLY (SEE NOTE 2)

NETWORK

DESIG	LOC	CODE
L1	1B8	185A
L2	1C8	185A
L3	1D8	185A

RESISTOR

DESIG	LOC	CODE	
R1	1F8	0.1 MEG	ALLEN BRADLEY, EB, ±10%
R2	1D8	0.47 MEG	ALLEN BRADLEY, EB, ±10%
R3	1E8	0.47 MEG	ALLEN BRADLEY, EB, ±10%

SWITCH

	B-191179	B-190969-1	B-19069-2
	S1	S2	S3
	1F9	1D9	1E9

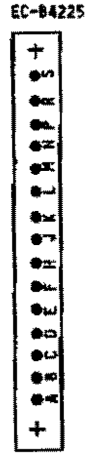
ⓂAPP FIG. 3 (MFR DISC)
(FS 5)

KS-16765,L4.
RECORD-REPRODUCE AMPLIFIER AND BIAS OSCILLATOR CIRCUIT (PRINTED WIRING BOARD ASSEMBLY)

CAPACITOR

DESIG	LOC	CODE	
C1	3A0	65P22391	SPRAGUE OR EQUIV
C2	3B0	CM150511J	ELMENCO OR EQUIV
C3	3B1	75P68291	SPRAGUE OR EQUIV
C4	3A1	CM150471J	ELMENCO OR EQUIV
C5	3B2	75P68291	SPRAGUE OR EQUIV
C6	3A3	CM150361J	ELMENCO OR EQUIV
C7	3A4	65P10491	SPRAGUE OR EQUIV
C8	3C3	85P22291	SPRAGUE OR EQUIV
C9	3C1	CM150511J	ELMENCO OR EQUIV
C10	3C2	CM150391J	ELMENCO OR EQUIV
C11	3C3	85P15291	SPRAGUE OR EQUIV
C12	3B1	150D107X0010R2	SPRAGUE OR EQUIV
C13	3D1	85P22291	SPRAGUE OR EQUIV
C14	3C1	150D106X0035R2	SPRAGUE OR EQUIV
C15	3A4	85P22291	SPRAGUE OR EQUIV
C16	3C1	65P22391	SPRAGUE OR EQUIV

CONNECTOR
H. H. BUGGIE
EC-84225



DESIG	P1
CONN	PLUG
OPTION	

NO.	LOC
S	3D4
R	3A4
P	3C0
N	3D1
M	3D2
L	3D3
K	
J	
H	3A4
F	3D3
E	3B0
D	3D4
C	
B	
A	3A0

DIODE

DESIG	LOC	CODE
X1	3C0	400

POTENTIOMETER

DESIG	LOC	CODE
R5	3A2	JA-4052-502024, ALLEN BRADLEY

RESISTOR

DESIG	LOC	CODE	TOL	
R1	3B0	0.47 MEG	±20%	ALLEN BRADLEY, EB OR EQUIV
R2	3B2	39Ω	±5%	ALLEN BRADLEY, EB OR EQUIV
R3	3A1	4.7 MEG	±5%	ALLEN BRADLEY, EB OR EQUIV
R4	3A1	1 MEG	±5%	ALLEN BRADLEY, EB OR EQUIV
R6	3D1	10,000		ALLEN BRADLEY, EB OR EQUIV
R7	3A2	3.2 MEG	±5%	ALLEN BRADLEY, EB OR EQUIV
R8	3A3	0.52 MEG	±5%	ALLEN BRADLEY, EB OR EQUIV
R9	3A0	47 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV
R10	3B3	2.7 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV
R11	3B3	47Ω	±5%	ALLEN BRADLEY, EB OR EQUIV
R13	3A3	8.2 MEG	±5%	ALLEN BRADLEY, EB OR EQUIV
R14	3D3	75,000Ω	±5%	ALLEN BRADLEY, EB OR EQUIV
R15	3B4	12,000Ω	±5%	ALLEN BRADLEY, EB OR EQUIV
R16	3C1	10,000Ω	±5%	ALLEN BRADLEY, EB OR EQUIV
R17	3C1	0.62 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV
R18	3D1	2.2 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV
R21	3D1	10,000Ω	±10%	ALLEN BRADLEY, EB OR EQUIV
R22	3C1	0.27 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV
R23	3C0	0.27 MEG	±10%	ALLEN BRADLEY, EB OR EQUIV

TRANSFORMER

DESIG	LOC	CODE
T1	3C2	669D

TUBE, ELECTRON

DESIG	LOC	CODE	
V1	3A0	CK-512AX	RAYTHEON MFG CO.
V2	3A2	CK-512AX	RAYTHEON MFG CO.
V3	3A3	CK-5672	RAYTHEON MFG CO.
V4	3C2	CK-5672	RAYTHEON MFG CO.

SHEET NOTE:

1. UNLESS OTHERWISE SPECIFIED "B" NUMBERS REFERRED TO ARE BTL DRAWING NUMBERS; ORDER AS FOLLOWS; "B- (NO.) (PART OF KS-16765,L3)".
2. ON SETS WITH SERIAL NO. 5813 AND LOWER, M1 MOTOR IS KS-15914,L1 OR L2.

COMMON SYSTEMS
ANNOUNCEMENT CIRCUIT

② SD-95283-01-C2

BELL TELEPHONE LABORATORIES
INCORPORATED

6S

4

APP FIG. 4
(FS 9)

KS-16765,L6 AMPLIFIER (PRINTED WIRING BOARD ASSEMBLY)

CAPACITOR

DESIG	LOC	CODE
C601	3B5	65P10491 SPRAGUE OR EQUIV
C602	3D6	CM20D911J ELMENCO OR EQUIV
C603	3E6	CM20E472J OR EQUIV
C604	3A6	KS-16390,L6
C605	3B6	65P10491 SPRAGUE
C606	3C6	40D114A2 OR EQUIV
C607	3A7	KS-16390,L8
C608	3C7	85P47291 SPRAGUE
C609	3C8	85P27291 OR EQUIV
C610	3B8	65P10491 SPRAGUE
C611	3C8	40D149A2 OR EQUIV
C612	3C8	85P10291 SPRAGUE OR EQUIV
C613	3E8	KS-16390,L8
C615	3C9	40D186A2 SERIES 1
		40D187A2 SERIES 2 SPRAGUE OR EQUIV
C616	3C9	85P15291
C617	3D9	542C

CONNECTOR

H.H. BUGGIE
EC-84225

DESIG	P601
CONN	PLUG
OPTION	
TERM.	LOC
S	3B9
R	3B9
P	3D5
N	3F6
M	3F8
L	3C9
K	3F9
J	3F8
H	3A9
F	
E	3C5
D	
C	3A9
B	3F9
A	3B5

DIODE

DESIG	LOC	CODE
CR601	3C6	1N1692 TEXAS INSTRUMENT OR EQUIV
CR602	3C6	
CR603	3E9	420M

INDUCTOR

DESIG	LOC	CODE
L601	3E6	15898

POTENTIOMETER

DESIG	LOC	CODE
R620	3C8	RV4NAYSB102D, TYPE J ALLEN BRADLEY OR EQUIV

RESISTOR

DESIG	LOC	CODE
R601	3A5	5100Ω
R602	3B6	10,000Ω
R603	3C5	7500Ω
R604	3E5	430Ω
R605	3C6	1000Ω
R606	3B6	10,000Ω
R607	3C6	0.1 MEG
R608	3B7	0.13 MEG ALLEN BRADLEY, EB, ±5% OR EQUIV
R609	3C7	0.1 MEG
R610	3D7	360Ω
R611	3E7	43,000Ω
R612	3E7	620Ω
R613	3A7	30,000Ω
R614	3C5	1000Ω
R615	3E7	30,0000
R616	3C7	10,000Ω

RESISTOR (CONT)

DESIG	LOC	CODE
R617	3D8	30,000Ω
R618	3A8	20,000Ω
R619	3B8	30,000Ω
R621	3D8	27,000
R622	3E8	1000Ω ALLEN BRADLEY, EB, ±5% OR EQUIV
R623	3A8	5600Ω
R624	3A8	15,000Ω
R625	3B8	0.1 MEG
R626	3C9	10,000Ω
R627	3D8	20,000Ω
R628	3A9	510Ω
R629	3A9	20,000Ω
R630	3B9	300Ω
R631	3E9	1000Ω

TRANSISTORS

DESIG	LOC	CODE
Q601	3B7	2N2712 GENERAL ELECTRIC OR EQUIV
Q602	3D7	2N697
Q603	3E7	2N697
Q604	3B8	2N2712
Q605	3B8	2N697

APP FIG. 5
(FS 10)

CORD

DESIG	LOC	CODE
CC1	3E1	KS-16765,L8

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DRAWING ISSUE 4B

4

COMMON SYSTEMS ANNOUNCEMENT CIRCUIT (2) SD-95283-01-C3

BELL TELEPHONE LABORATORIES INCORPORATED 6S

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER
BATTERY SYMBOL			VOLTAGE RANGE

102.

FEATURE OR OPTION		PROVIDE APP FIG.	PROVIDE APP OR WRG	PROVIDE QUANTITY
KS-16765,L2 ANNOUNCEMENT SET		1,2,3	M	1 PER SET
		1,2,4	L	1 PER SET
KS-16765,L3 RECORDER		2		AS REQD
KS-16765,L4 AMPLIFIER		3	M	AS REQD
KS-16765,L6 AMPLIFIER		4	L	AS REQD
KS-16765,L8 CORD		5		SEE NOTE 106
RECORD-REPRODUCE AMPLIFIER	INTERNAL		*Z	
	EXTERNAL		Y	REMOVE Z
NOMINAL OUTPUT	600Ω		*X	
	20Ω		W	REMOVE X
MUTING OF OUTPUT DURING RECORDING			V	
INTERNAL 48V DC SUPPLY			S	
LEAD MU (MU3) GROUNDED			R	
LEADS T AND R MUTED			N	
USE WITH KS-16754 AMPLIFIER			N,Y N,Z	
48V POWER SUPPLY TO CONNECTING CKT			P	
AUTOMATIC CALL DISTRIBUTING SYSTEM NO. 2A AND 2B			H	
RECEIVER OFF HOOK TONE CONNECTING CIRCUIT FOR 5-X S			F	REMOVE R REMOVE P REMOVE G SEE NOTE 109
STD WIRING WHEN OPTION F IS NOT USED			G	SEE NOTE 104
REDUCE 60 CYCLE HUM			D	SEE NOTE 104

* SEE NOTE 105

103.

NETWORK VALUES		
NETWORK NO.	RESISTANCE IN OHMS	CAPACITANCE IN UF
1	470	0.11

CIRCUIT NOTES: (CONT)

104.

RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES							
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT STD	A&M	MD	
4B			107	L		M	
5B			108	K		J	
6A	F		102	G			
9B	B OR C	C	102	B		C	
9B	D OR E	E	102	D		E	

105. OPTIONS Z AND X ARE FURNISHED WITH SETS AS MANUFACTURED AND SHOULD BE LEFT IN PLACE UNLESS OTHERWISE SPECIFIED.

106. KS-16765,L8 CORD ORDERED AS REQUIRED.

107. M WIRING IN SETS WITH SERIAL NUMBERS LOWER THAN 5559 FOR USE WITH EITHER LIST 4 OR LIST 6 AMPLIFIERS. L WIRING FOR USE WITH LIST 6 AMPLIFIERS ONLY.

108. K WIRING INTRODUCED IN SETS BEGINNING WITH SERIAL NO. 7950.

109. OPTION F PROVIDES +48V DC POWER TO CONNECTING CIRCUIT ON LEAD B2 AT PUNCHING 31, T81, WHEN THE BAIL SOLENOID OPERATES.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYMBOL + (PLUS) OR - (MINUS) ARE IN VOLTS.

DRAWING ISSUE
4B WF WEB ALT
5B CAP PLY
6A WF F/B FC
7B
9B

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9

ANNOUNCEMENT CIRCUIT (2) SD-95283-01-DI

BELL TELEPHONE LABORATORIES INCORPORATED 6S

SG1
ANNOUNCEMENT RECORD

S4 KEY (DICTATE) OR LEAD D GROUNDED
D
ST
MOTOR ENERGIZED DRUM ROTATES — L3 ERASE COIL
3 SEC (MAX)
S3 SWITCH
A
S3 SWITCH
2 SEC (APPROX)
S2 SWITCH
B
LEAD CT GROUNDED — S2 SWITCH
1 SEC (APPROX)
S3 SWITCH
A
GRD REMOVED FROM LEAD CT — S3 SWITCH
E
LEADS MU3 AND MU4 SHORTED — SHORT REMOVED FROM LEADS MU1, MU2 AND MU3 — LEADS DL1 AND DL2 GROUNDED — L3 ERASE COIL — L1 SOLENOID — L2 SOLENOID
RECORDING STARTS
2 SEC (APPROX)
S2 SWITCH
LEAD VAE GROUNDED
S2 SWITCH
GRD REMOVED FROM LEAD VAE
RECORDING CONTINUES

NORMAL SEQUENCE

DICTATING ENDS
S4 KEY OR GRD REMOVED FROM LEAD D
D
L2 SOLENOID — ST — GRD REMOVED FROM LEADS DL1 AND DL2
E — B — L1 SOLENOID — MOTOR DE-ENERGIZED DRUM STOPS
SHORT REMOVED FROM LEADS MU3 AND MU4 — LEADS MU1, MU2, AND MU3 SHORTED
END OF RECORD CYCLE

STOP SEQUENCE

RECORDING CONTINUES
S1 SWITCH
STP
LEADS CO AND L1N GROUNDED — E — B — ST MOTOR DE-ENERGIZED DRUM STOPS — L1 SOLENOID — L2 SOLENOID — S1 SWITCH
SHORT REMOVED FROM LEADS MU3 AND MU4 — LEADS MU1, MU2, AND MU3 SHORTED — GRD REMOVED FROM LEADS DL1 AND DL2
S4 KEY OR GRD REMOVED FROM LEAD D
D
STP
GRD REMOVED FROM LEADS CO AND L1N
END OF RECORD CYCLE

DRAWING ISSUE
1 CAR EDG
4B WHM WSM DLV

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4

COMMON SYSTEMS
ANNOUNCEMENT CIRCUIT
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BELL TELEPHONE LABORATORIES INCORPORATED
6S

SC2
ANNOUNCEMENT REPRODUCE

S4 KEY (CHECK) OR LEAD ST(ANN) GROUNDED

ST

MOTOR ENERGIZED DRUM ROTATES — A

3 SEC (MAX)

S2 SWITCH

(Z)

B

LEAD CT GROUNDED — S2 SWITCH

1 SEC (APPROX)

S3 SWITCH

A

GRD REMOVED FROM LEAD CT — S3 SWITCH

F

LEADS AL1 AND AL2 GROUNDED — SHORT REMOVED FROM LEADS HU1, HU2 AND HU3 — LEADS HU3 AND HU4 SHORTED — L1 SOLENOID

ANNOUNCEMENT STARTS

2 SEC (APPROX)

S2 SWITCH

LEAD VAE GROUNDED

S2 SWITCH

GRD REMOVED FROM LEAD VAE

ANNOUNCEMENT CONTINUES

[A]

S4 KEY OR GRD REMOVED FROM LEAD ST(ANN)

ST

B — E — L1 SOLENOID — MOTOR DE-ENERGIZED DRUM STOPS

SHORT REMOVED FROM LEADS HU3 AND HU4 — LEADS HU1, HU2, AND HU3 SHORTED — GRD REMOVED FROM LEADS AL1 AND AL2

END OF ANNOUNCEMENT REPRODUCE CYCLE

ANNOUNCEMENT ENDS

S1 SWITCH

STP

B — LEADS CO AND L1H GROUNDED — L1 SOLENOID

E

A — GRD REMOVED FROM LEADS AL1 AND AL2 — LEADS HU1, HU2, AND HU3 SHORTED — SHORT REMOVED FROM LEADS HU3 AND HU4

STP

GRD REMOVED FROM LEADS CO AND L1H

3 SEC (MAX)

S2 SWITCH

(Y)

SEQUENCE IS REPEATED BETWEEN (Z) AND (Y) AS LONG AS S4 KEY IS IN CHECK POSITION OR GRD REMAINS ON LEAD ST (ANN)

TO [A]. 2H25

DRAWING ISSUE

1 — ICAN 6 DG

4B — WEN DLF

4

COMMON SYSTEMS

ANNOUNCEMENT CIRCUIT

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CIRCUIT REQUIREMENTS

ANNOUNCEMENT CIRCUIT

APPARATUS DESIG	APPARATUS CODE	APPARATUS OPTION	APPARATUS FIG.		MECH REQT BSP FIG	MECH REQT CONT PRESS	MECH REQT ARM. TRVL	CIRCUIT PREPARATION BLOCK OR INSULATE	TEST CLIP DATA CONN BAT.	TEST CLIP DATA CONN GRD	TEST SET PREP	SEE TEST NOTE	DIRECT CURRENT FLOW REQT TEST WDG	TEST FOR	AFTER SOAK MA.	TEST MA.	READJ MA.	REMARKS
MAGNETS																		
SOLENOIDS																		
L1, L2	SEE APP FIG. 2		2									1,2, 3,4,5						
RELAYS																		
A	1/2AK4		1		202			(ST)NO, (E)D	2L(A)	1L(A)	B/G	1	B	0		11.9	11.3	MTD WITH (B)
B	1/2AK4		1		202			(ST)NO, (E)D, (STP)O	2U(B)	1U(B)	B/G	1	T	0		11.9	11.3	MTD WITH (A)
D	AJ5		1		220			(ST)NO	L(D)	U(D)	B/G	1,6		0		13.3	12.6	
E	AJ5		1		220			(ST)NO, (D)D, (STP)O, 4B(A)	L(E)	U(E)	B/G	1		0		13.3	12.6	
ST	1/2AK4		1		202			(D)O, (STP)O	2L(ST)	1L(ST)	B/G	1,6	B	0		11.9	11.3	MTD WITH (STP)
STP	1/2AK4		1		202			9B(D)	2U(STP)	1U(STP)	B/G	1	T	0		11.9	11.3	MTD WITH (ST)

TEST NOTES:

1. DISCONNECT AC POWER FROM ANNOUNCEMENT SET BEFORE PERFORMING ANY TESTS.
2. REFER TO BSP FOR MECHANICAL REQUIREMENTS AND ADJUSTING PROCEDURES.
3. BEFORE ENERGIZING SOLENOID (L1), ROTATE DRUM MANUALLY BY TURNING DRUM SHAFT PULLEY (AVOID TOUCHING MAGNETIC RECORDING BAND) CCW AS VIEWED FROM MOTOR SIDE UNTIL PIN ON DRUM OPERATES SWITCH (S3).
4. IF OPERATE TESTS ARE MADE ON SOLENOID (L2) IT WILL BE NECESSARY TO RECORD A NEW ANNOUNCEMENT AFTER COMPLETING TESTS.
5. MIN. OPR VOLTAGE 44V DC ACROSS WINDING.
6. FOR SETS WITH "S" OPTION (STRAP BETWEEN 26 & 36 OF TB1) REMOVE THIS STRAP WHEN TESTING.

DRAWING ISSUE		
1	CAH	A.J.G.
2D		J.H.M.
4B		

ANNOUNCEMENT CIRCUIT

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BELL TELEPHONE LABORATORIES INCORPORATED

SD-95283-01-F1

DRAWING ISSUE		
1	CAH	A.J.G.
2D		J.H.M.
4B		

4

COMMON SYSTEMS

ANNOUNCEMENT CIRCUIT

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6S