

SHEET INDEX

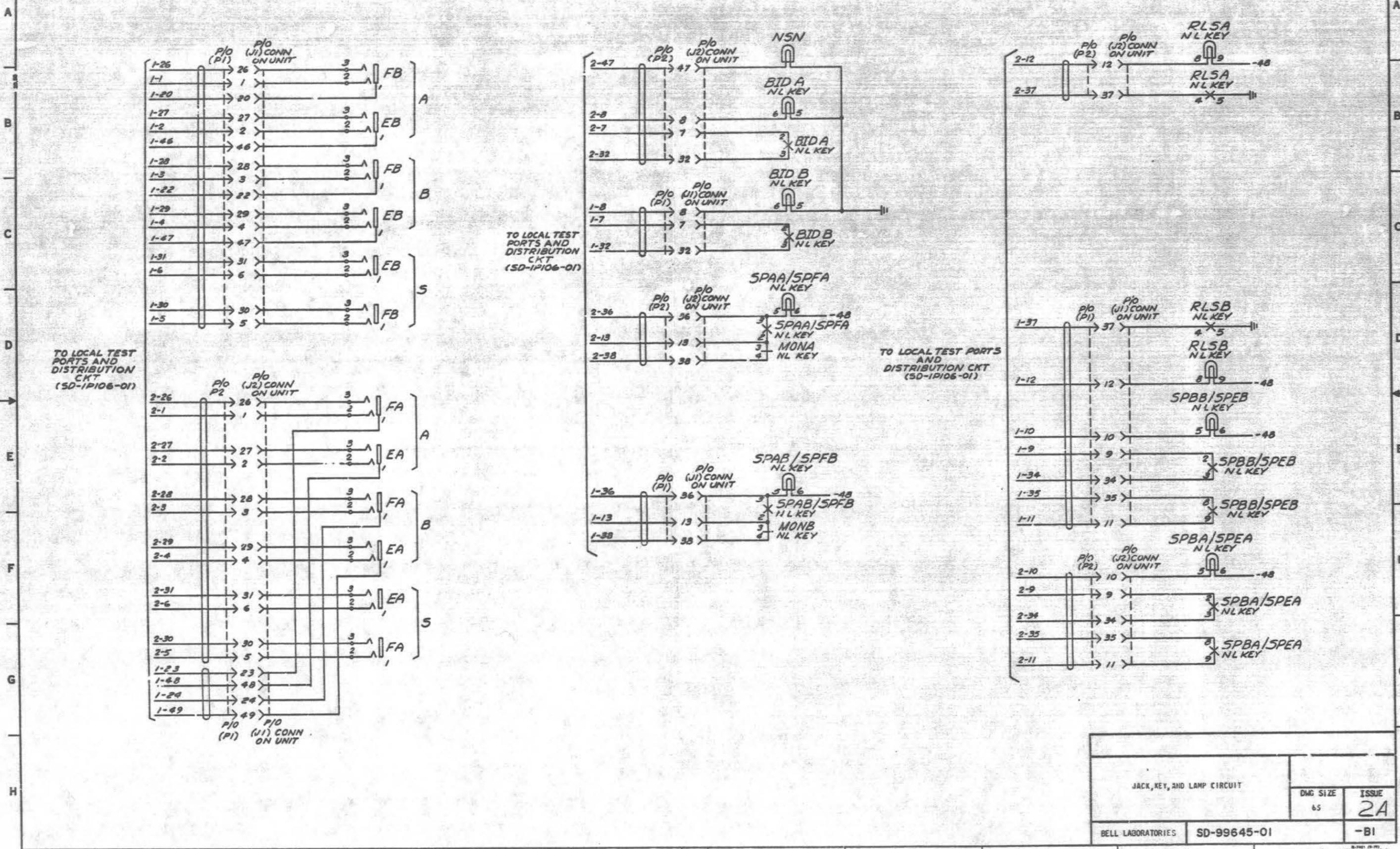
DWG. NO.	CD. NO.	DATE ISSUED	ISSUE NO.
/	/	7-18-70	2A
2A	PA	7-19-70	2A

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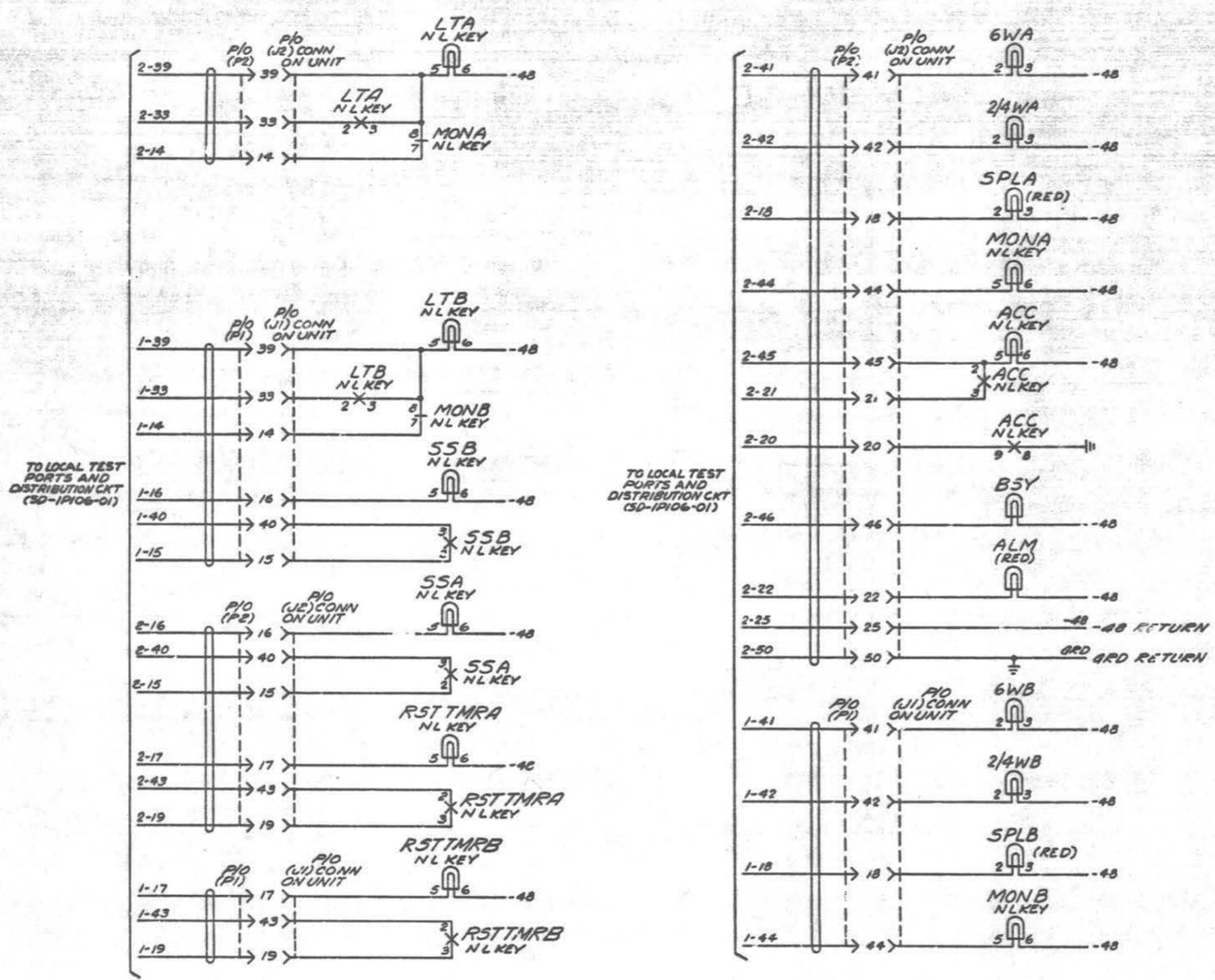


**PART OF FS I**  
**JACK, KEY, AND LAMP CIRCUIT**  
 (SEE NOTE 201)



JACK, KEY, AND LAMP CIRCUIT		DWG SIZE	ISSUE
		65	2A
BELL LABORATORIES	SD-99645-01	-B1	

PART OF FS I  
JACK, KEY, AND LAMP CIRCUIT  
(SEE NOTE 201)

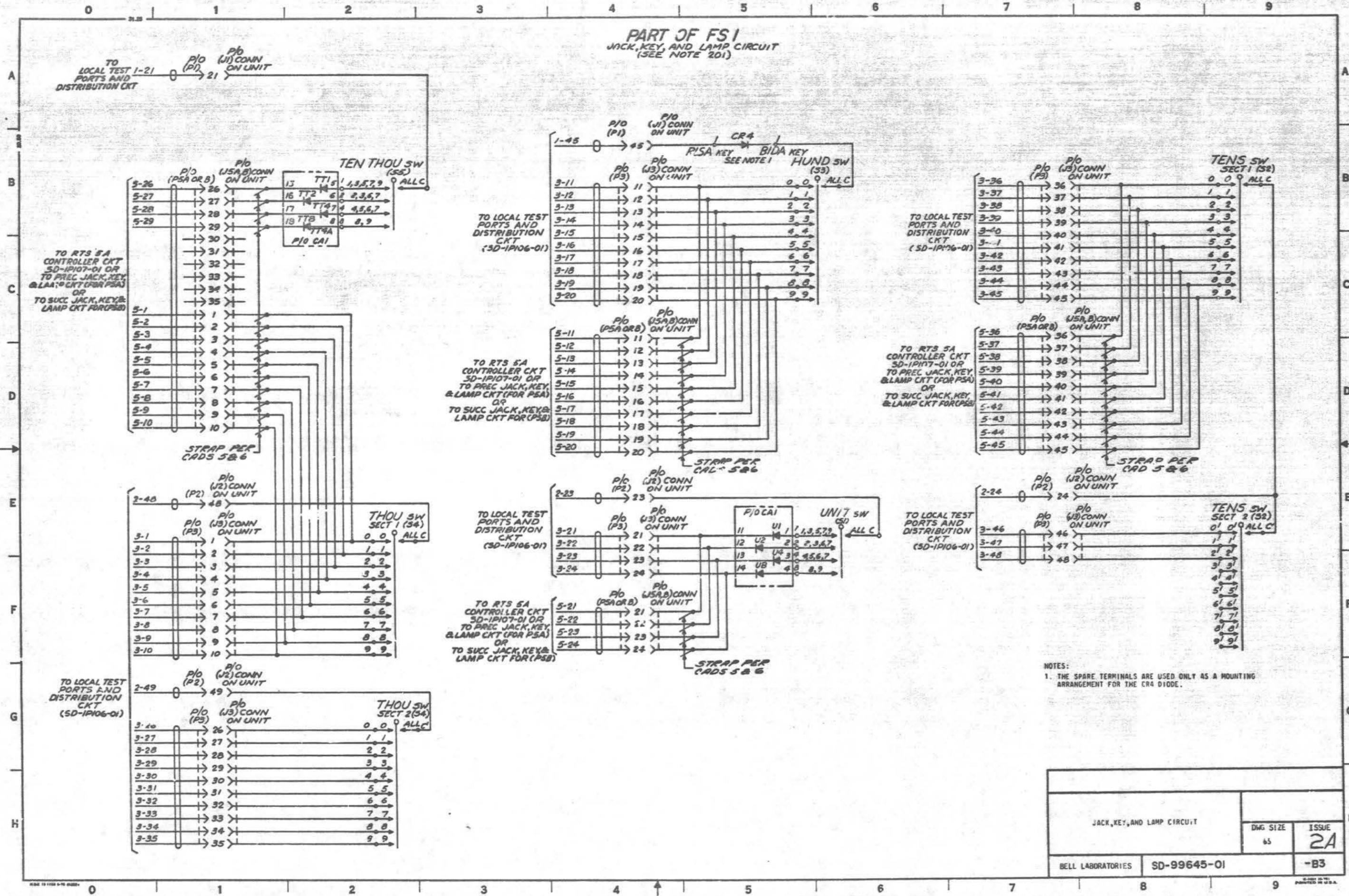


TO LOCAL TEST PORTS AND DISTRIBUTION CKT (SD-1PI06-01)

TO LOCAL TEST PORTS AND DISTRIBUTION CKT (SD-1PI06-01)

JACK, KEY, AND LAMP CIRCUIT		DWG SIZE	ISSUE
		65	2A
BELL LABORATORIES	SD-99645-01	-B2	

**PART OF FS I**  
**JACK, KEY, AND LAMP CIRCUIT**  
 (SEE NOTE 201)

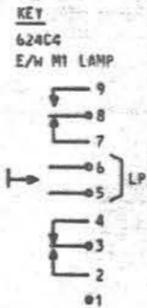
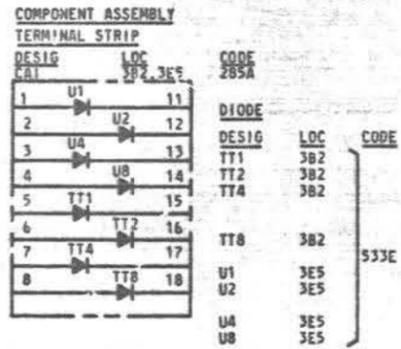


NOTES:  
 1. THE SPARE TERMINALS ARE USED ONLY AS A MOUNTING ARRANGEMENT FOR THE CR4 DIODE.

JACK, KEY, AND LAMP CIRCUIT		DWG SIZE	ISSUE
		65	2A
BELL LABORATORIES		SD-99645-01	-B3

APP FIG. 1

CONNECTOR		
DESIG	LOC	CODE
A EA	1E2	238AM (JACK)
A EB	1B2	238AM (JACK)
A FA	1E2	238AM (JACK)
A FB	1B2	238AM (JACK)
B EA	1F2	238AM (JACK)
B EB	1C2	238AM (JACK)
B FA	1E2	238AM (JACK)
B FB	1B2	238AM (JACK)
S EA	1F2	238AM (JACK)
S EB	1C2	238AM (JACK)
S FA	1G2	238AM (JACK)
S FB	1D2	238AM (JACK)

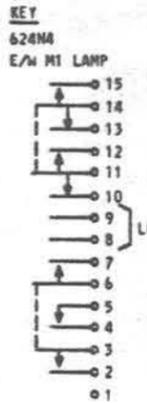


ACC	BIDA	BIDB	LTA	LTB	MONA	MONB	RST TMRA	RST TMRB	SPAA/ SPFA	SPAB/ SPFB	SPBA/ SPEA	SPBB/ SPFB	SSA	SSB
2D5			2B2	2C2							1G8	1F8		
2C5	1B5	1C5	2A2	2C2	2C5	2G5	2F2	2G2	1C5	1E5	1F8	1E8	2E2	2D2
2C5	1B5	1C5	2B2	2C2	1D5	1F5	2F2	2G2	1D5	1E5	1F8	1E8	2E2	2E2

CONNECTOR

A25C CONNECTOR CABLE INCLUDING:

DESIG	J1	J2	J3	J5A	J5B
CODE	KS-16786, L19	KS-16786, L19	KS-16786, L14	KS-16786, L14	KS-16786, L19
OPTION	CONN	CONN	CONN	CONN	CONN
TERPH.	LOC	LOC	LOC	LOC	LOC
50		2A4			
49		3G1			
48		3E1	3E7		
47		1A4	3E7		
46		2A4	3E7		
45		2A4		3C8	3C8
44	2F4	2A4		3C8	3C8
43	2G1	2E1		3C8	3C8
42	2F4	2A4		3C8	3C8
41	2F4	2A4		3C8	3C8
40	2C1	2E1		3C8	3C8
39	2C1	2A1		3C8	3C8
38	1E4	1D4	3B7	3C8	3C8
37	1D8	1A8	3B7	3C8	3C8
36	1E4	1D4	3B7	3C8	3C8
35	1D8	1F8	3G1		
34	1D8	1F8	3G1		
33	2C1	2A1	3G1		
32	1E4	1A4	3G1		
31	1A1	1D1	3G1		
30	1A1	1D1	3G1	3B1	3B1
29	1A1	1D1	3G1	3B1	3B1
28	1A1	1D1	3G1	3B1	3B1
27	1A1	1D1	3G1	3B1	3B1
26	1A1	1D1	3G1	3B1	3B1
25		2A4			
24		3E8	3E4	3F4	3F4
23		3E4	3E4	3F4	3F4
22		2A4	3E4	3F4	3F4
21	3B1	2A4	3E4	3F4	3F4
20		2A4	3B4	3B4	3B4
19	2G1	2E1	3B4	3B4	3B4
18	2F4	2A4	3B4	3B4	3B4
17	2G1	2E1	3B4	3B4	3B4
16	2C1	2E1	3B4	3B4	3B4
15	2C1	2E1	3B4	3B4	3B4
14	2C1	2A1	3B4	3B4	3B4
13	1E4	1D4	3B4	3B4	3B4
12	1D8	1A8	3B4	3B4	3B4
11	1D8	1F8	3B4	3B4	3B4
10	1D8	1F8	3E1	3B1	3B1
9	1D8	1F8	3E1	3B1	3B1
8	1C4	1A4	3E1	3B1	3B1
7	1C4	1A4	3E1	3B1	3B1
6	1A1	1D1	3E1	3B1	3B1
5	1A1	1D1	3E1	3B1	3B1
4	1A1	1D1	3E1	3B1	3B1
3	1A1	1D1	3E1	3B1	3B1
2	1A1	1D1	3E1	3B1	3B1
1	1A1	1D1	3E1	3B1	3B1



RLSA	RLSB
1A8	1D8
1B8	1D8
3B5 (SEE NOTE 1)	

LAMP

DESIG	LOC	CODE
2/4WA	2H5	M1
2/4WB	2F5	M1
6WA	2A5	M1
6WB	2F5	M1
ALM	2E5	M1
BSY	2D5	M1
NSH	1A5	A1
SPLA	2B5	M1(RED)
SPLB	2F5	M1(RED)

SWITCH

DESIG	HUND(S3)	TENS(S2)	TEN THOU(S5)	THOU(S4)	UNIT(S1)
CODE	KS-19889 L1	KS-19889 L5	KS-19889 L4	KS-19889 L5	KS-19889 L4
OPTION					
SECT.	1		2		
	TERM.	LOC	TERM.	LOC	TERM.
	0-9,C	3B6	0-9,C	3B9	0-9,C', 3E9
					1,2,4, 3B2
					0-9,C' 3E2
					0-9,C' 3G2
					1,2,4, 3E5

NOTES:  
1. TERMINAL 1 OF BOTH THE RLSA & BIDA KEYS IS USED TO PROVIDE A MOUNTING ARRANGEMENT FOR THE CR4 DIODE.

DIODE

DESIG	LOC	CODE
CR4	3B5	533E

JACK  
SEE CONNECTOR

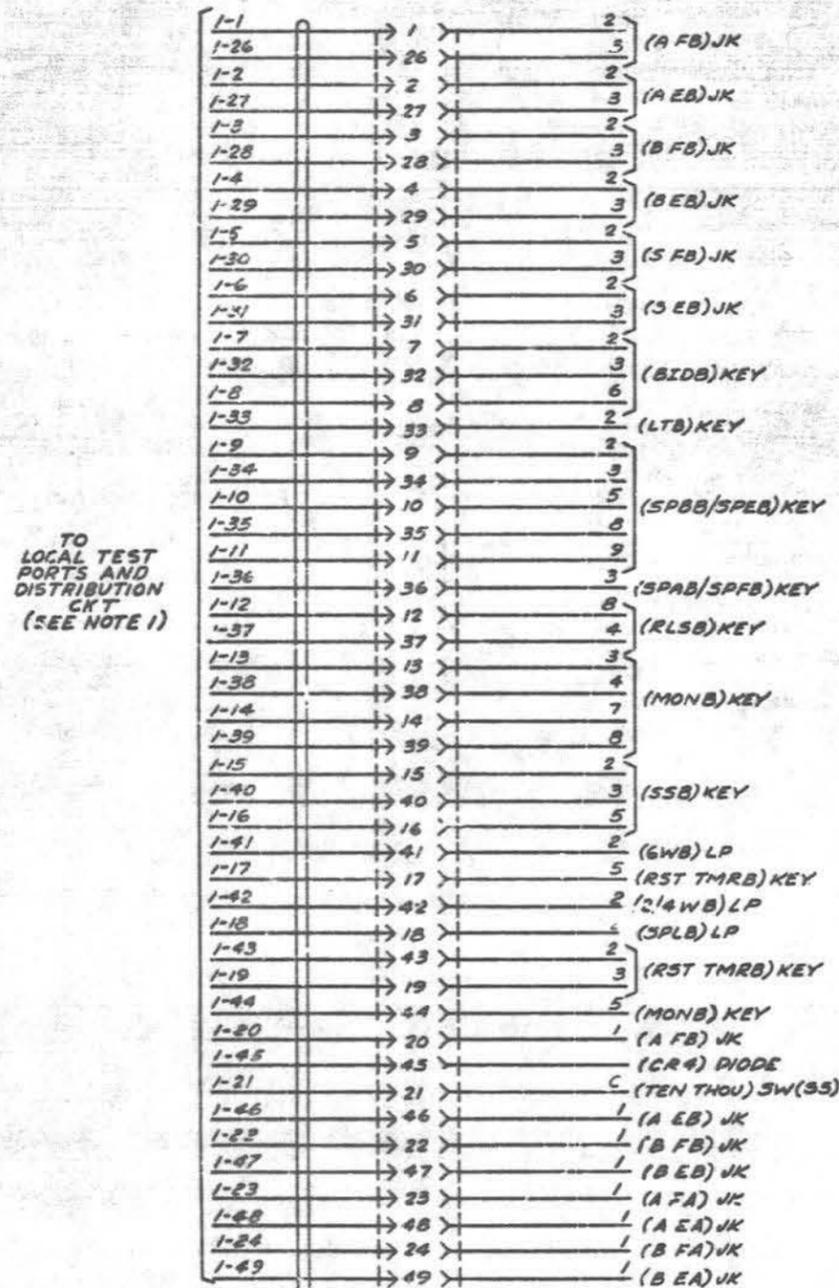
JACK, KEY, AND LAMP CIRCUIT		DWG SIZE	ISSUE
		65	2A
BELL LABORATORIES		SD-99645-01	-CI



**CAD 1**  
(FOR APP FIG. 1)

**CAD 2**  
(FOR APP FIG. 1)

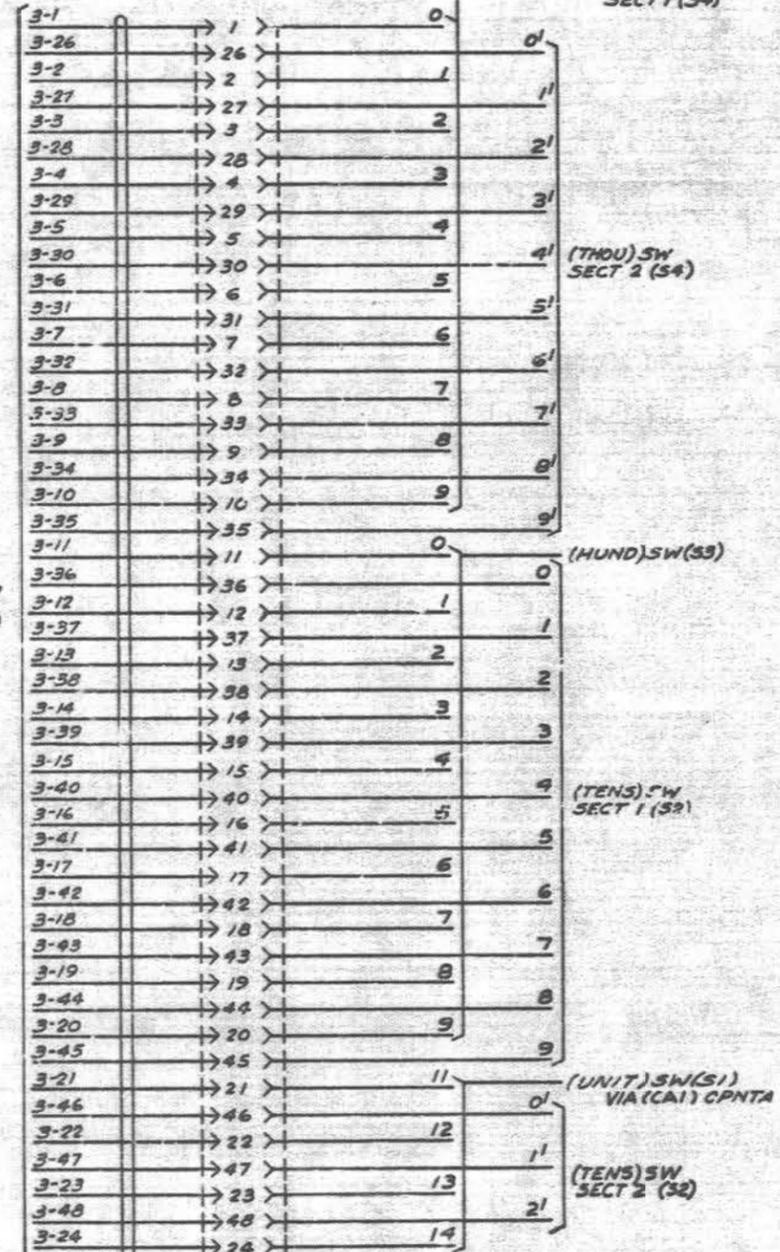
**CAD 3**  
(FOR APP FIG. 1)



TO LOCAL TEST PORTS AND DISTRIBUTION CKT (SEE NOTE 1)



TO LOCAL TEST PORTS AND DISTRIBUTION CKT (SEE NOTE 1)



(P1) PLUG ON CONN CABLE (SEE NOTE 201)  
(J1) CONN ON UNIT J99359 AP

(P2) PLUG ON CONN CABLE (SEE NOTE 201)  
(J2) CONN ON UNIT J99359 AP

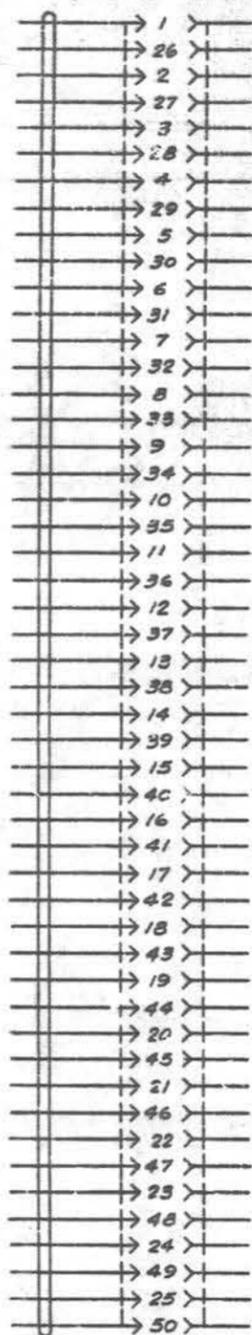
(P3) PLUG ON CONN CABLE (SEE NOTE 201)  
(J3) CONN ON UNIT J99359 AP

NOTES:

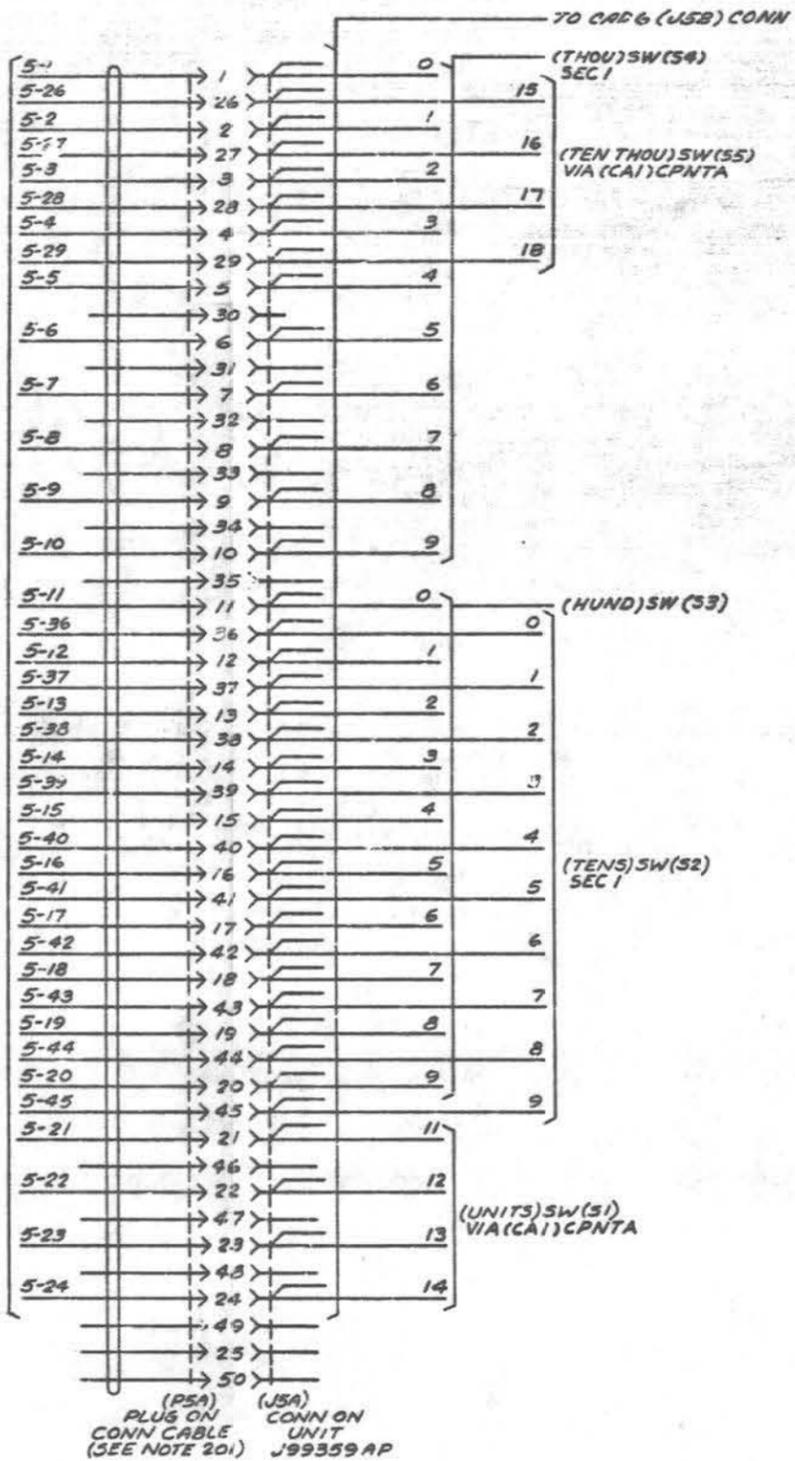
CONNECTIONS TO (J3) ARE NOT REQUIRED WHEN JACK KEY AND LAMP CKT IS CONTROLLED BY RELAY CIRCUITRY (JIPO33AK) OF SD-1P106-01 FOR SNAS 5B APPLICATION.

JACK, KEY, AND LAMP CIRCUIT		DWG SIZE 65	ISSUE 2A
BELL LABORATORIES	SD-99645-01	-G1	

**CAD 4**  
(FOR APP FIG. 1)



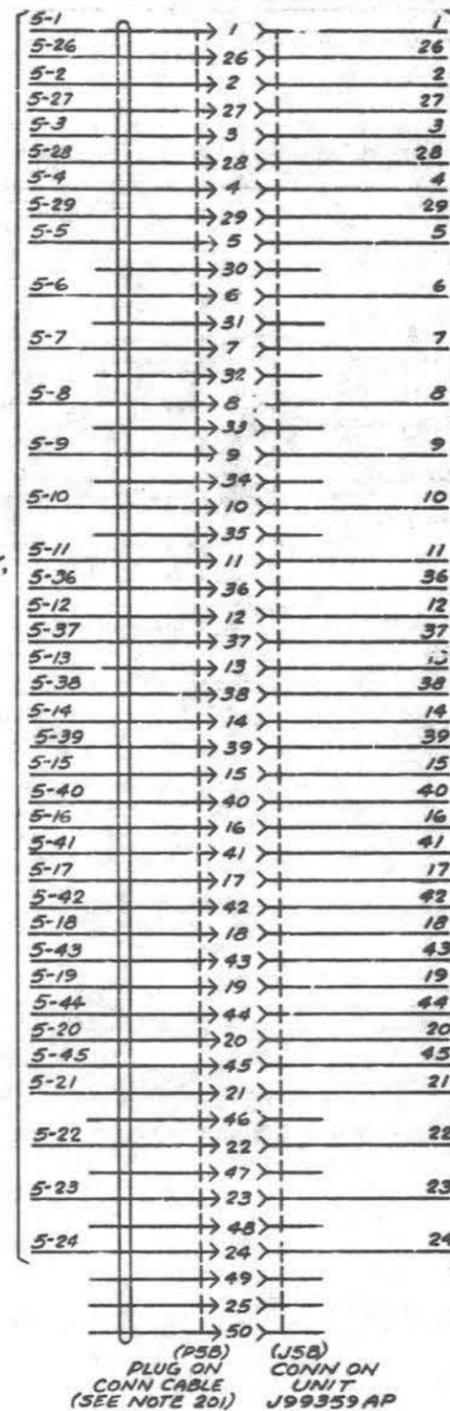
**CAD 5**  
(FOR APP FIG. 1)



TO  
RTS SA  
CONTROLLER CKT OR  
TO PREC JACK, KEY, & LAMP  
CIRCUIT FOR (PSA)  
OR  
TO SUCC JACK, KEY, &  
LAMP CKT FOR (PSB)

TO  
SUCC JACK,  
KEY, & LAMP  
CKT

**CAD 6**  
(FOR APP FIG. 1)



JACK, KEY, AND LAMP CIRCUIT		DMG SIZE	ISSUE
		65	2A
BELL LABORATORIES	SD-99645-01	-G2	