SD-99725-01

SHEET INDEX

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CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER
	1-1/3	-48V TALK	CKT

102.

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY
KS-19221, L1 AMPLIFIER	1		1 PER CKT

103.

NETWORK VALUES		
NETWORK	RESISTANCE IN OHMS	CAPACITANCE IN UF

104.

RECORD OF FIGURES, WIRING & APPARATUS CHANGES							
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT			
				STD	A&M	MD	

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS,
CAPACITANCE VALUES ARE IN MICROFARADS,
VALUES PRECEDED BY THE SYMBOL + (PLUS) OR
- (MINUS) ARE IN VOLTS.

302. THE VOLTAGES SHOWN REPRESENT TYPICAL DC VALUES FOR A QUIESCENT CONDITION WITH AVERAGE TRANSISTORS AND NOMINAL SUPPLY VOLTAGE. THE VOLTAGES ARE MEASURED FROM POINTS SHOWN TO TERMINAL S USING A VOLTMETER HAVING A RESISTANCE OF 10MEGOHMS (MINIMUM).

303. Q3 AND Q4 ARE SELECTED RCA2N1484 TRANSISTORS HAVING MINIMUM BETA OF 50 AT D-C COLLECTOR CURRENT OF 150 MILLI-AMPERES.

SUPPORTING INFORMATION

CATEGORY	NO.

OPTION INDEX

APP OR WRG	LOCATION

DWG ISSUE	EE OR CD ISSUE	DATE ISSUED	DESIGN	APPD
1	1	12-3-62	[illegible]	[illegible]
2D	1 APP 1D	5-18-64	CMC -	[illegible]
3D	1 APP 2D	6-12-64	CMC -	[illegible]
4A	1 APP 3B	8-13-64	KAP -	[illegible]
5D	1 APP 4D	6-18-65	JG -	CH [illegible]
6D	1 APP 5D	3-3-67	JE -	EVK CEP
7D	1 APP 6D	1-17-68	HBE LBF	[illegible]
8D	1 APP 7D	1-26-79	LMT	WF AB JTR

NOTICE
NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT

ISSUE 8D

SD-99725-01 2N06

COMMON SYSTEMS

KS-19221, L1 AMPLIFIER CIRCUIT

(2)

A&M ONLY

SD-99725-01-1
3 SHEETS

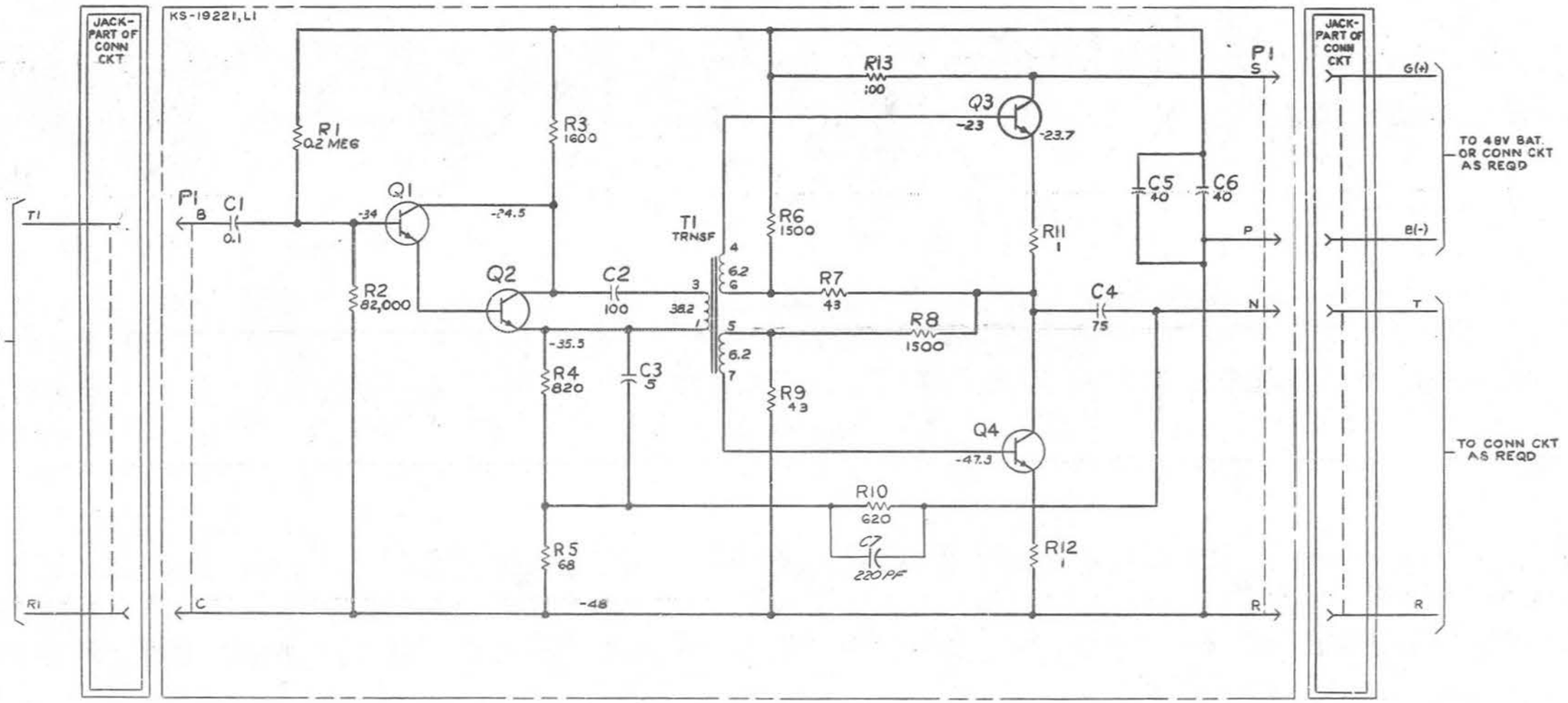
BELL TELEPHONE LABORATORIES INCORPORATED

SIZE 6S

PRINTED IN U.S.A.

SD-99725-01-1

FS 1
AMPLIFIER CIRCUIT



DRAWING ISSUE	
1	ORIG. ISSUE
2D	CHG. DLY
3D	CHG. DLY
4A	CHG. DLY
5D	CHG. DLY
6D	CHG. DLY
7D	CHG. DLY

8D

COMMON SYSTEMS KS-19221, L1 AMPLIFIER CIRCUIT (2)	SD-99725-01-2
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S

99725-01-2

APP FIG. I

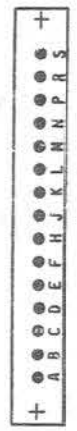
(FS1)
KS-19221,L1 AMPLIFIER
(PRINTED WIRING BOARD ASSEMBLY)

CAPACITOR

DESIG	LOC	CODE
C1	2D2	0.1UF, 535AB
C2	2D4	100UF, KS-16390,L6
C3	2D4	5UF, 601A
C4	2D6	75UF, KS-16390,L12
C5	2C7	40UF, KS-16390,L8
C6	2C7	40UF, KS-16390,L8
C7	2E5	220 PF, SPRAGUE 40C204A

CONNECTOR

KS-16345,L1



DESIG	P1
CONN	PLUG
OPTION	
NO.	LOC
S	2C7
R	2F7
P	2D7
N	2D7
M	-
L	-
K	-
J	-
H	-
F	-
E	-
D	-
C	2F2
B	2D2
A	2C2

RESISTOR

DESIG	LOC	CODE
R1	2C2	.2 MEG, KS-19150,L1
R2	2D2	82,000Ω,KS-19150,L1
R3	2C3	1600Ω, KS-19151,L1
R4	2E3	820Ω, KS-19150,L1
R5	2F3	68Ω, KS-19150,L1
R6	2D4	1500Ω, KS-19151,L1
R7	2D5	43Ω, KS-19150,L1
R8	2D5	1500Ω, KS-19151,L1
R9	2E4	43Ω, KS-19150,L1
R10	2E5	620Ω, KS-19150,L1
R11	2D6	1Ω, 221A
R12	2F6	1Ω, 221A
R13	2C5	100Ω, KS-19150,L1

TRANSFORMER

DESIG	LOC	CODE
T1	2D4	2578G

TRANSISTOR

DESIG	LOC	CODE
Q1	2C2	29A
Q2	2D3	29A
Q3	2C5	2N1484, RCA (SEE NOTE 303) OR 40484, RCA OR 2N1486, RCA
Q4	2E5	2N1484, RCA (SEE NOTE 303) OR 40484, RCA OR 2N1486, RCA

DRAWING ISSUE: 1, 2D, 3D, 4A, 5D, 6D, 7D, 8D

COMMON SYSTEMS
KS-19221,L1 AMPLIFIER CIRCUIT
(2) SD-99725-01-3
BELL TELEPHONE LABORATORIES INCORPORATED
6S